

6.894 : Lightweight Formal Methods

Lecture 11 : Temporal Logic I

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Why Model checking?

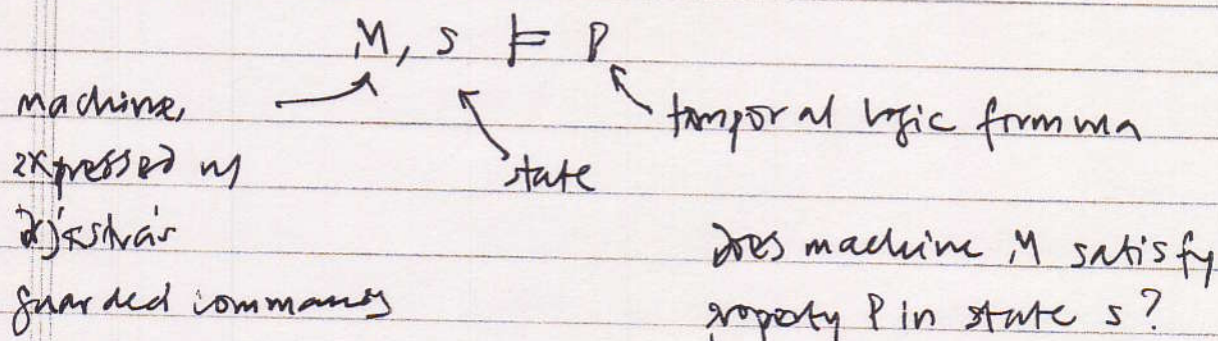
We've seen how Alloy gives limited ability to reason about temporal behaviour: only on bounded traces, typically short.

Model checking allows analysis of all traces of a finite state machine. Modern model checkers can scale to machines of $> 10^{60}$ states, depending on how uniform their structure is.

Instead of arbitrary temporal properties expressed in FOL, they use special temporal logics. For example, LTL, which has 'always' and ~~can~~ 'eventually' operators - less expressive than FOL but more tractable.

Aside from arb. trace length, other major difference from Alloy is lack of data structures. Most model checking languages allow only v. low-level data structures.

Basic form of model checking query:



History of Model checking

- 1975 Dijkstra's Guarded Commands
- 1976-9 Jan Hajek's "Approver"
- 1977 Pnueli's Temporal Logic.
- 1978 Hoare's CSP.
- 1981 Clarke + Emerson
Quinn + Sifakis
'model checking'
- 1980 Holmann's PAT
- 1981 Wolper + Vardi: automaton framework for LTL.
- 1989 SPIN version 0
- 1989 McMillan's model checking with OBDD's.
- 1992 SMV.
- 1994 Pentium FPU bug: formal verification takes off.

Some current model checkers.

Explicit state.

SPIN

Java Pathfinder

Concurrence

TLC.

Process algebra.

Concurrency workbench

FDR

LTSA

Symbolic.

SMV : CMU, Cadence.

MUSMV

Specialized: for hybrid systems

HyTech

Uppaal.

Packaged.

SLAM / ~~BLAST~~ ~~Boyer~~ Microsoft : now SDV, for Windows dev.

BLAST Berkeley, MC for C programs drivers

axes of classification.

Spec language?

Temporal logic : CTL, LTL.

Process algebra

$\Box \text{Req} \Rightarrow \Diamond \text{Resp}$

$S = \text{Req} \rightarrow \text{Resp} \rightarrow S$

Machine language

Guarded commands,

C-like syntax, ~~with~~

SPIN

Raw assignments

SMV

Synchronous

SMV

Asynchronous

SPIN.

Analysis method

explicit state

SPIN

data str holds states

language induction

COSPAN

symbolic

SMV, Alloy.

word, formula

Applications

Hybrid

Real time

Hardware

Protocols

Software.

Modal Logics-

Logics of necessity and possibility.

□ It is necessary that ...

◇ It is possible that ...

Temporal logics

G It will always be the case that ...

F It will be the case that ...

H It has always been the case that ...

P It was the case that ...

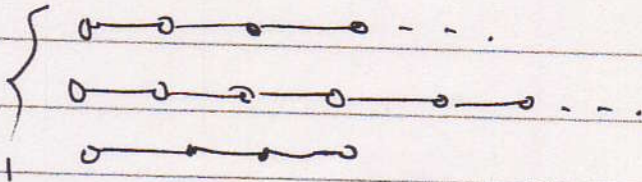
Modal logic invented by Lewis in 1918 to overcome paradox of implication ("false $\Rightarrow$ X" is true) by distinguishing necessary from contingent truth.

Applications in CS originated with Amir Pnueli's work in 1977, applying temporal logic to reasoning about programs.

Semantic models for temporal logic

Linear:

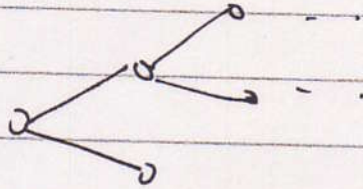
(eg. LTL)



set of traces : state sequences

Branching:

(eg. CTL)



tree of states

We'll start by explaining CTL*, a branching-time logic.
Then we'll explain CTL and LTL as subsets of it.

Computation trees.

Take state machine M with transition relation R over states S

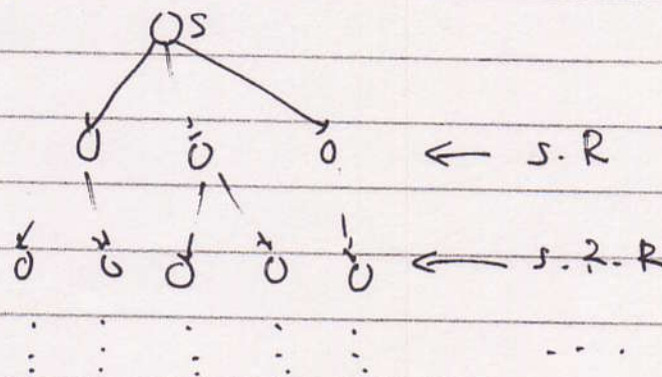
Important assumption:

R is total i.e. S in $\text{dom}(R)$

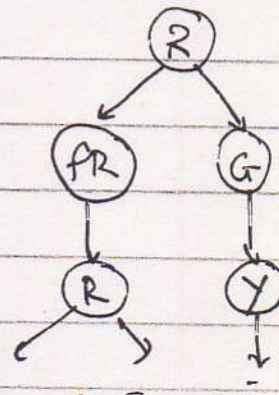
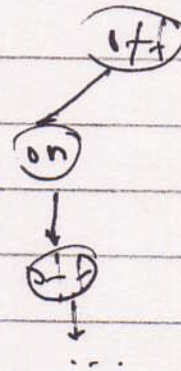
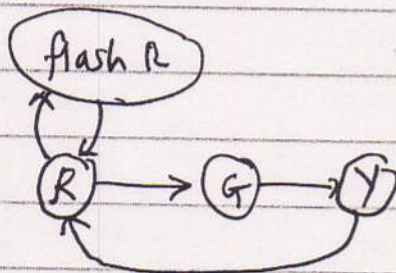
all $s: S \mid \text{some } s.R$

Usually R is not functional : non-determinism.

Computation tree T for R , rooted in state s :



Eg.



Kripke structures-

We'll want to have a notion of evaluation of non-temporal properties of states. Simplest theoretical model — associate set of propositions with each state

Kripke str is $\langle S, R, L \rangle$

$$R \subseteq S \times S$$

$$L : S \rightarrow 2^{\mathcal{P}}$$

Paths

path or trace $\pi = s_0 s_1 s_2 \dots$

s.t. $(s_i, s_{i+1}) \in R$

$s_i \rightarrow s_{i+1}$ in R

π^i is suffix starting at s_i : $s_i s_{i+1} \dots$

Rework traffic light.

propositions

$R, G, Y, F(\text{flashing})$

CTL*

Two kinds of formulas:

state formulas meaning def'd wrt. a state
path formulas path.

Syntax.

proposition p
 p is a state formula
if f is a

state formulas true $\phi \vee \psi$
 $\phi ::= p \mid \text{false} \mid \neg \phi \mid \phi \wedge \psi \mid A\alpha \mid E\alpha$
path formulas $\alpha \vee \beta$ $A\alpha$ $E\alpha$
 $\alpha ::= \phi \mid \neg \alpha \mid \alpha \wedge \beta \mid \alpha \cup \beta \mid G\alpha \mid F\alpha \mid X\alpha$
always all paths exists path. until globally finally next

NOTE that

state formula $\subseteq$ path formula,
but not v.v.

Semantics of CTL*

$$\begin{aligned} M, s \models p &\iff p \in L(s) \\ M, s \models \neg f &\iff M, s \not\models f \\ M, s \models f \vee g &\iff M, s \models f \vee M, s \models g \end{aligned}$$

$$\begin{aligned} M, s \models E f &\iff \exists \pi = s \dots \mid M, \pi \models f \\ M, s \models A f &\iff \forall \pi = s \dots \mid M, \pi \models f \end{aligned}$$

state formula

$$M, \pi \models f \iff \pi = s_0 \dots \mid M, s_0 \models f$$

$$\begin{aligned} M, \pi \models X f &\iff M, \pi^1 \models f \\ M, \pi \models F f &\iff \exists k \geq 0 \mid M, \pi^k \models f \\ M, \pi \models G f &\iff \forall k \geq 0 \mid M, \pi^k \models f \\ M, \pi \models f U g &\iff \exists k \geq 0 \mid M, \pi^k \models g \\ &\quad \wedge \forall 0 \leq j < k \mid M, \pi^j \models f \end{aligned}$$

$\neg, \neg, X, U, E$
are sufficient

$$\begin{aligned} \neg f &\equiv \text{True} \cup f \\ G f &\equiv \neg F \neg f \\ A f &\equiv \neg E \neg f \end{aligned}$$