Unified Compilation for Lossless Compression and Sparse Computing

Daniel Donenfeld, Stephen Chou, Saman Amarasinghe



Kristina

★★★☆☆ Great Product

March 30, 2017

Color: White Verified Purchase

Great product. Large enough for all spoons and fits nicely on my stovetop. Would definitely buy it again.



Teresa

** Excellent buy

October 25, 2017

Verified Purchase

This is a great product for your boy who loves sports! It was a good value as well. Other stores sell for 3x the cost. I bought one for a basketball and football and my 9 year old loves it in his room. Solid item too, not flimsy. Will hold items nicely.



Lisa

** Twas really disappointed. The spoon holder it self was great and ...

December 31, 2016

Color: Black | Verified Purchase

This product came with a manufacture's chips in it. It is not the sellers fault but I do not know how many in this batch this seller may have. I was really disappointed. The spoon holder it self was great and larger then I expected.



Sarah

*** Malfunctioned within a month. Waste of \$.

December 5, 2017

Style: Battery Powered Alarm | Size: 1 Pack | Verified Purchase



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Users

OMaple Peter Lilly Paul Billy Hilde Bob Sam Mary



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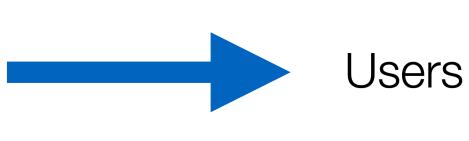
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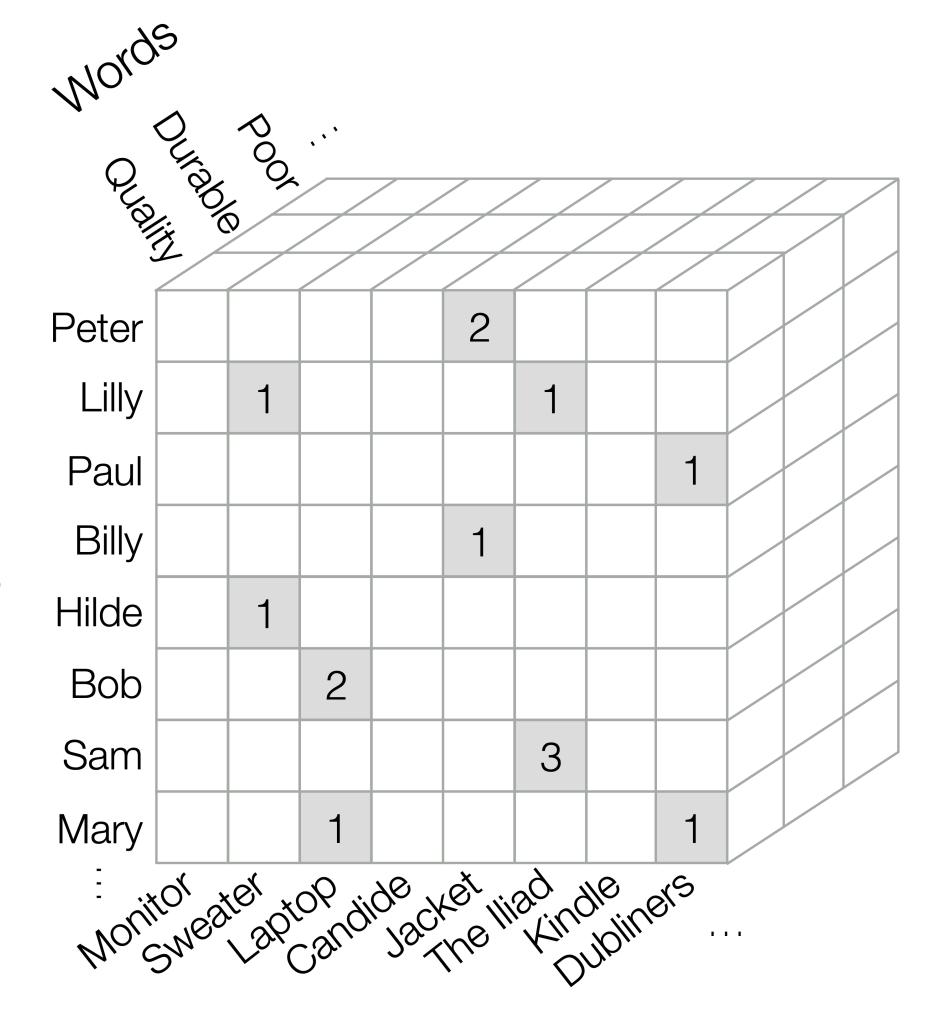
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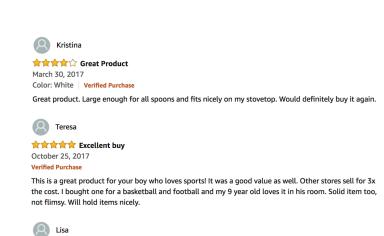
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Dense storage: 107 exabytes

Sparse storage: 13 gigabytes



December 31, 2016

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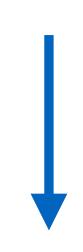
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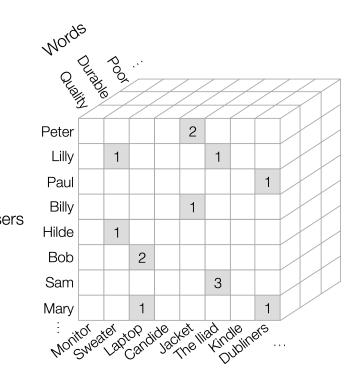


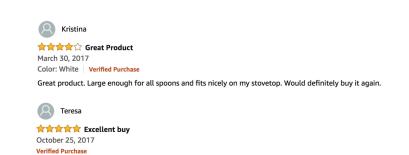
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December 5, 2017 Style: Battery Powered Alarm | Size: 1 Pack | Verified Purchase

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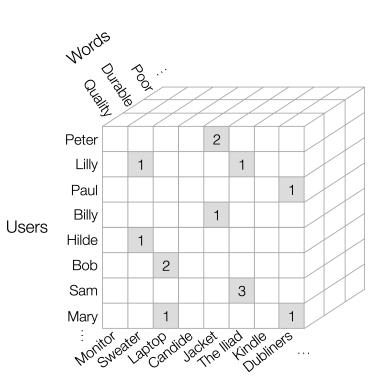
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Sarah

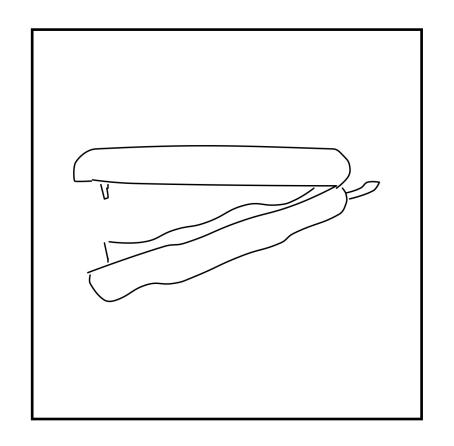
Malfunctioned within a month. Waste of \$. December 5, 2017

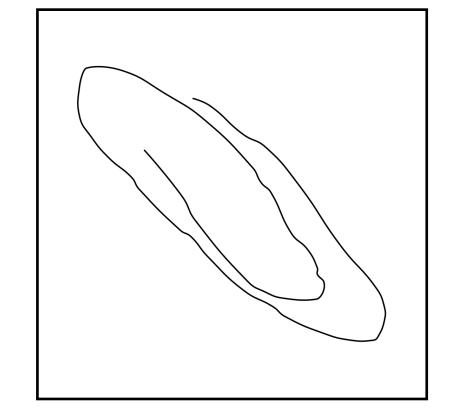
Style: Battery Powered Alarm | Size: 1 Pack | Verified Purchase

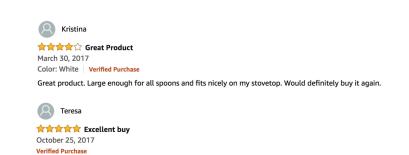












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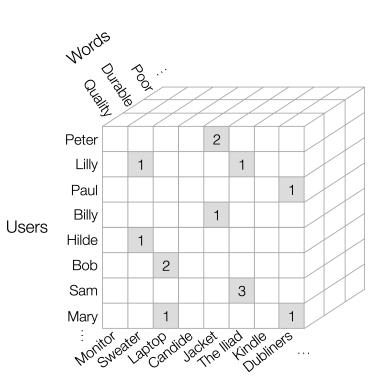
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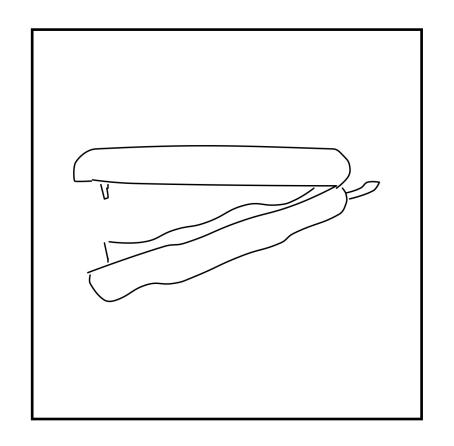
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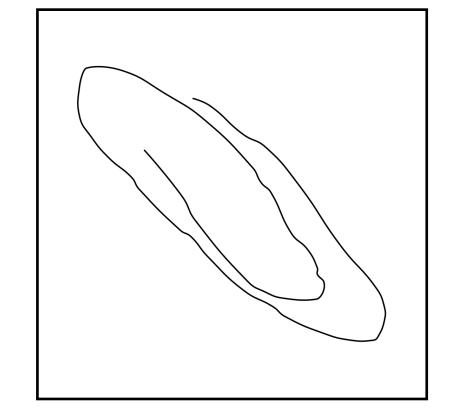
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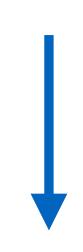
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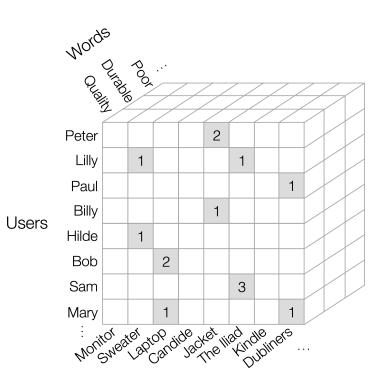
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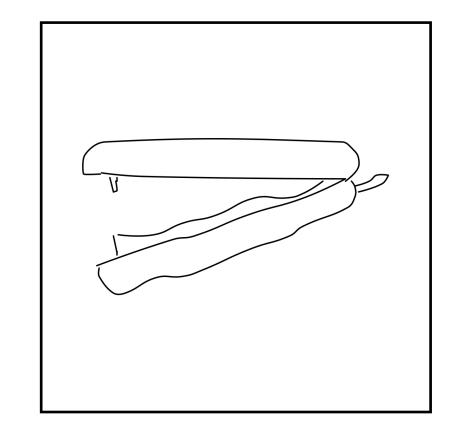
December 5, 2017

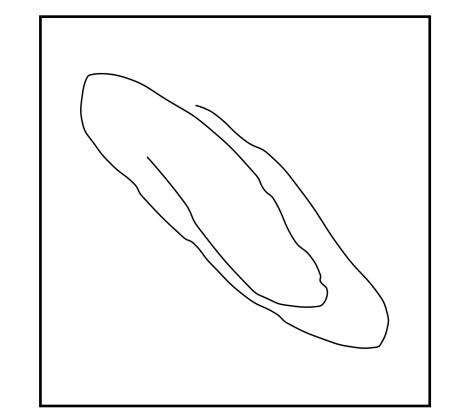
Style: Battery Powered Alarm | Size: 1 Pack | Verified Purchase



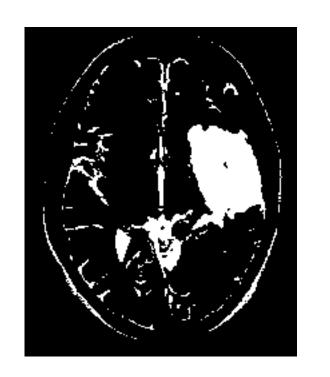








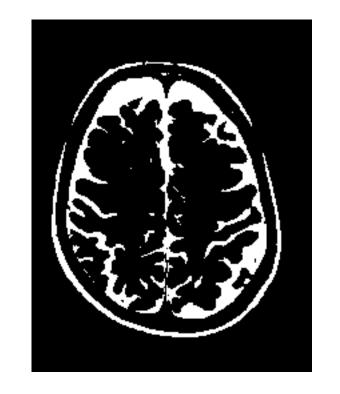




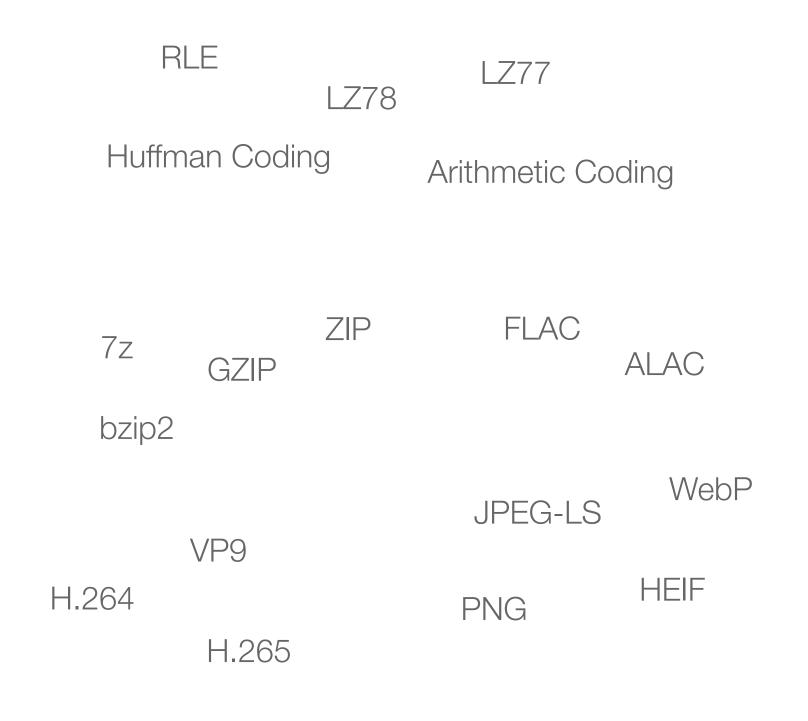


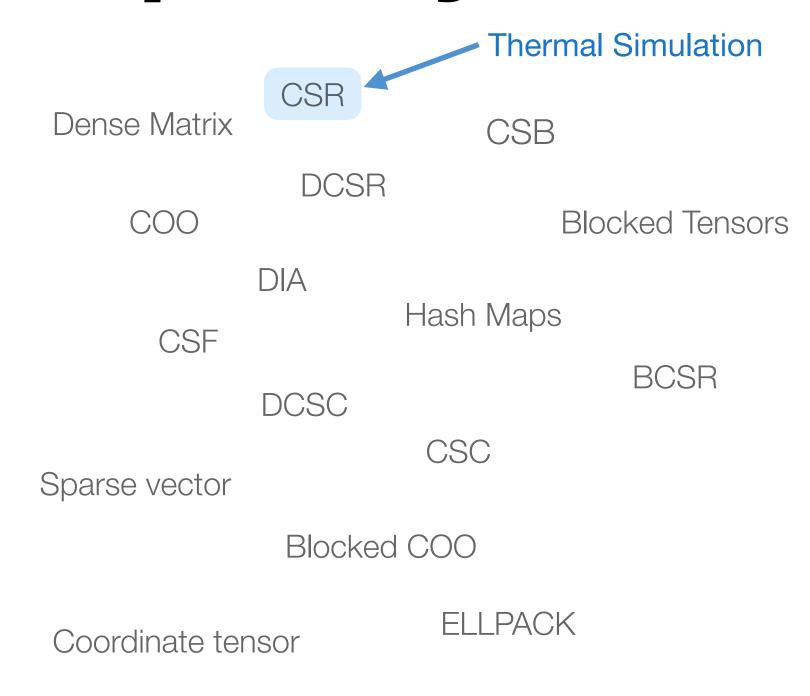


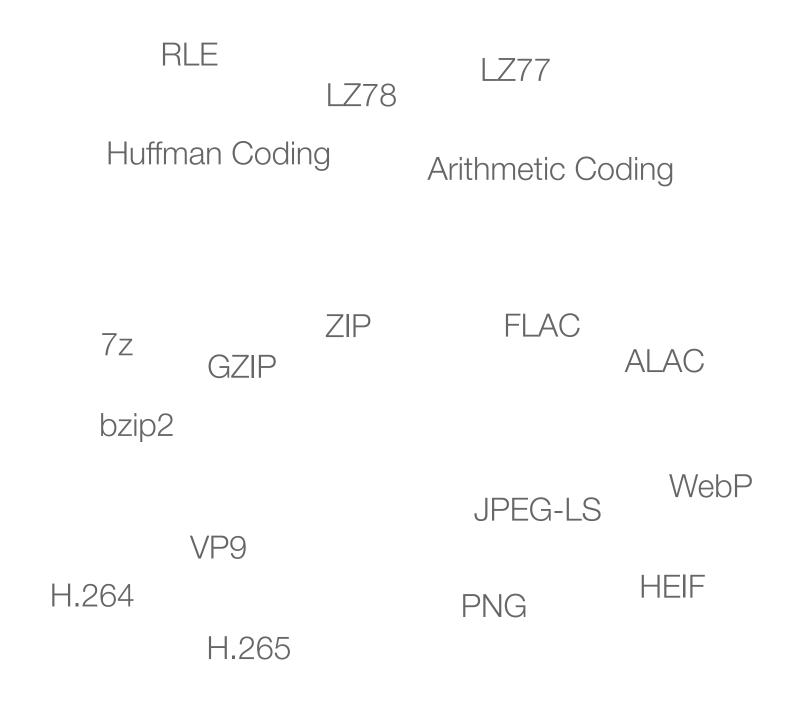


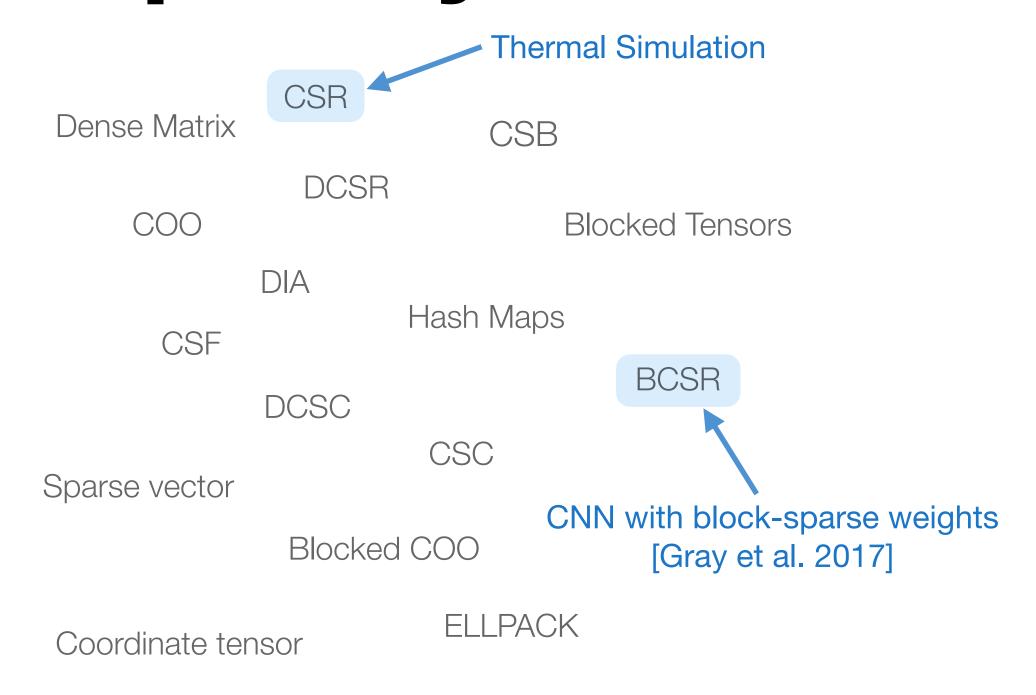


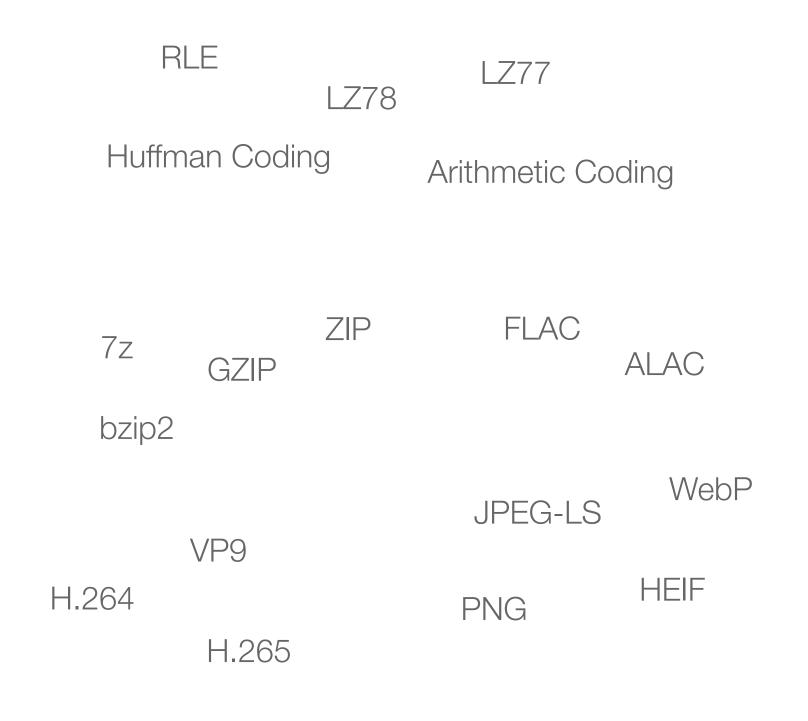
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CSR
Dense Matrix
                          CSB
               DCSR
     COO
                              Blocked Tensors
             DIA
                     Hash Maps
      CSF
                                   BCSR
             DCSC
                      CSC
Sparse vector
              Blocked COO
                       ELLPACK
Coordinate tensor
```

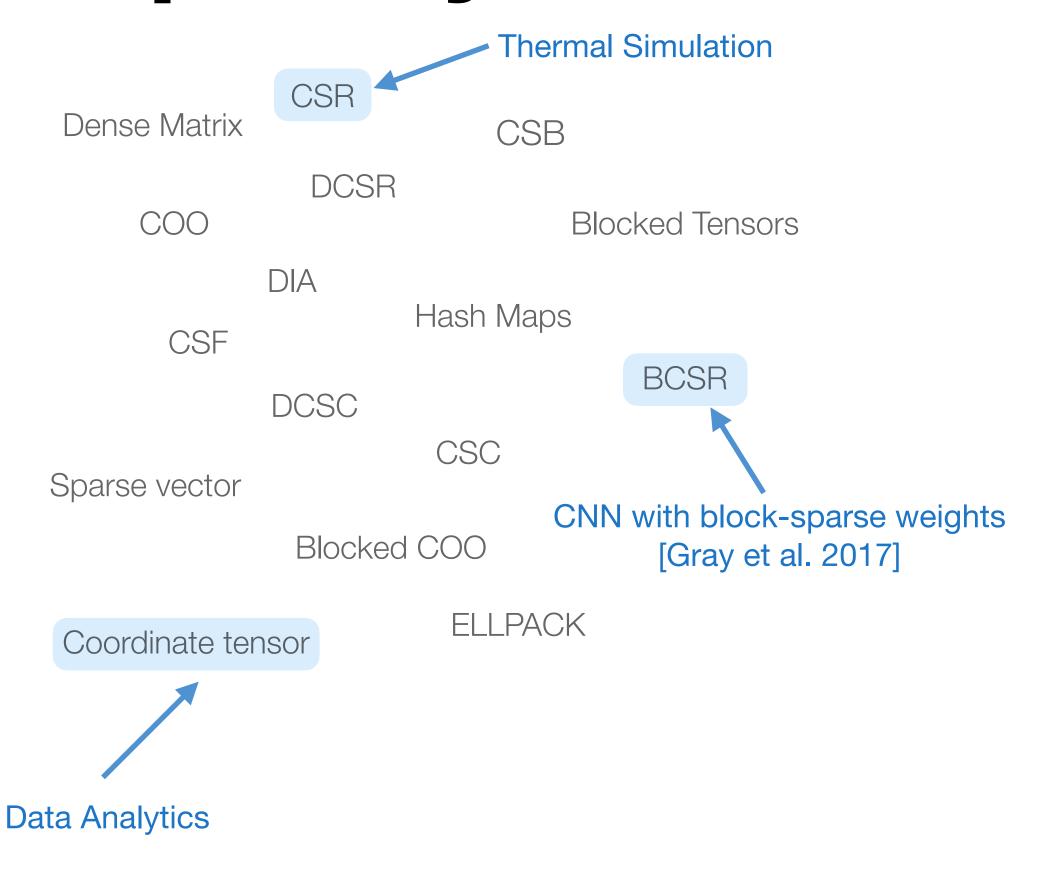


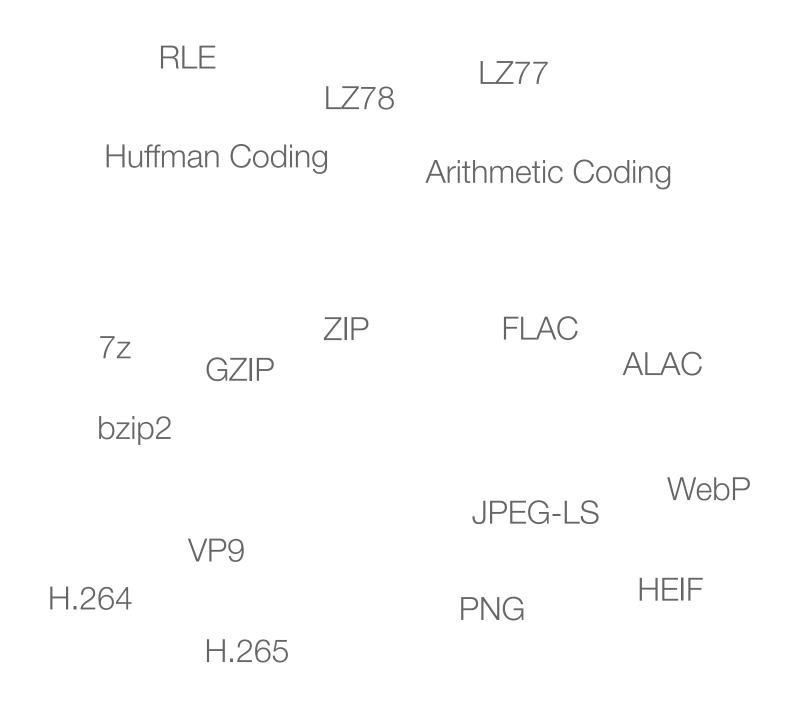


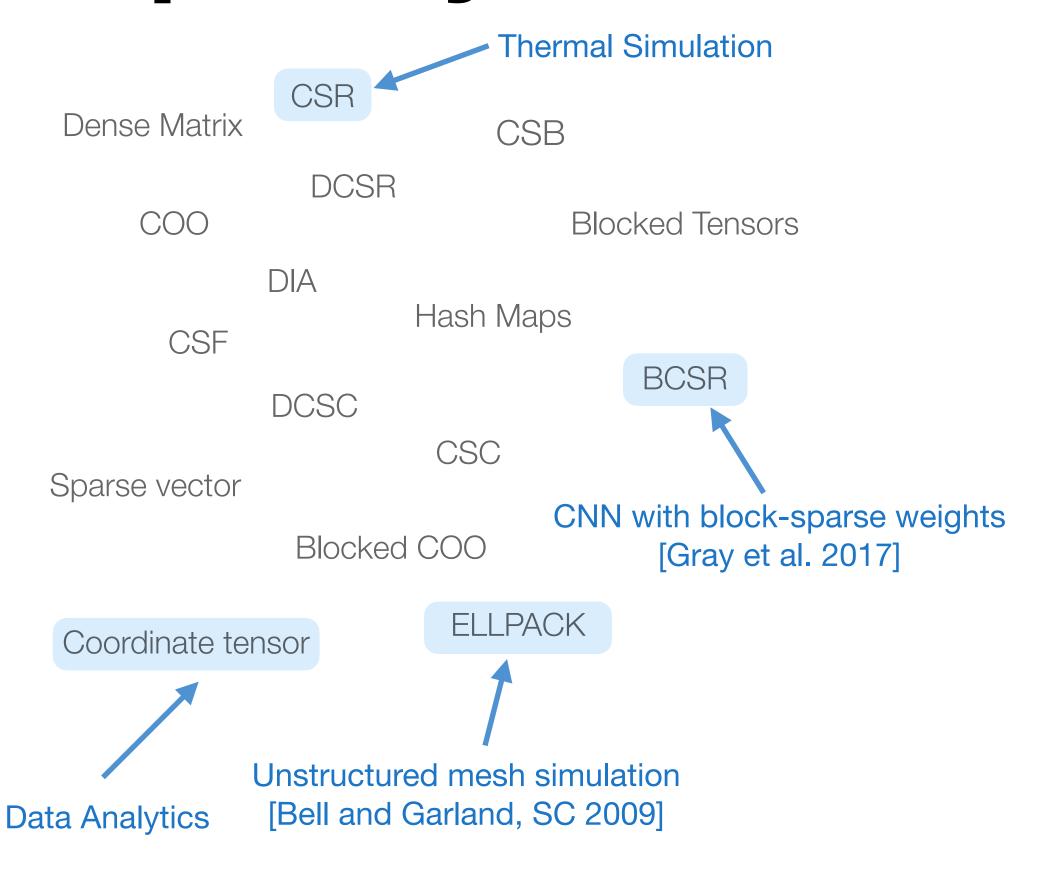


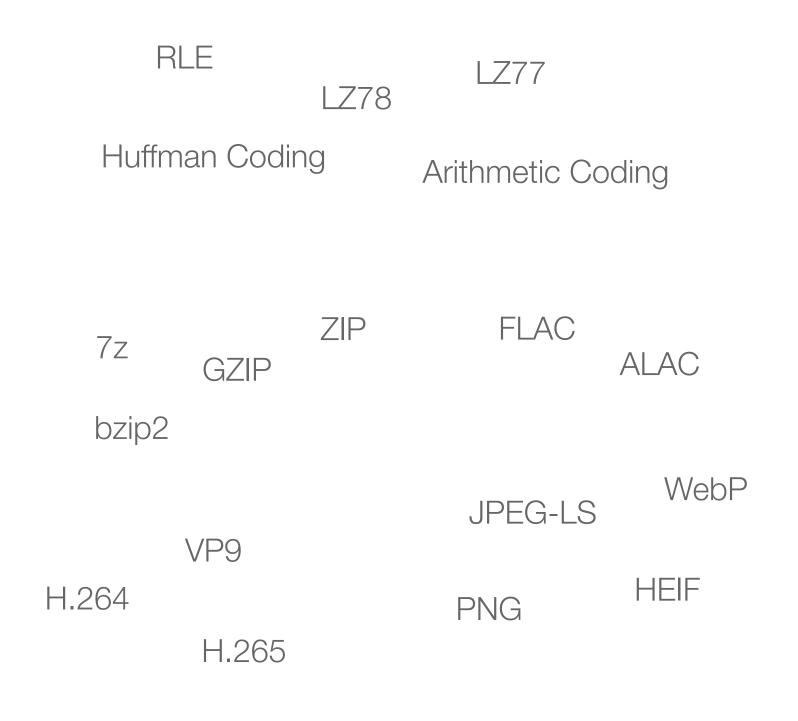


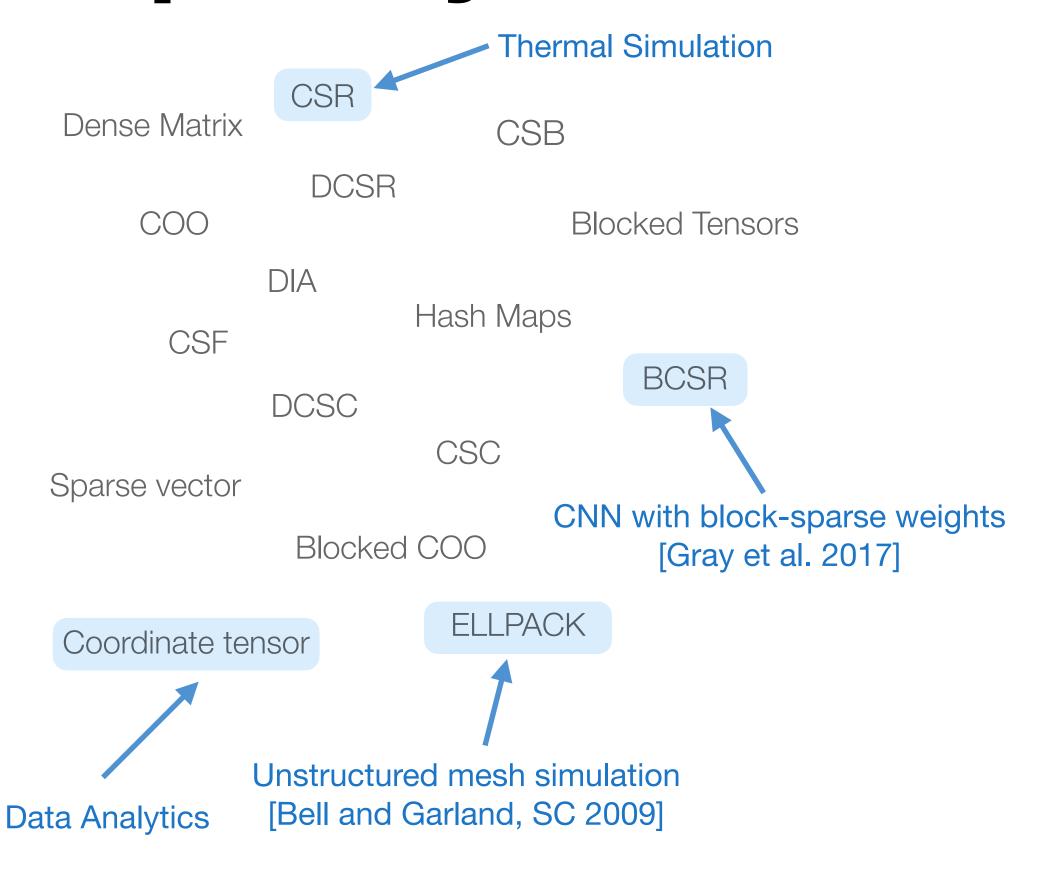


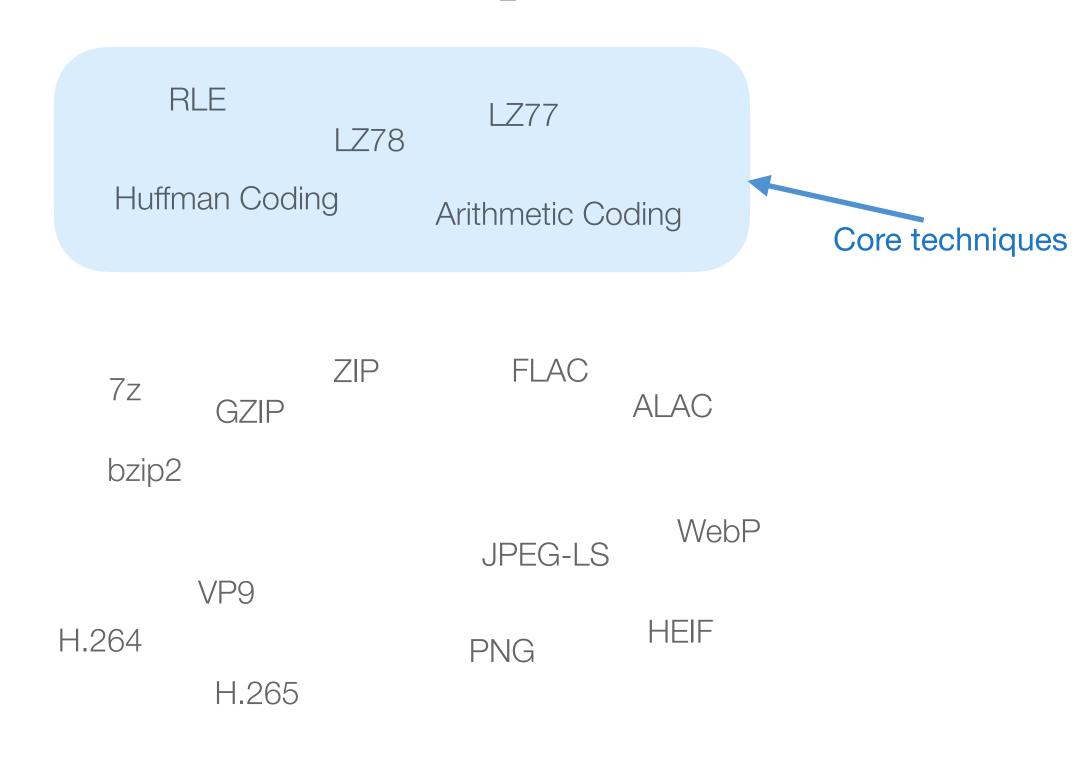


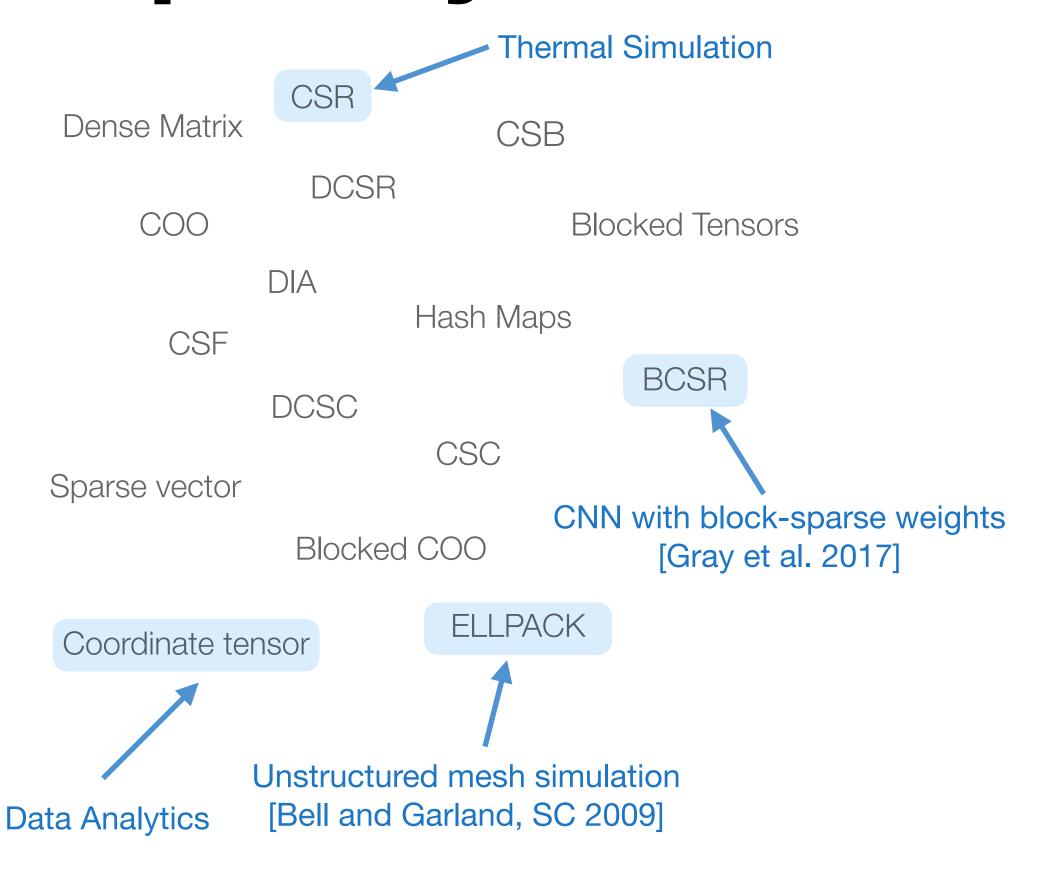


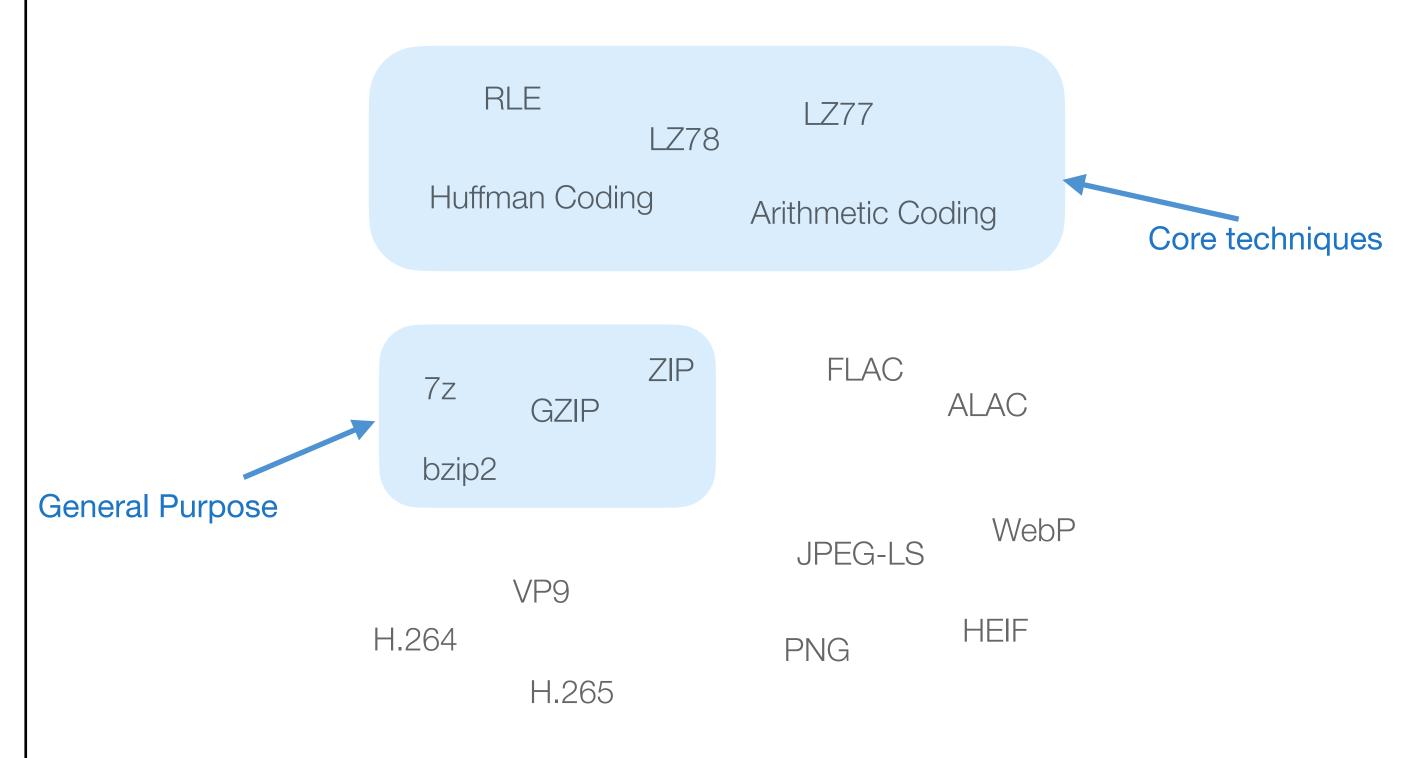


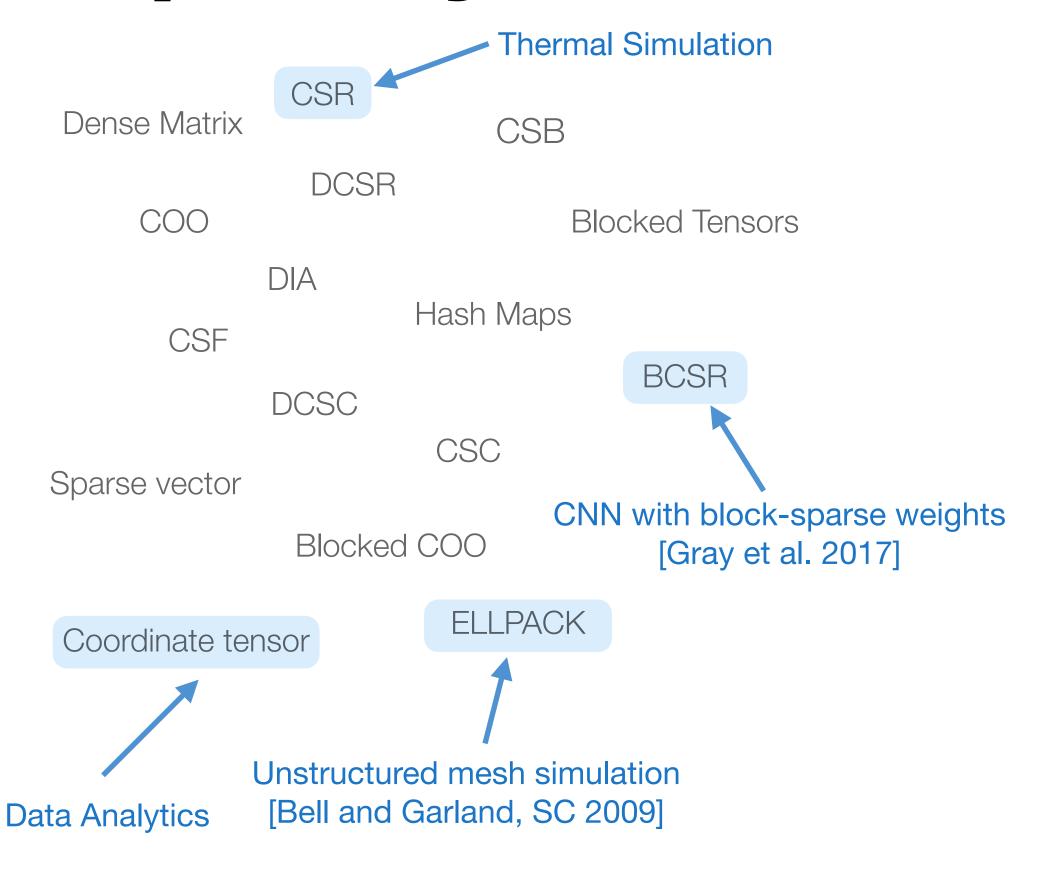


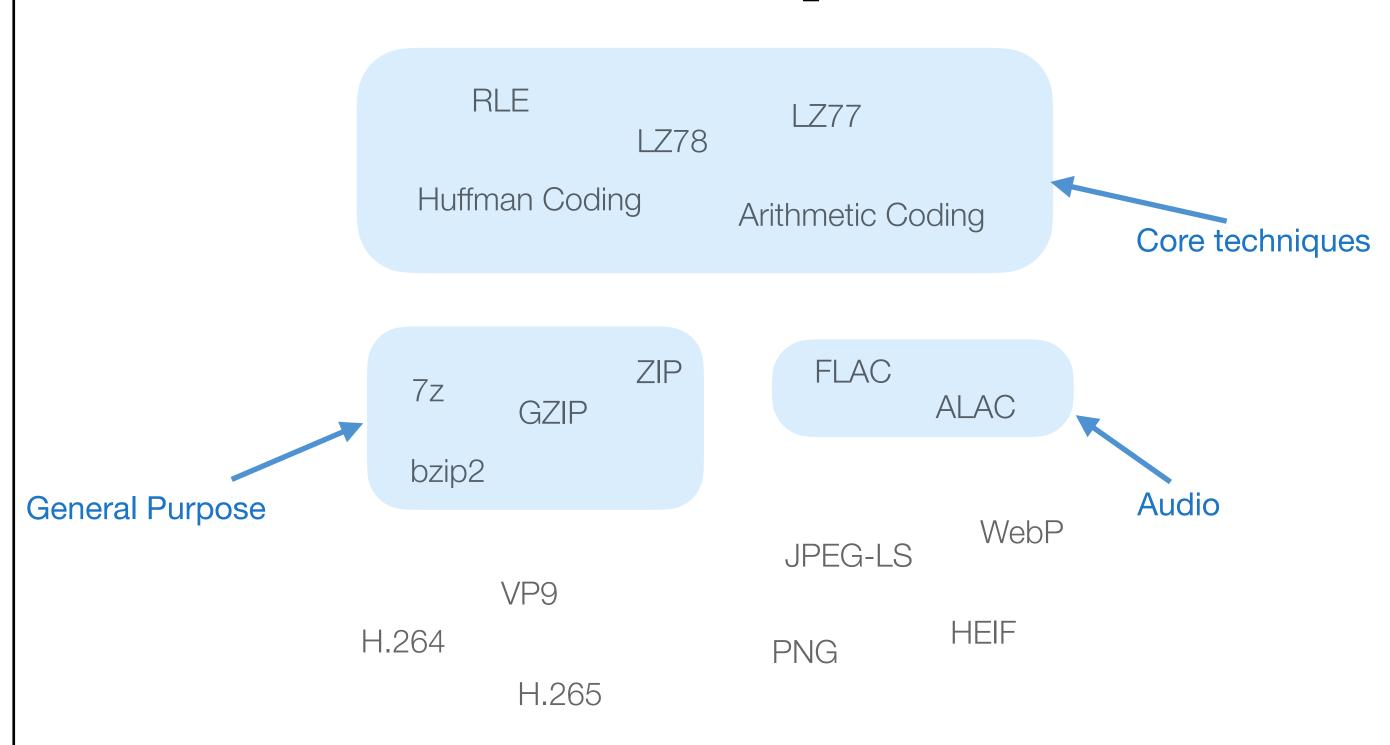


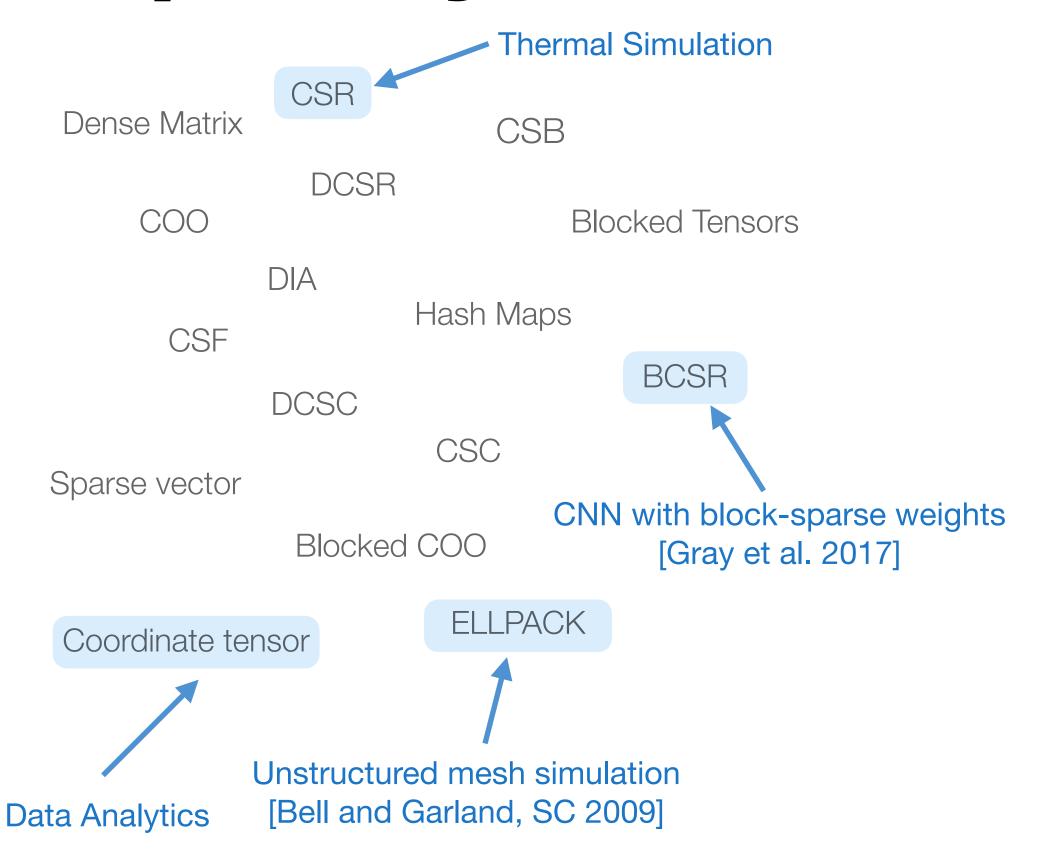


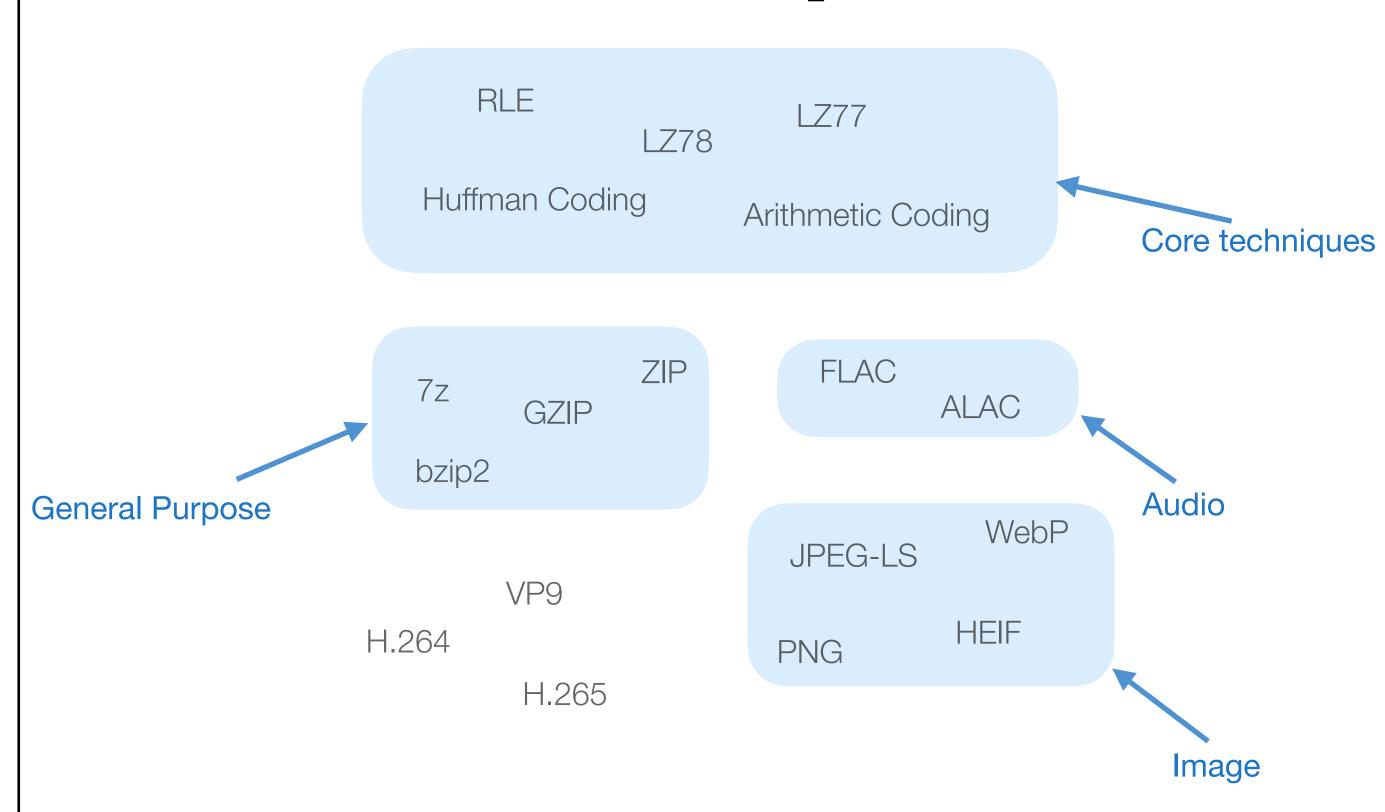


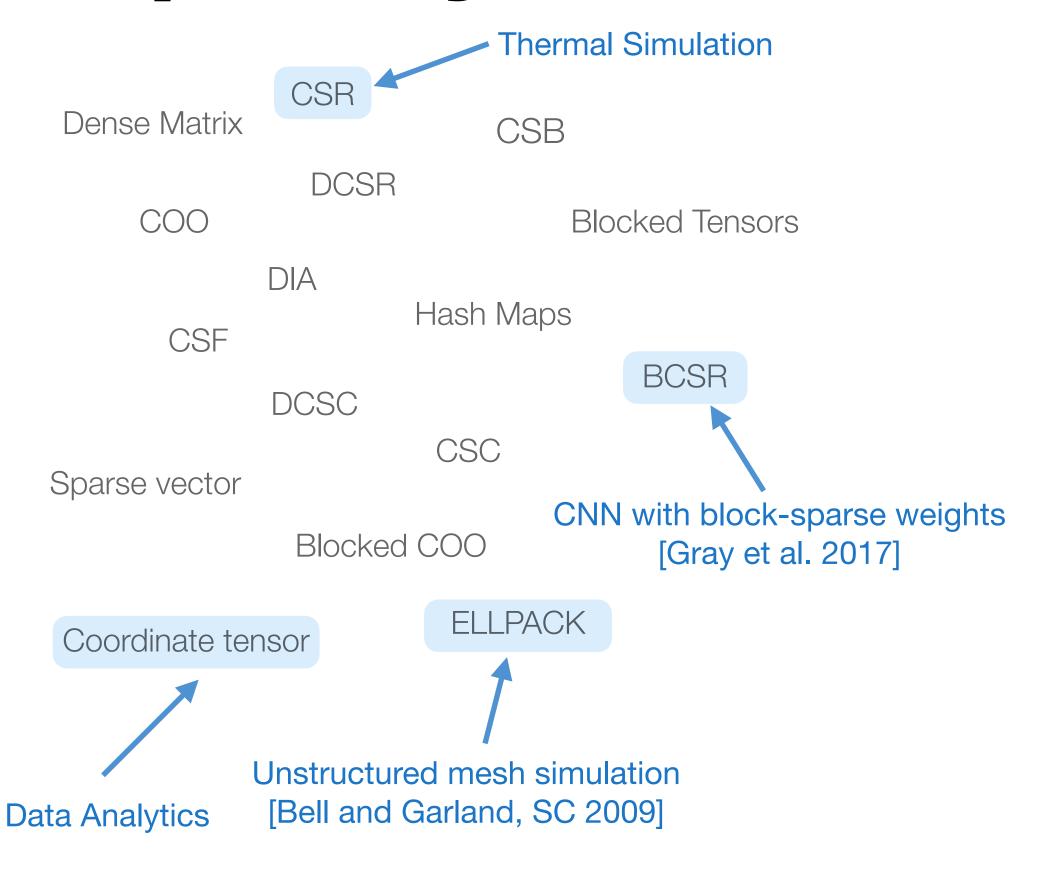


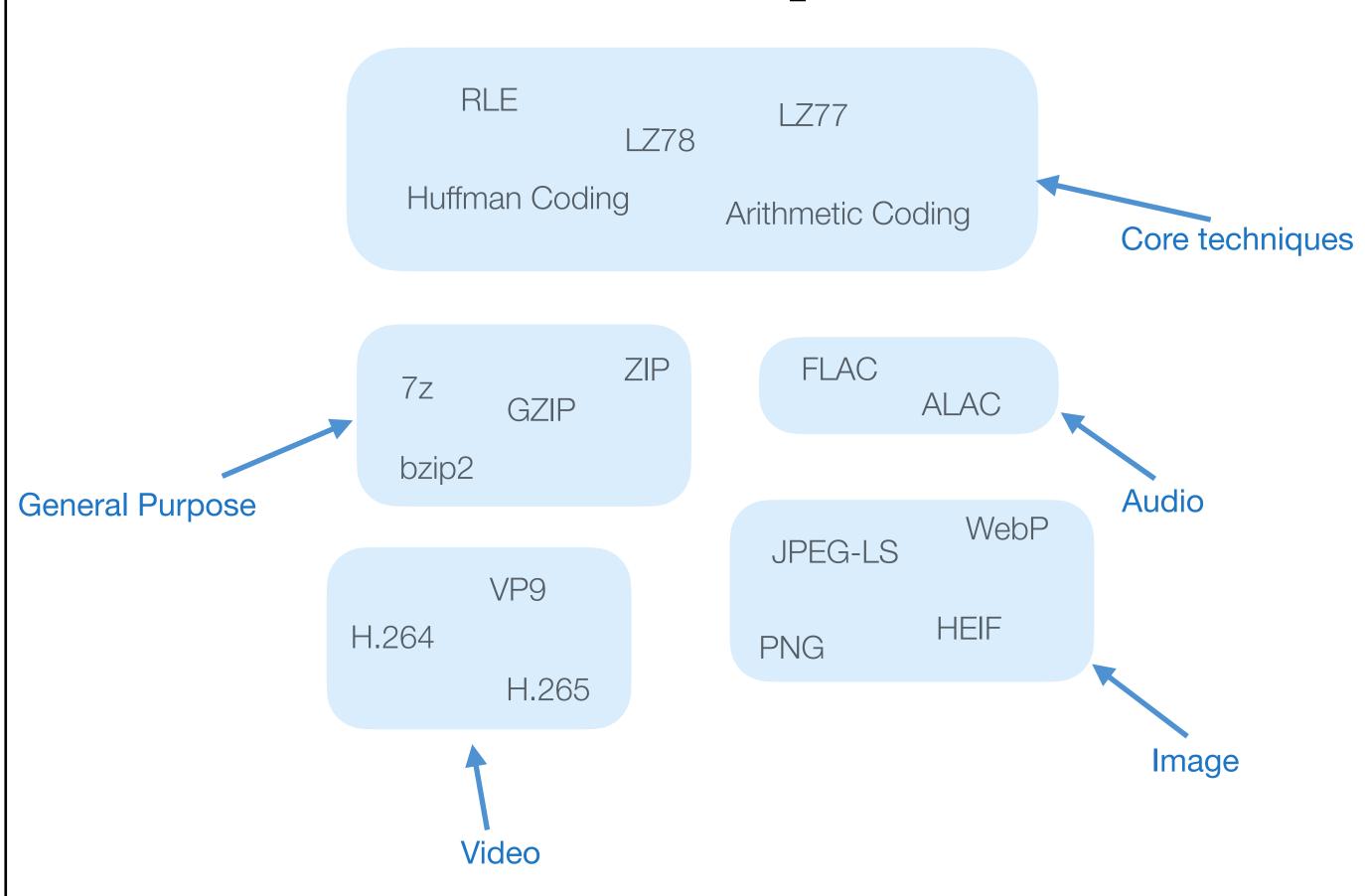




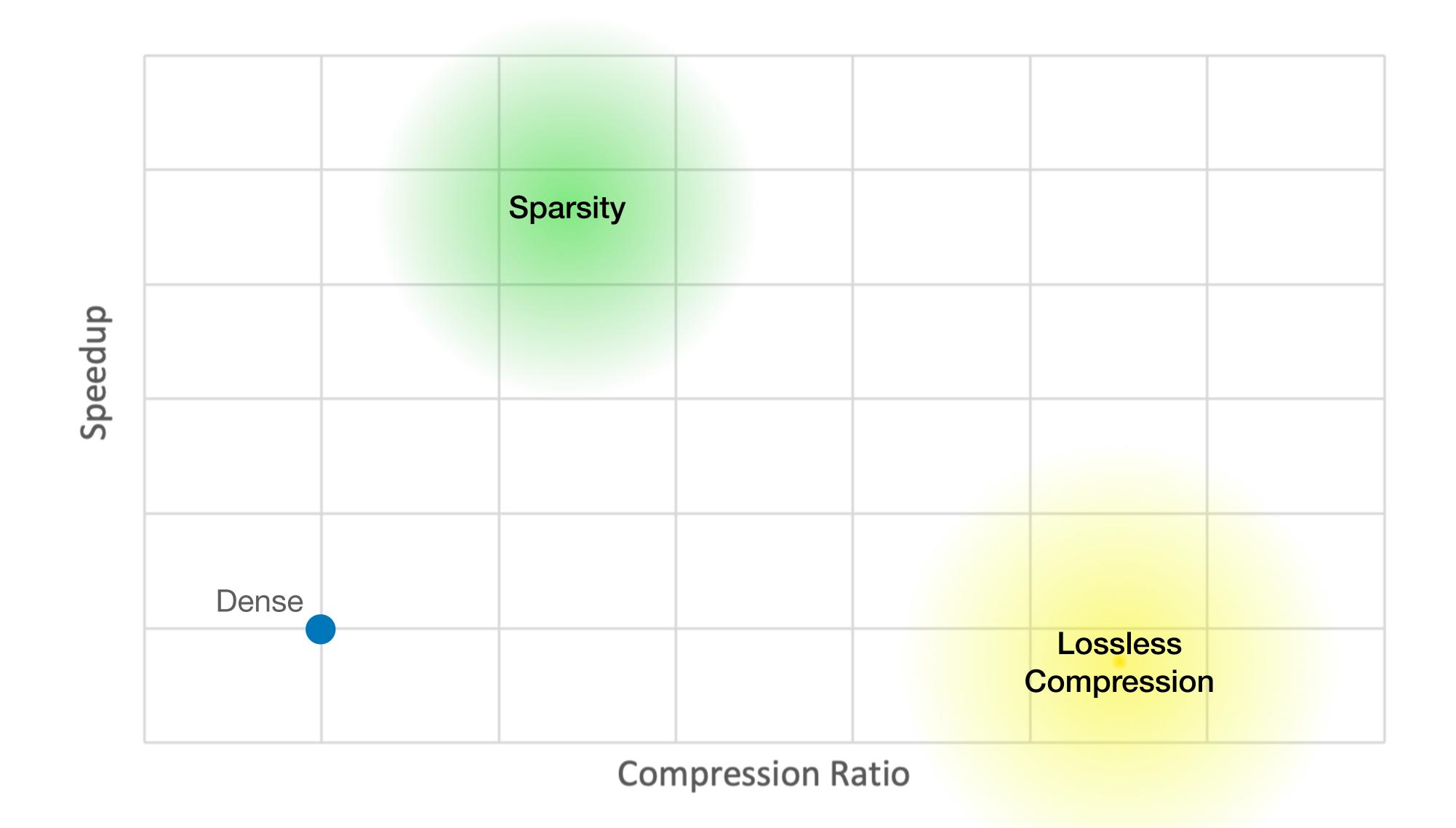








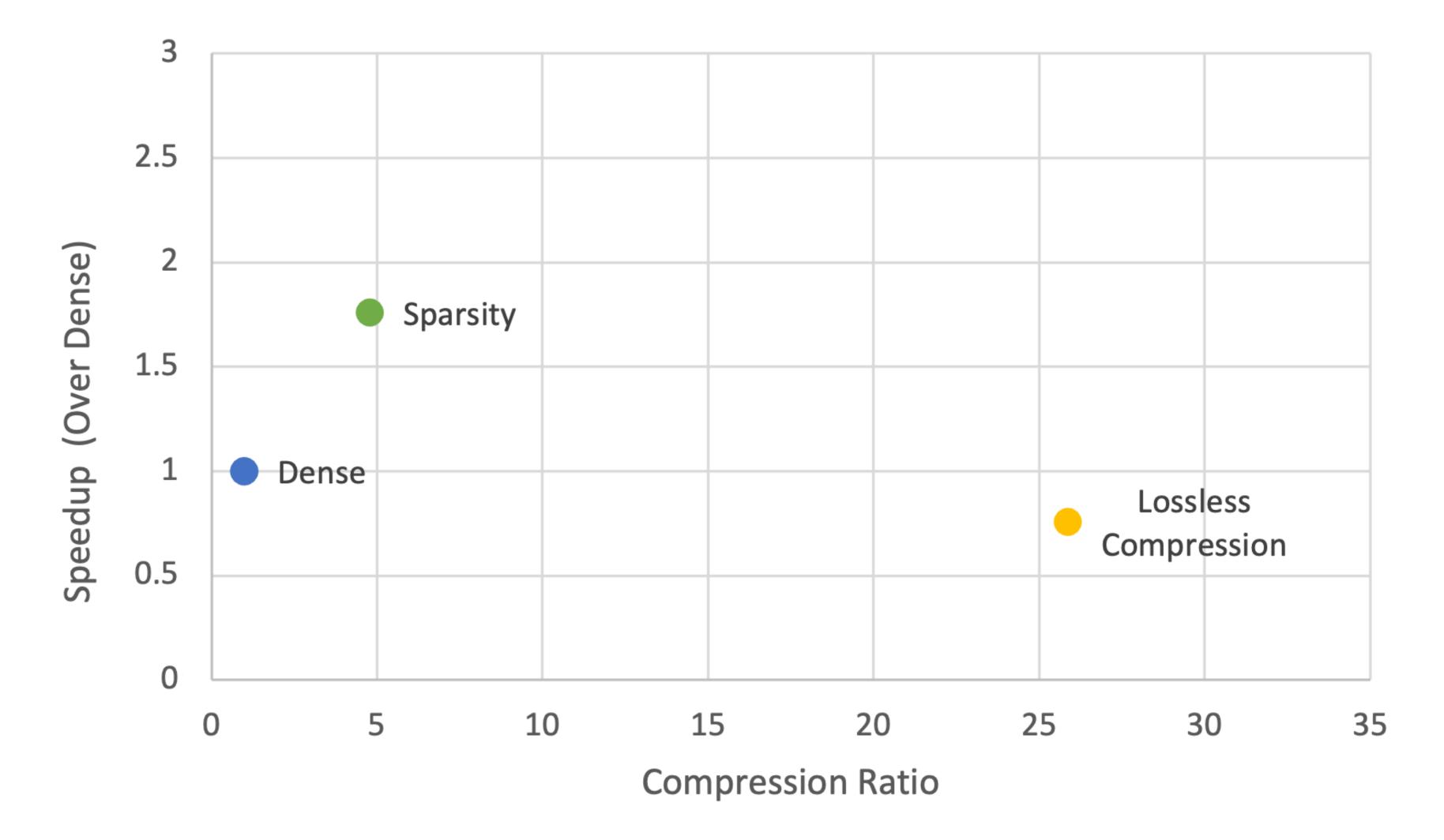
Sparsity and Compression have traditionally had different goals



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Edge Detection

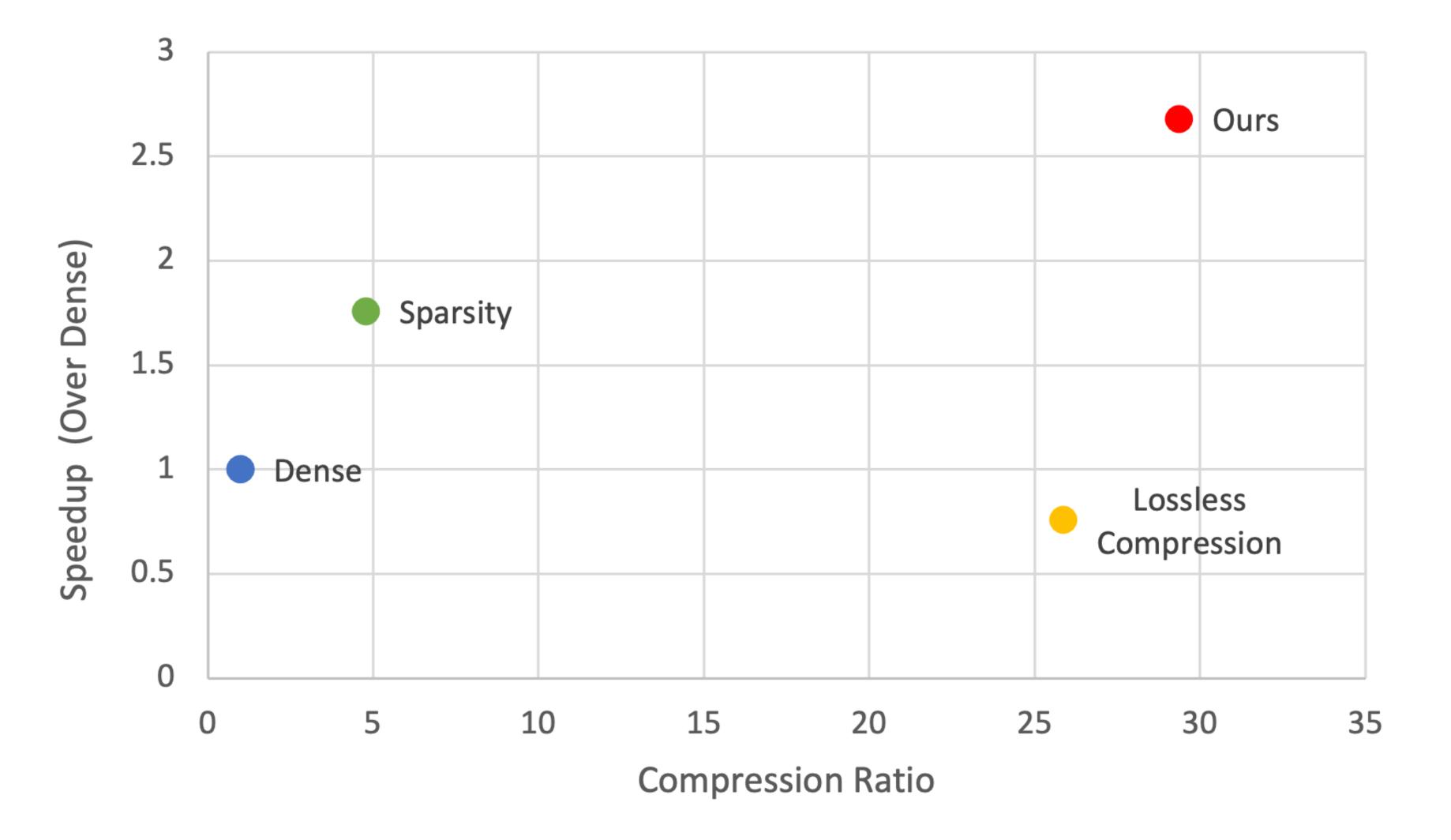


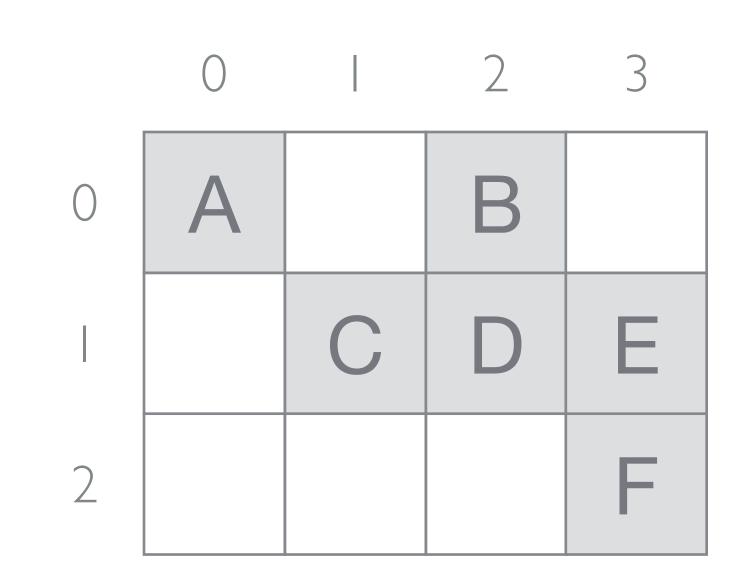


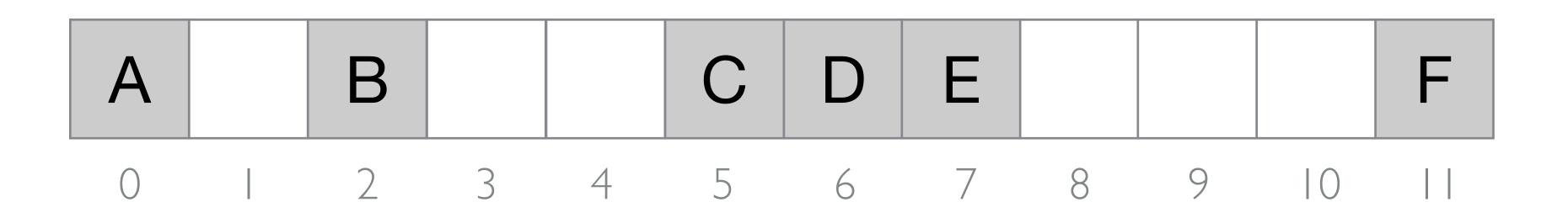
Unifying both can result in the best of both compression and performance

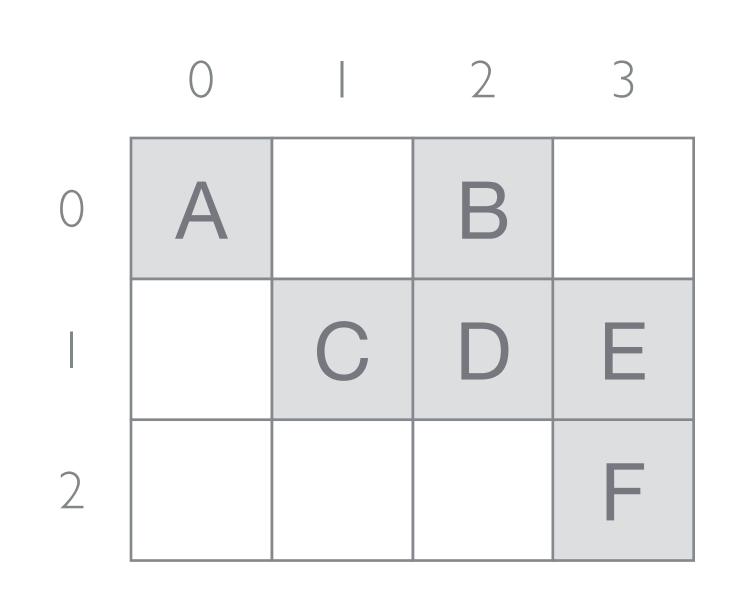
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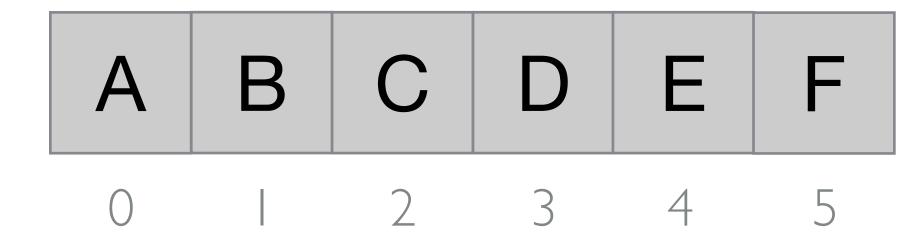




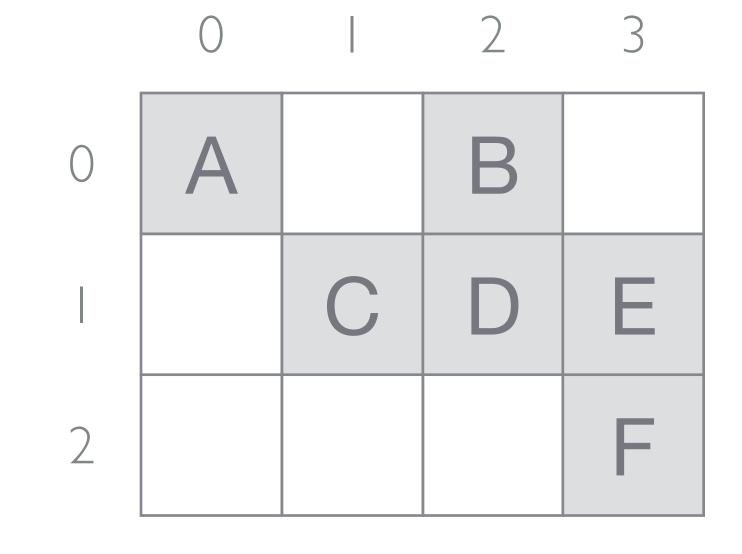




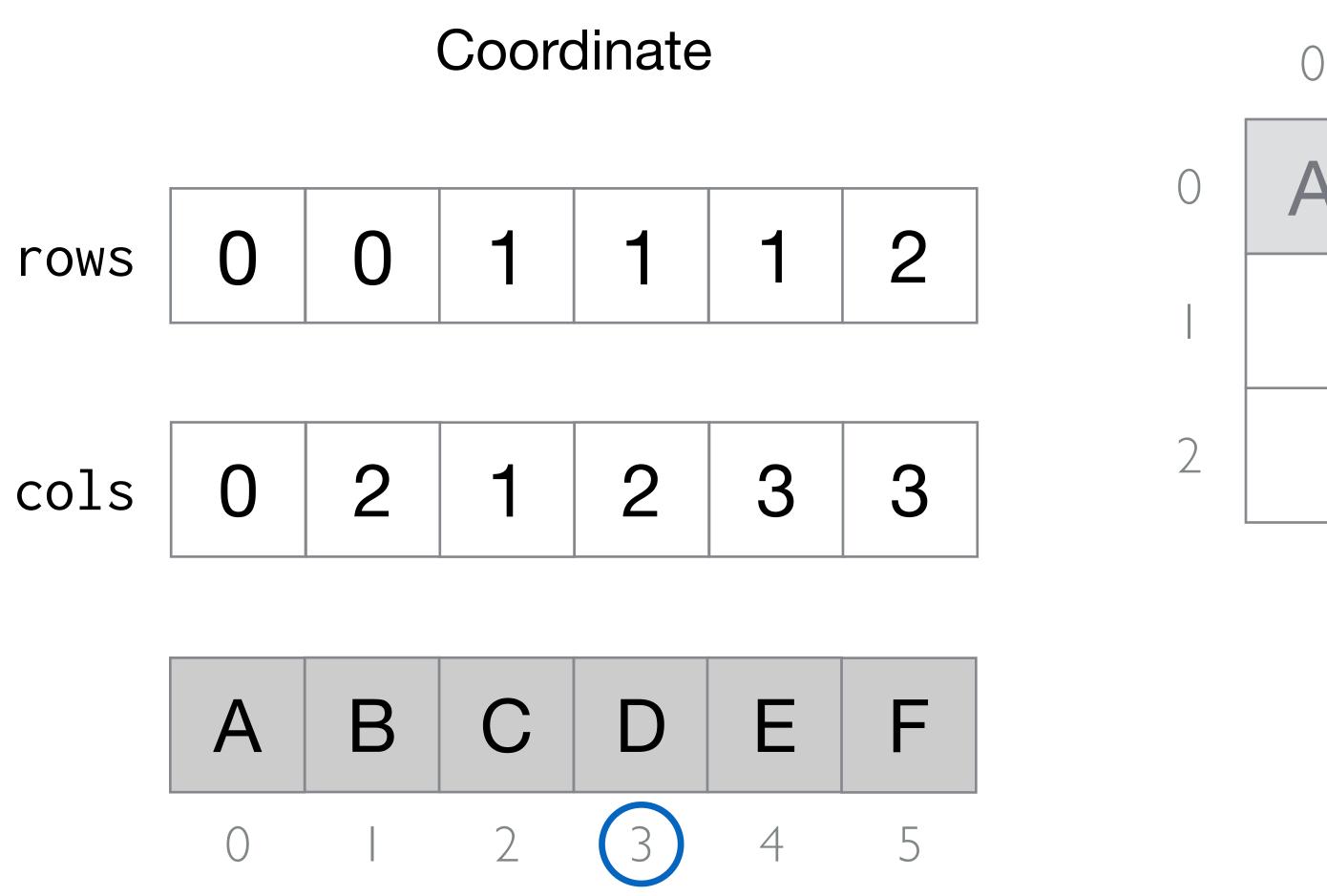


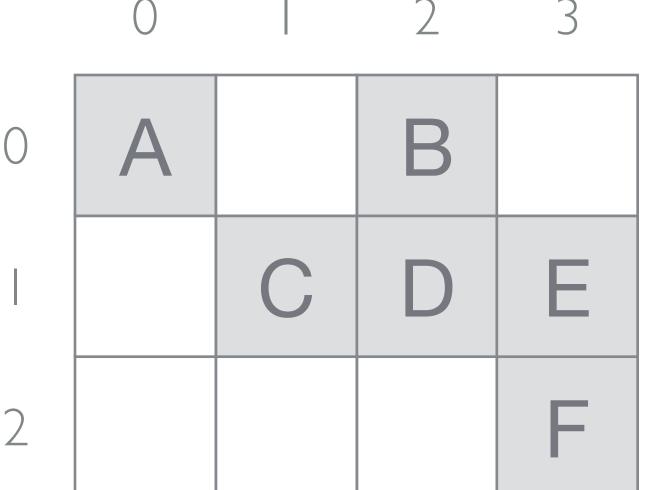


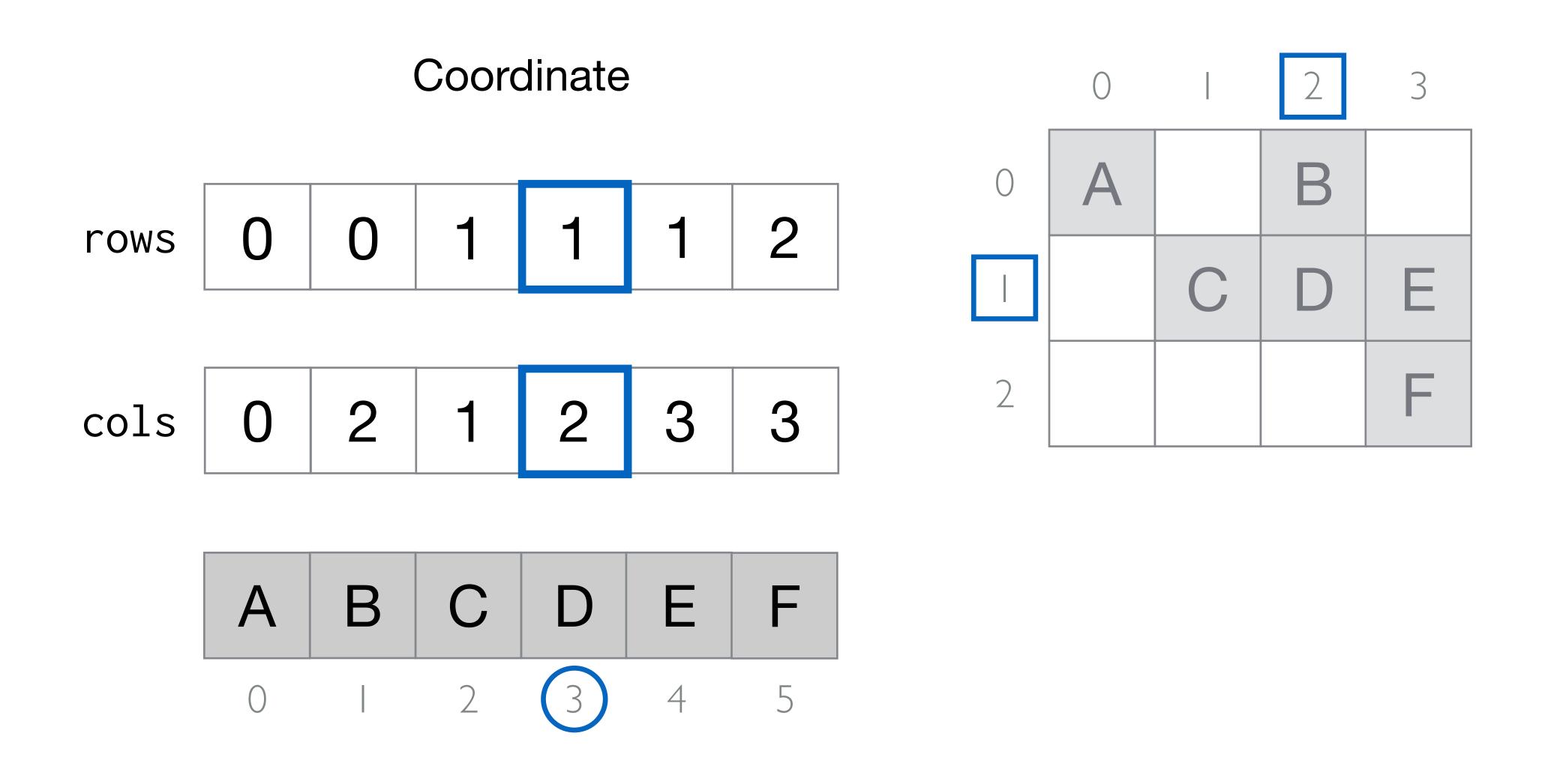
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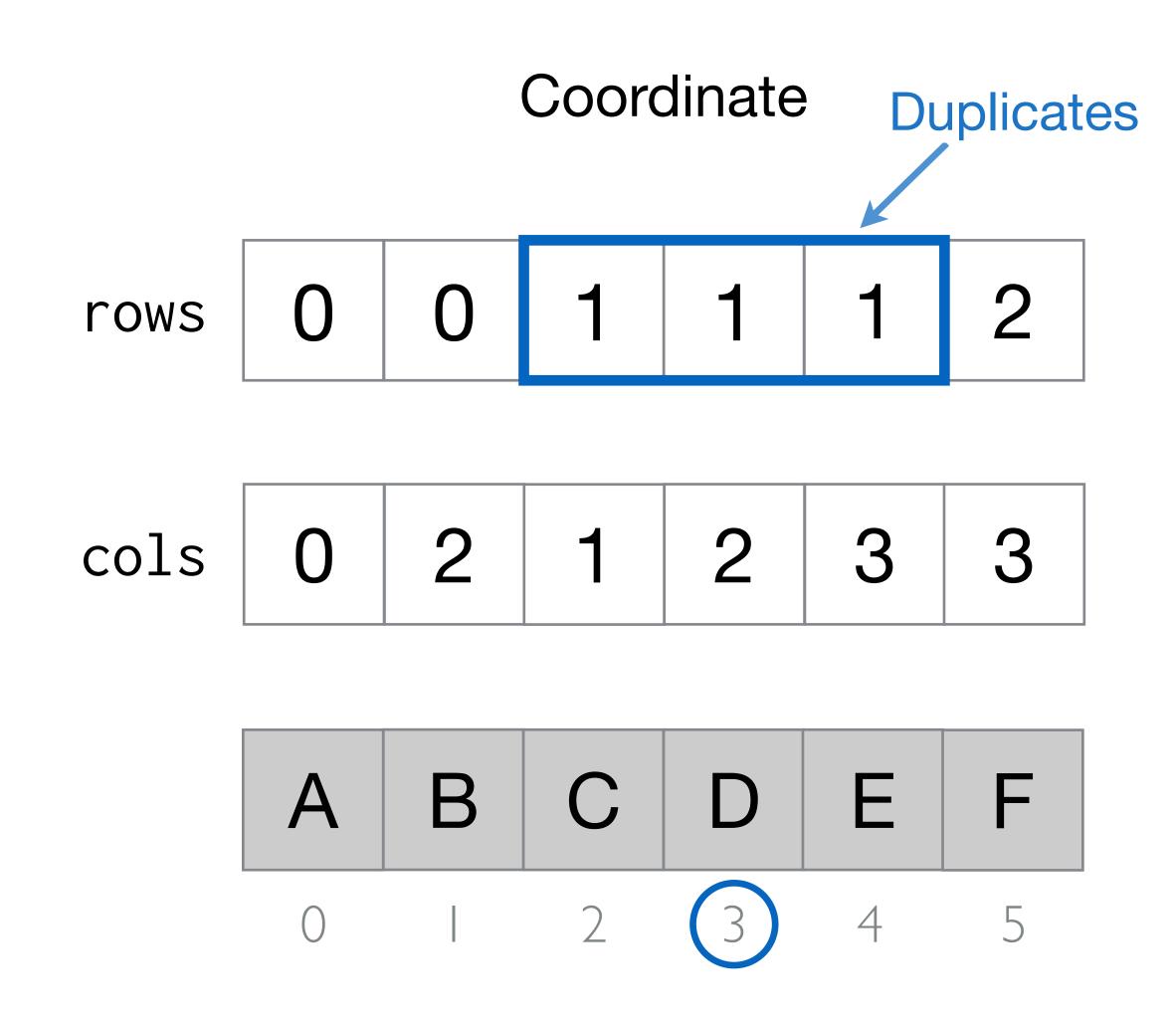


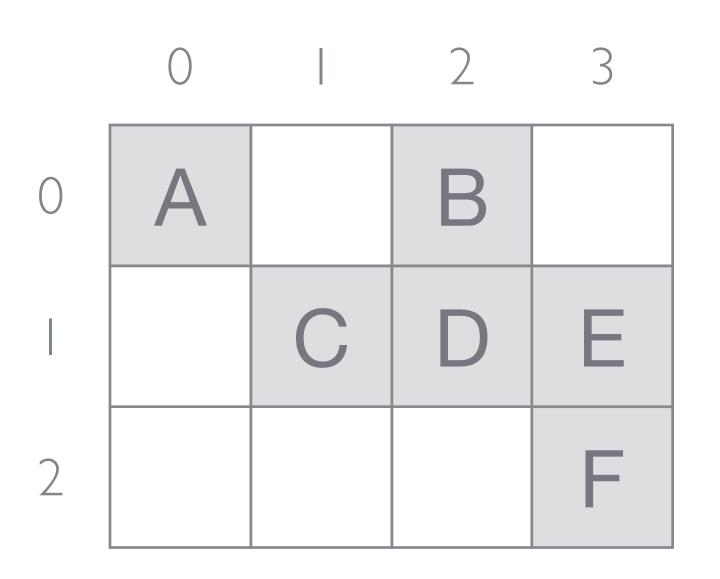
A	В	C	D	Е	F
		2	3	4	5



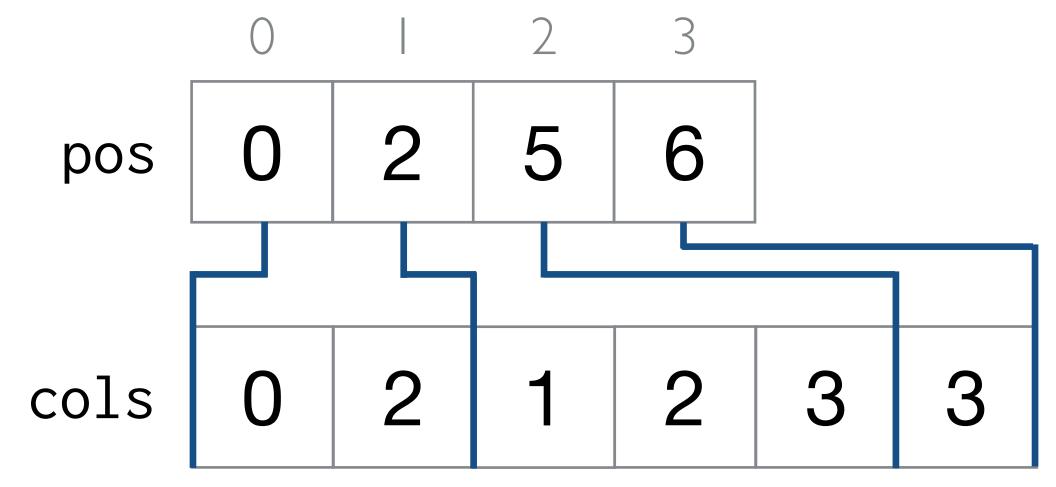


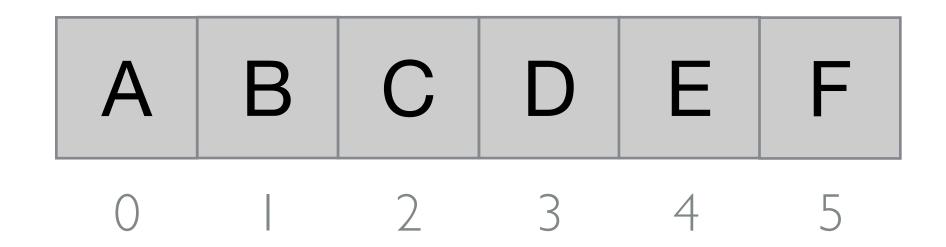


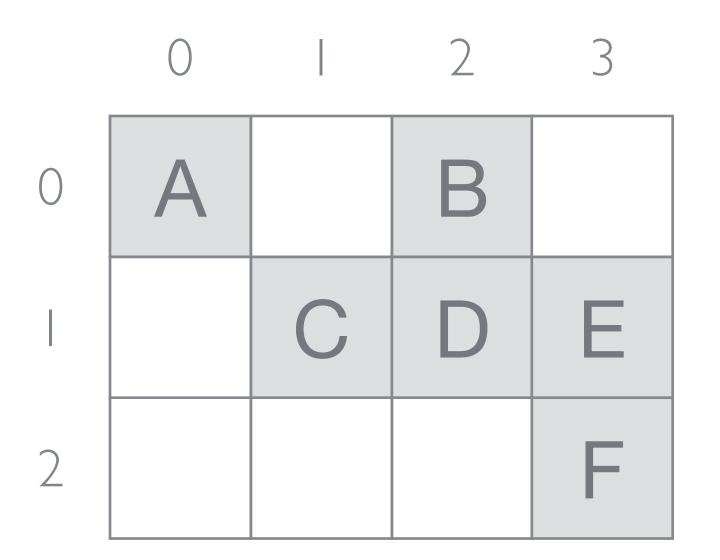


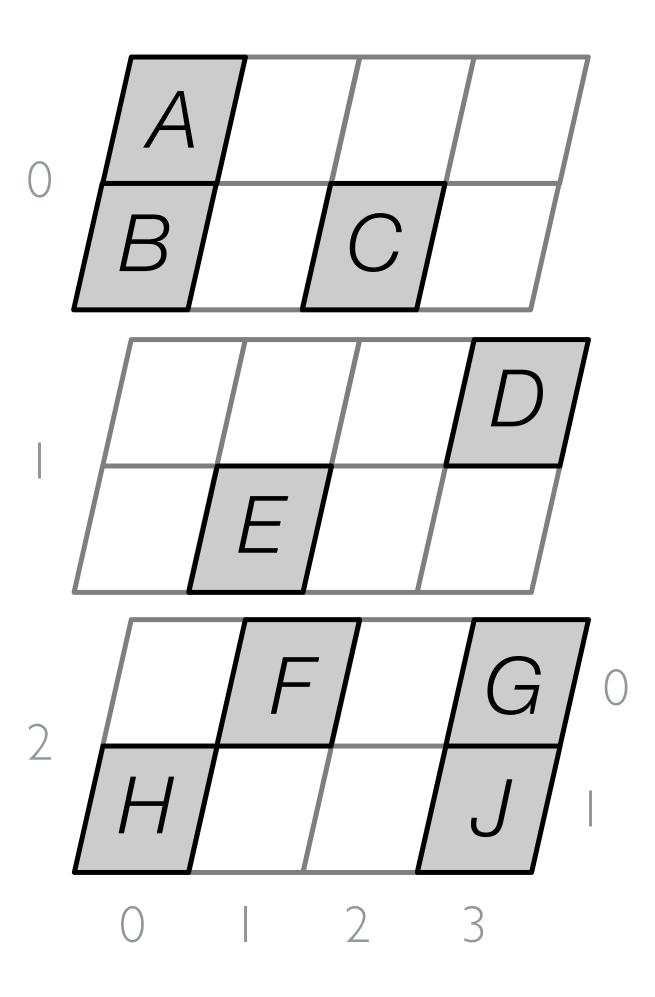




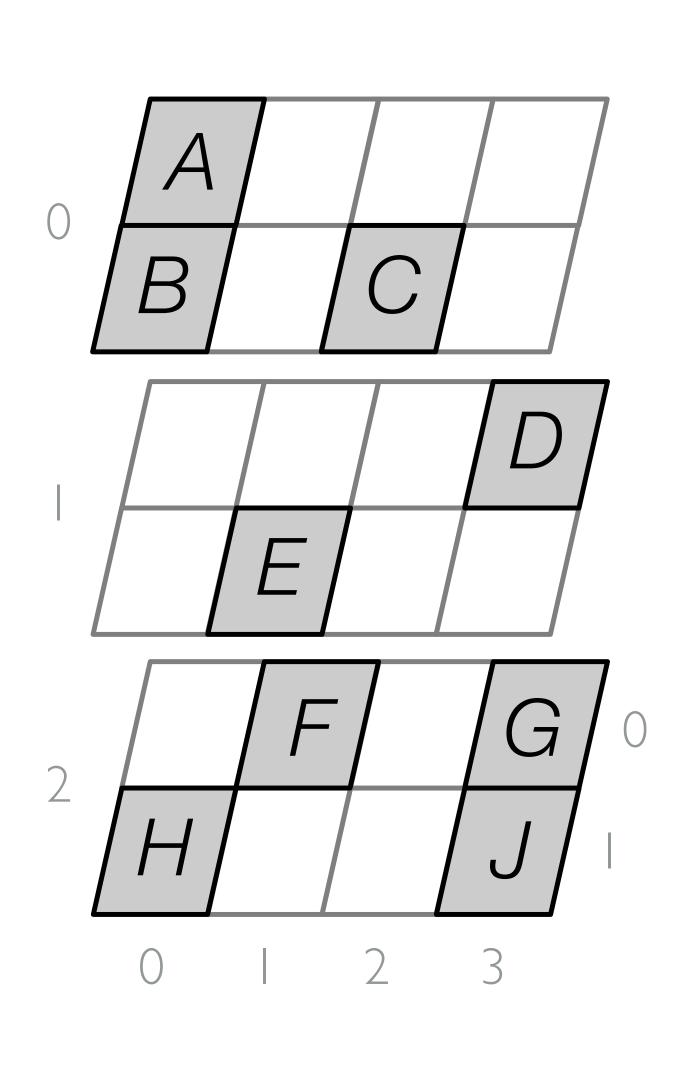






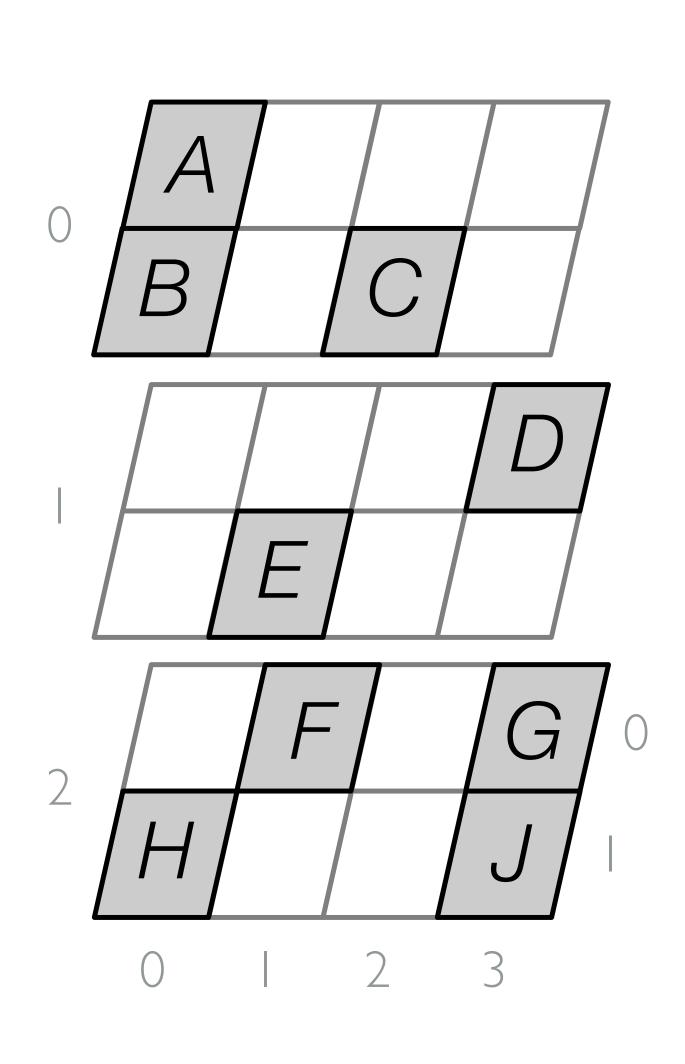


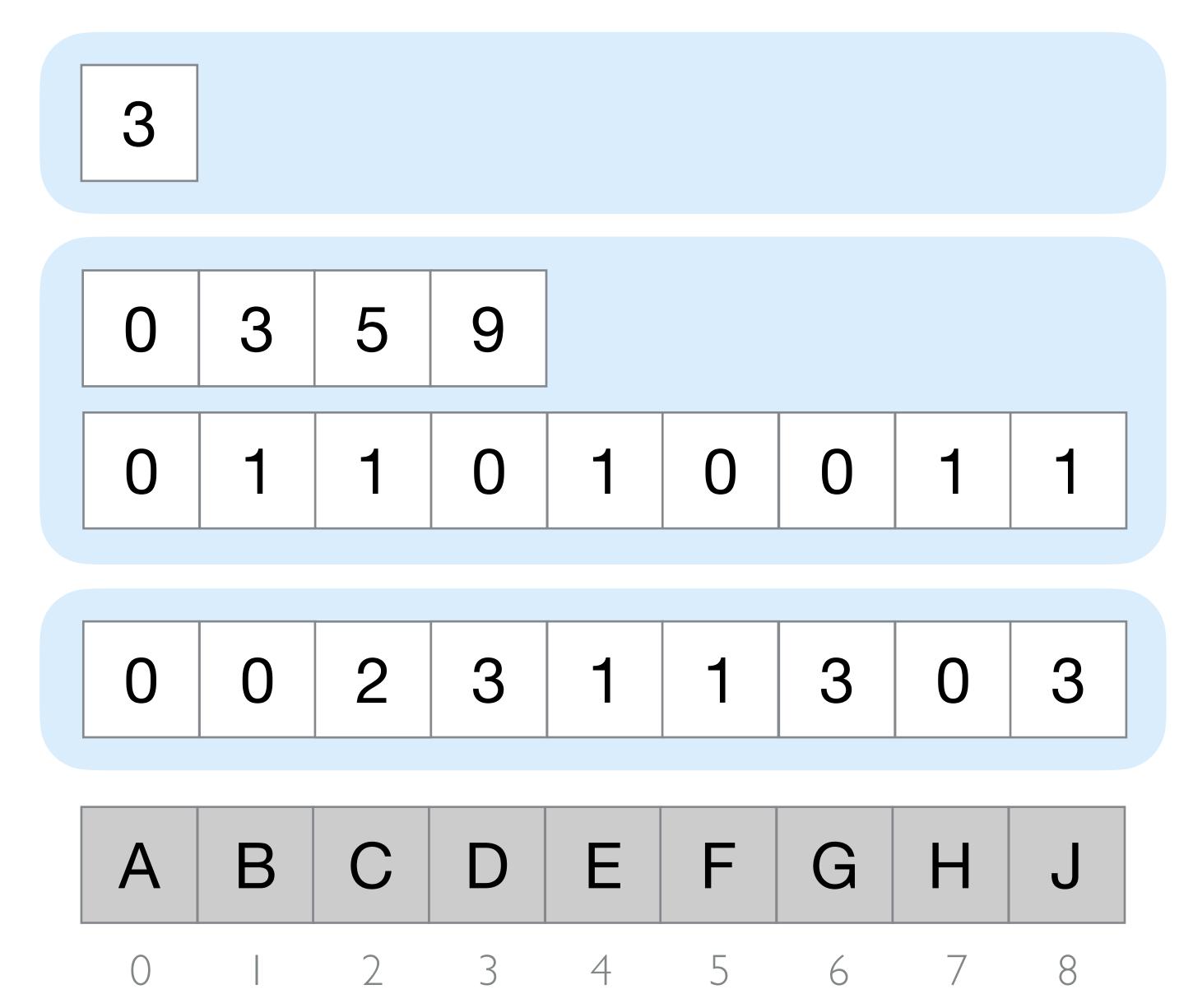
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[Kjolstad et al. OOPSLA, 2017]

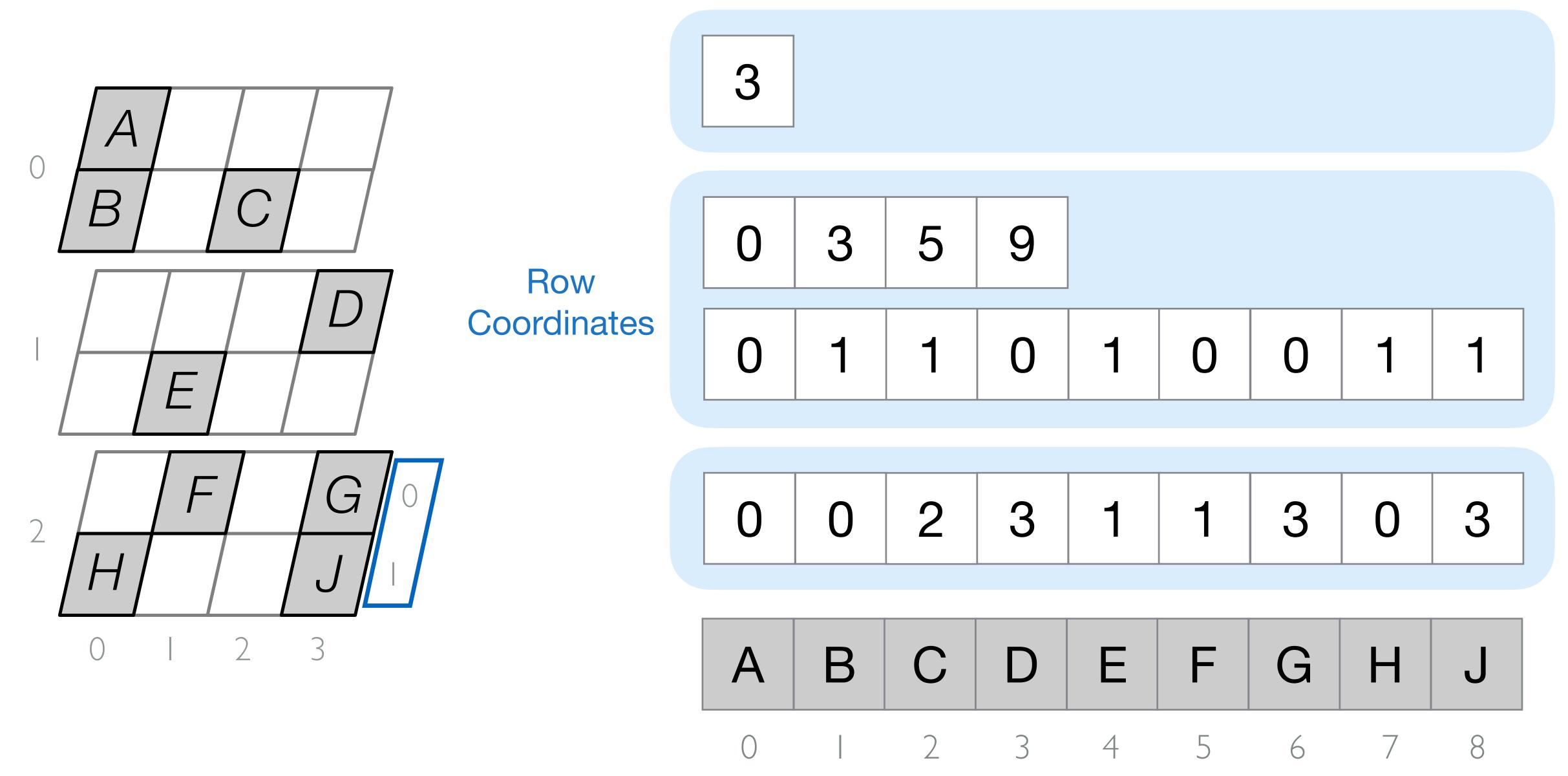




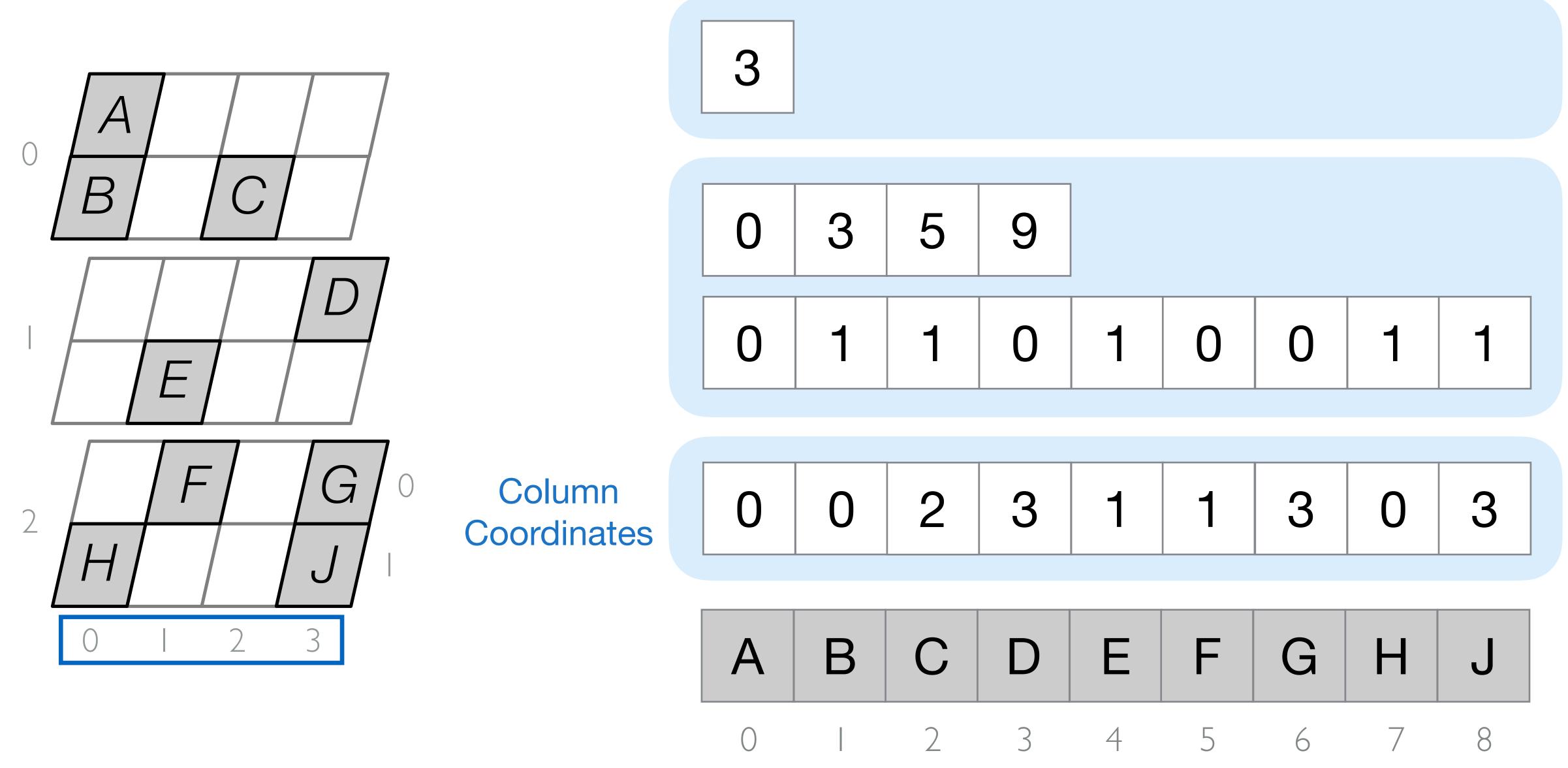
[Kjolstad et al. OOPSLA, 2017]



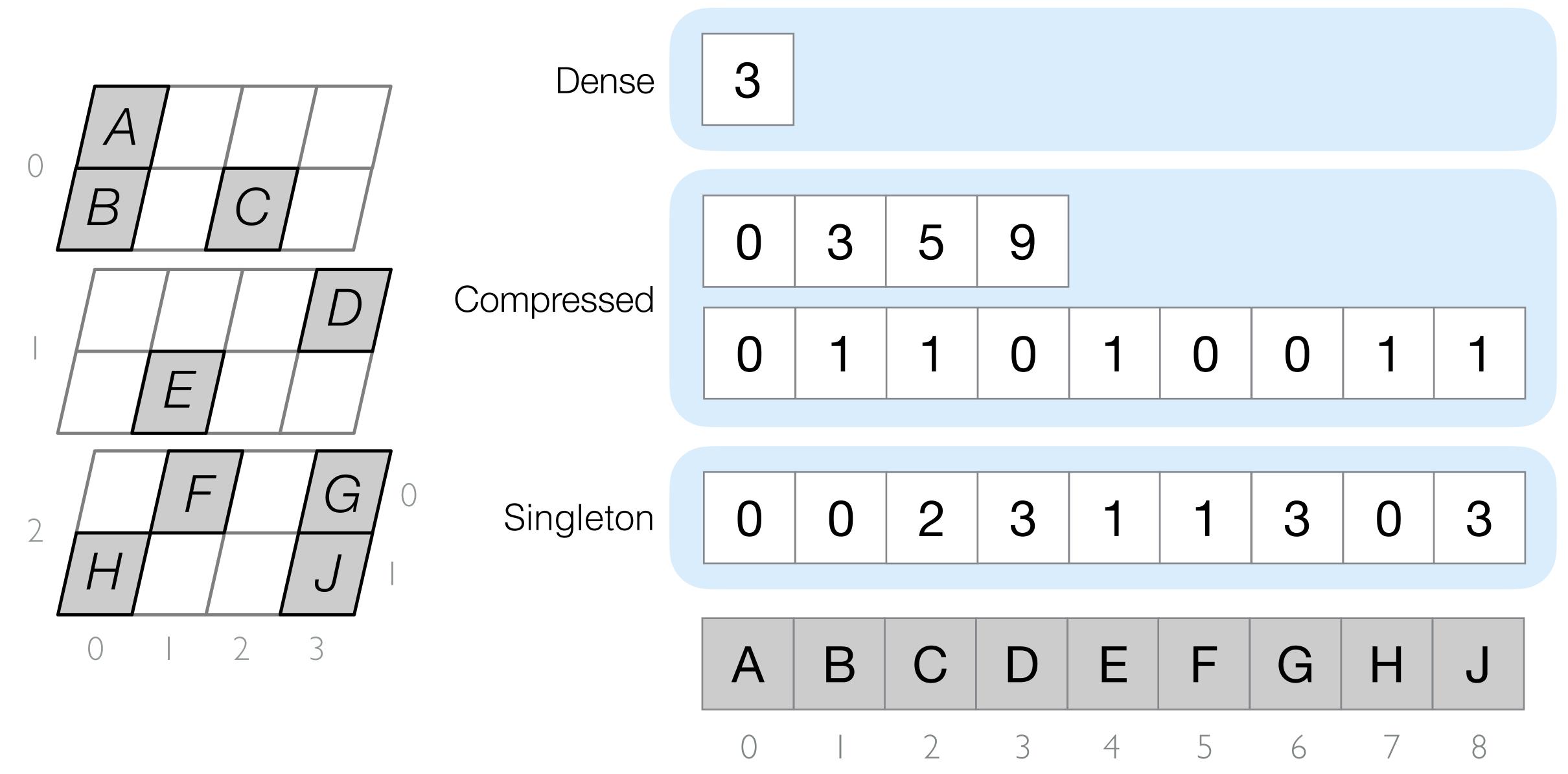
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[Kjolstad et al. OOPSLA, 2017]



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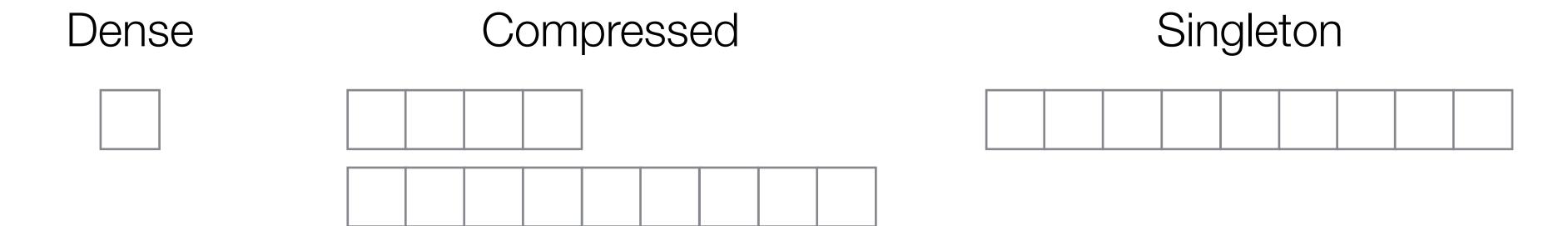


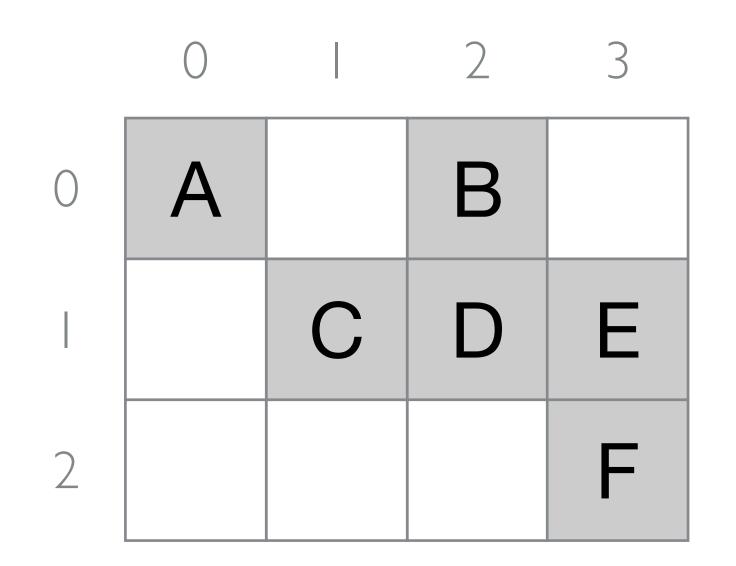
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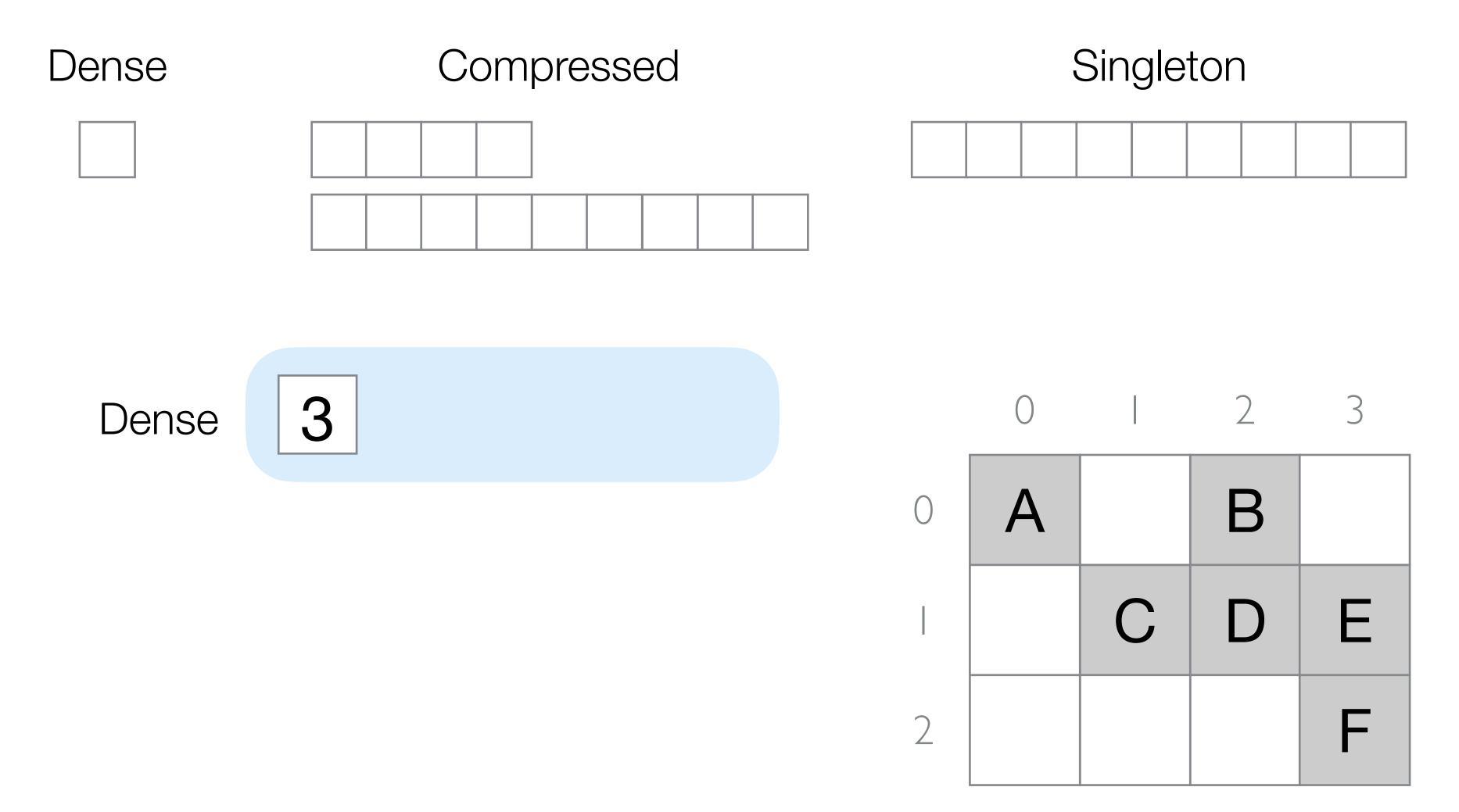
Per-dimension formats can be composed in many ways

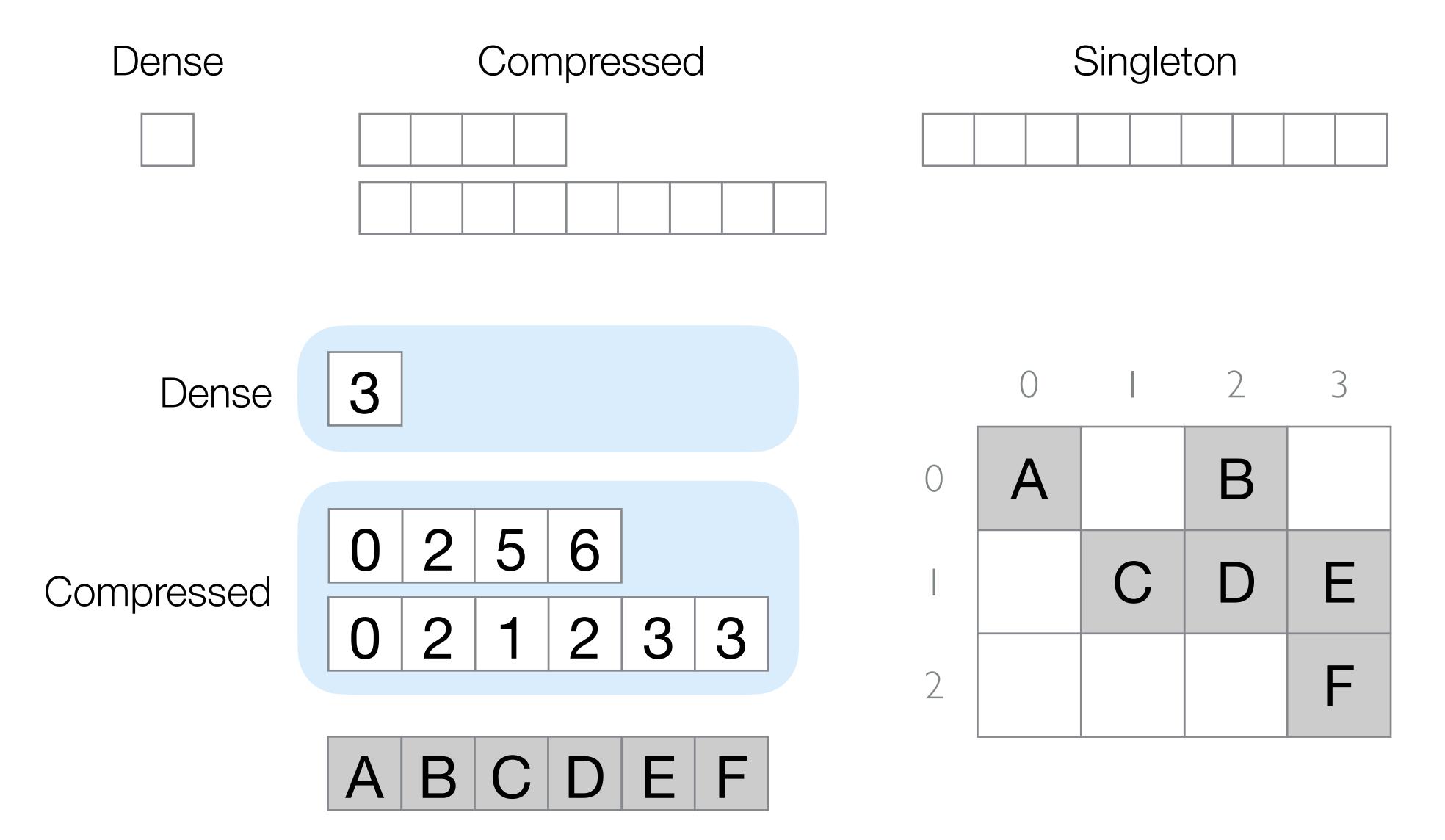
Dense	Compressed	Singleton			

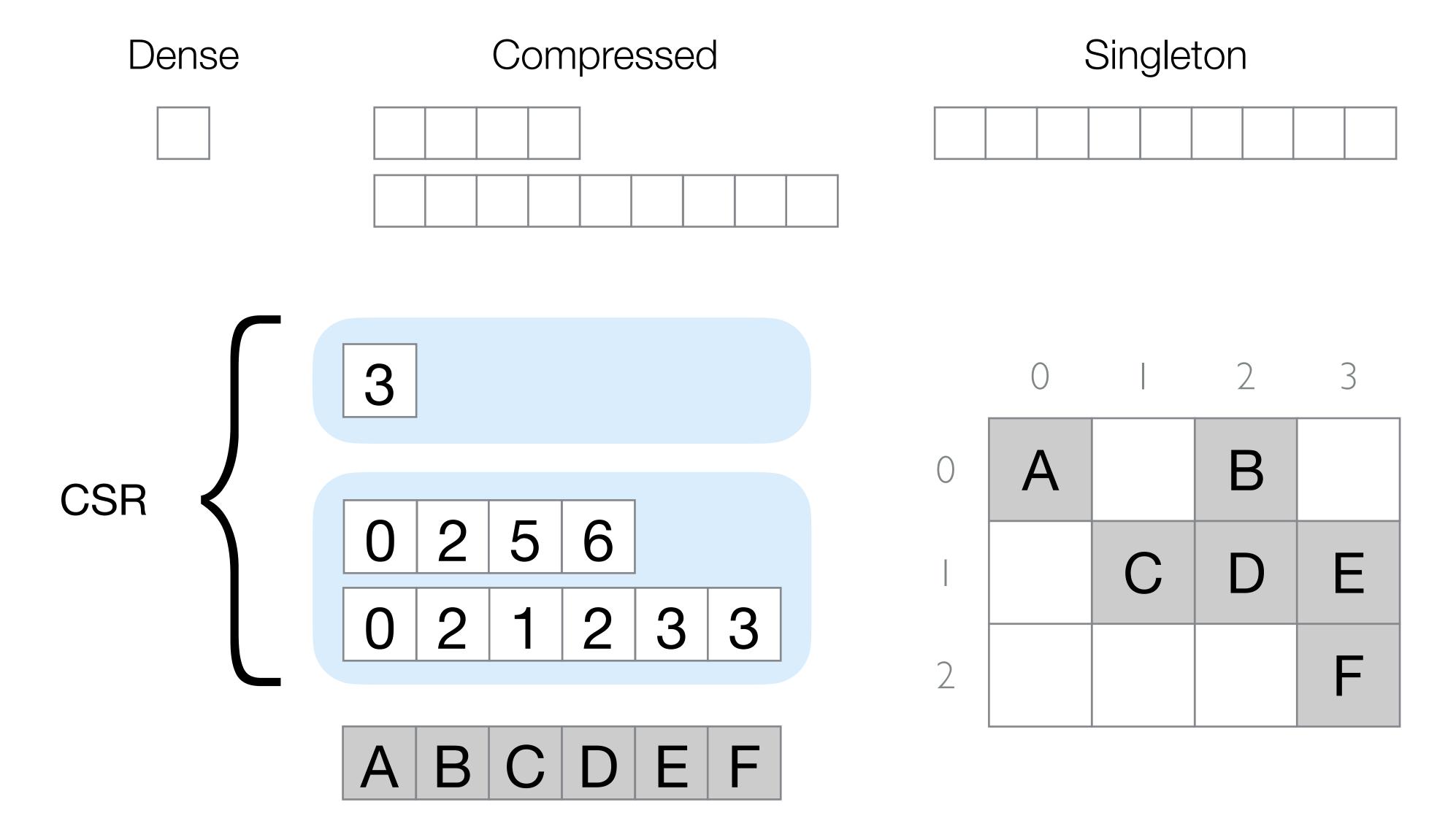
[Chou et al. OOPSLA, 2018]

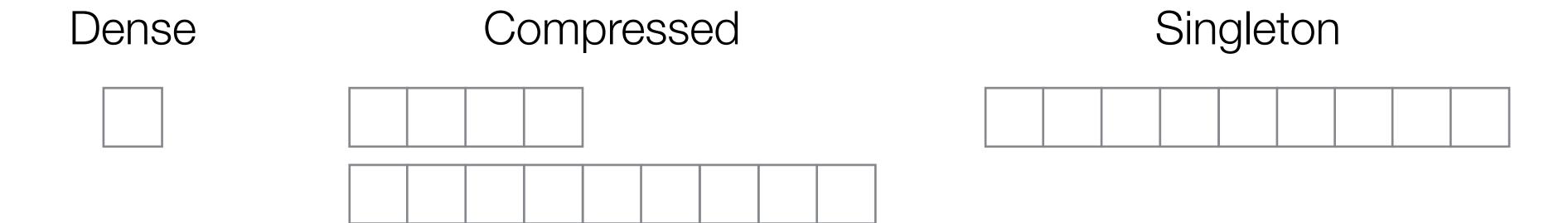


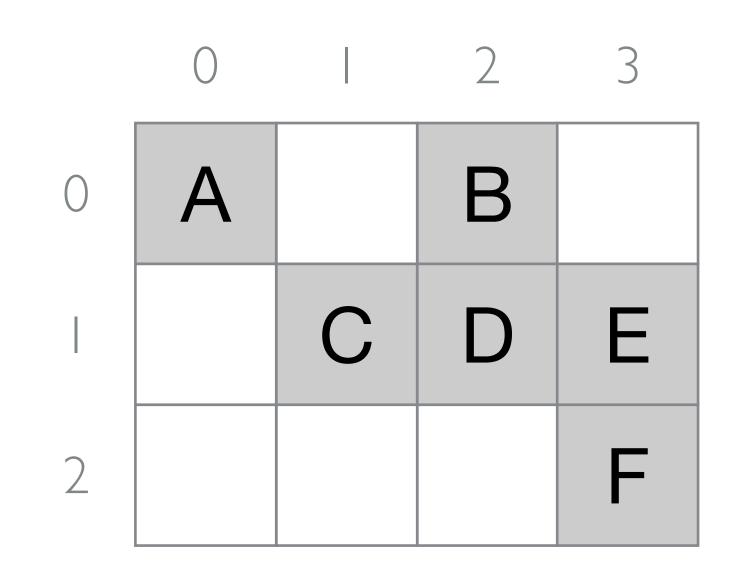


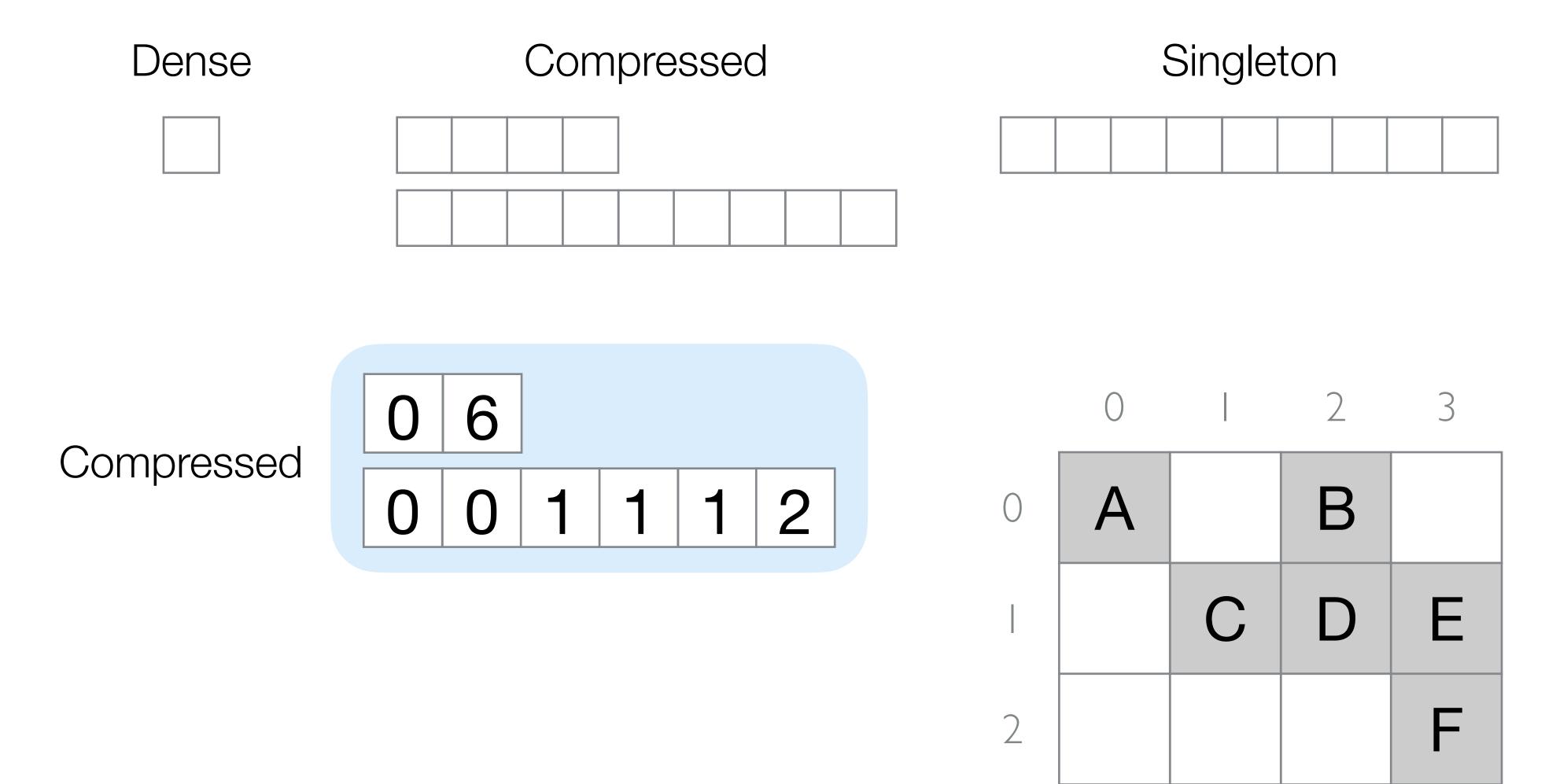


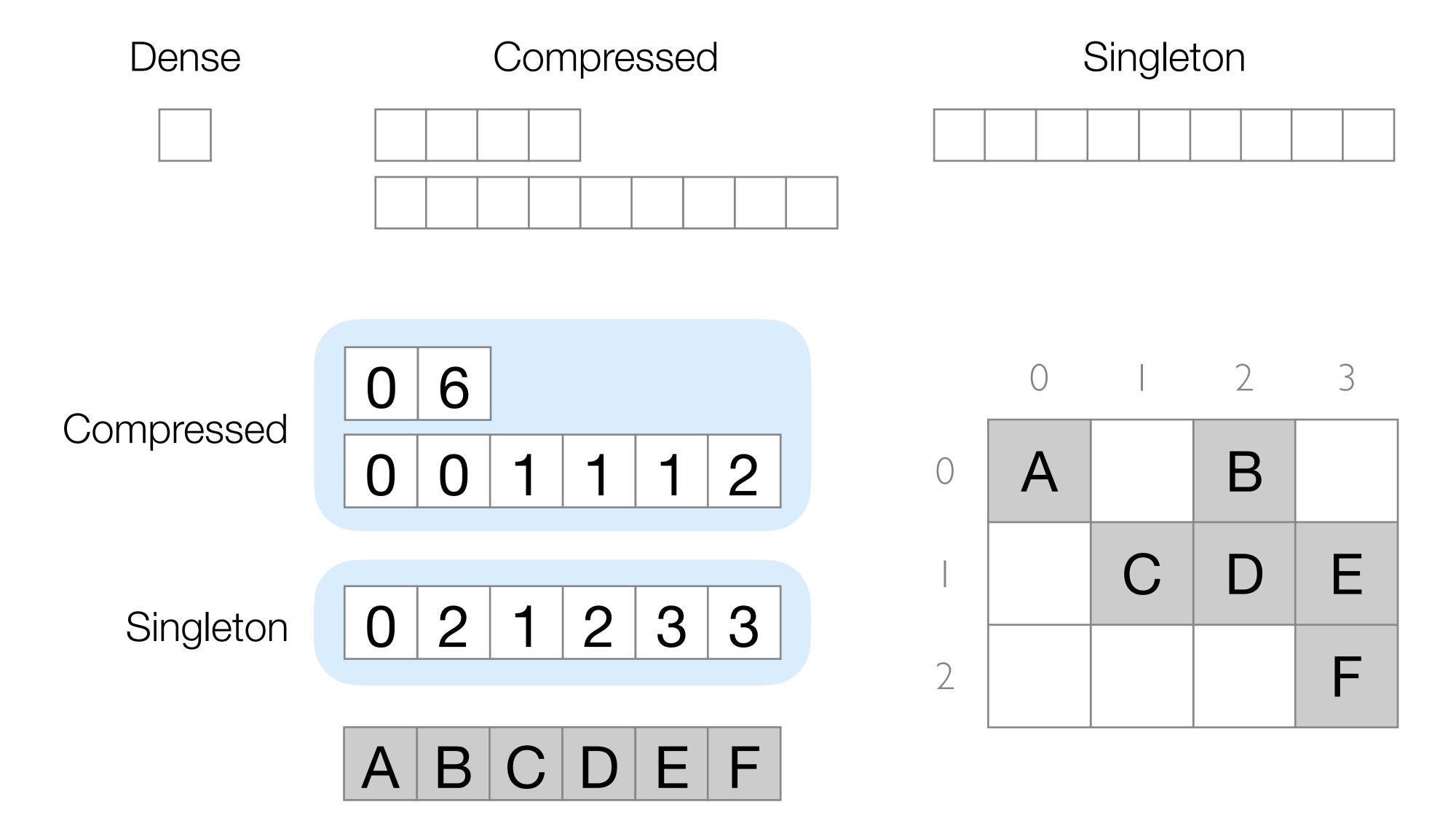


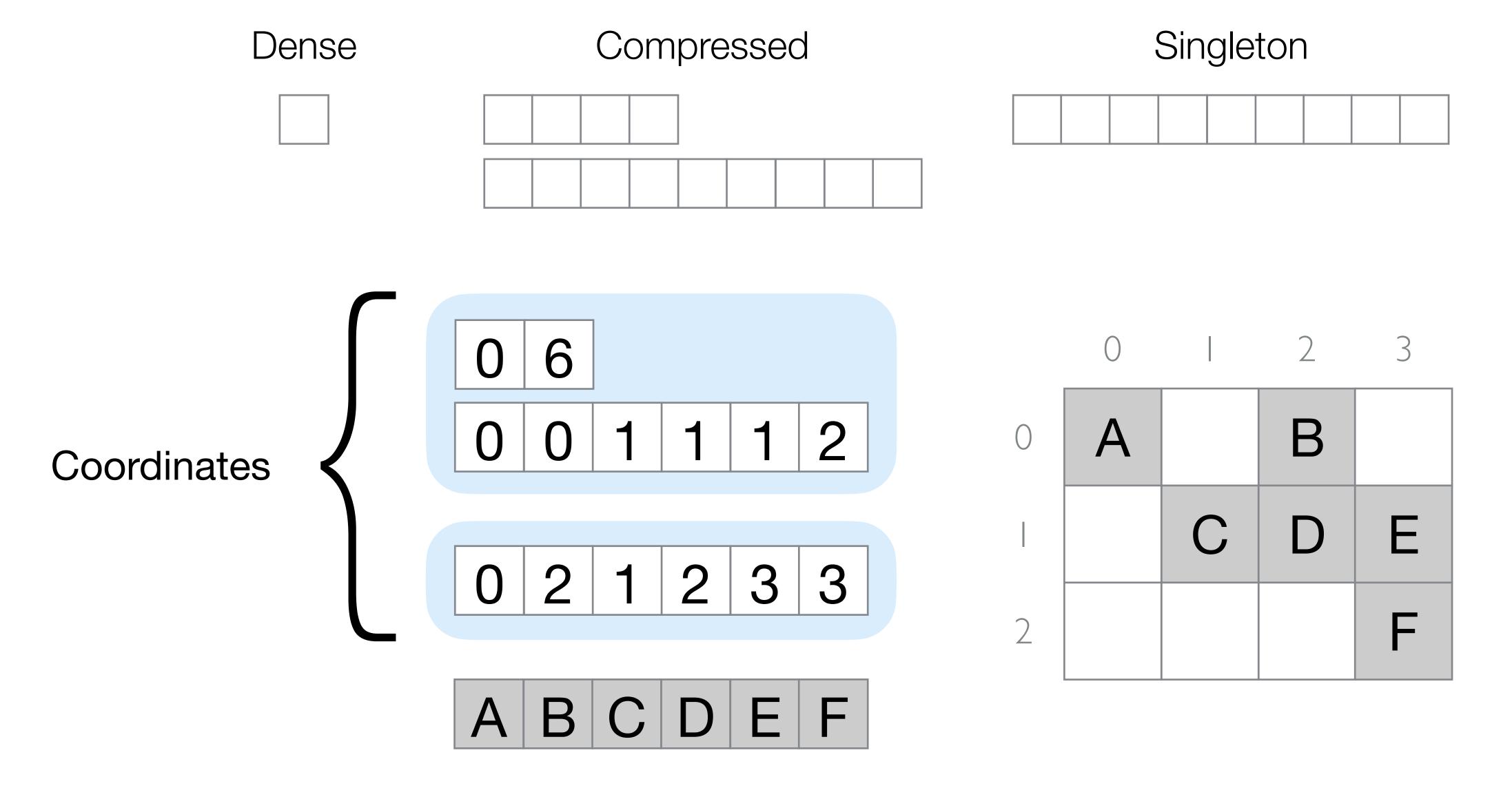












Dense Compressed Singleton

Level formats

Dense

Compressed

Singleton



CSR
Dense
Compressed
Tinney and Walker, 1967

Tensor formats

Level formats

Dense

Compressed

Singleton

	Compressed
	Singleton
,	

Coordinate matrix

CSR
Dense
Compressed

[Tinney and Walker, 1967]

Dense array tensor
Dense
Dense
Dense

Coordinate tensor
Compressed
Singleton
Singleton

Tensor formats

Mode-generic tensor
Compressed
Singleton
Dense
Dense

[Baskaran et al. 2012]

Level formats

Dense

Compressed

Singleton

Te	ns	SC	r
for	m	at	ts

Coordinate matrix	
Compressed	
Singleton	

CSR	
Dense	
Compressed	
Tinney and Walker, 1967]	

Dense array tensor
Dense
Dense
Dense

Coordinate tensor
Compressed
Singleton
Singleton

Mode-generic tensor
Compressed
Singleton
Dense
Dense
[Baskaran et al. 2012]

BCSR
Dense
Compressed
Dense
Dense
[Im and Yelick 1998]

CSB
Dense
Dense
Compressed
Singleton
[Buluç et al. 2009]

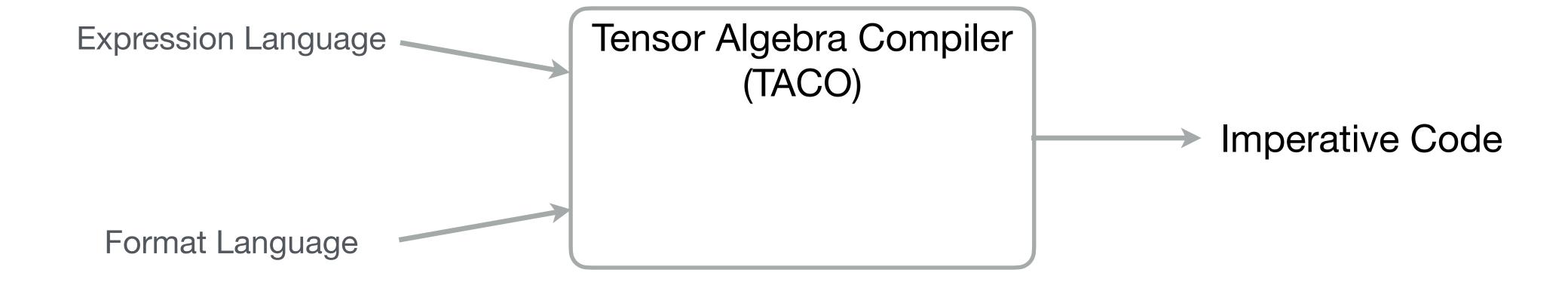
ELLPACK

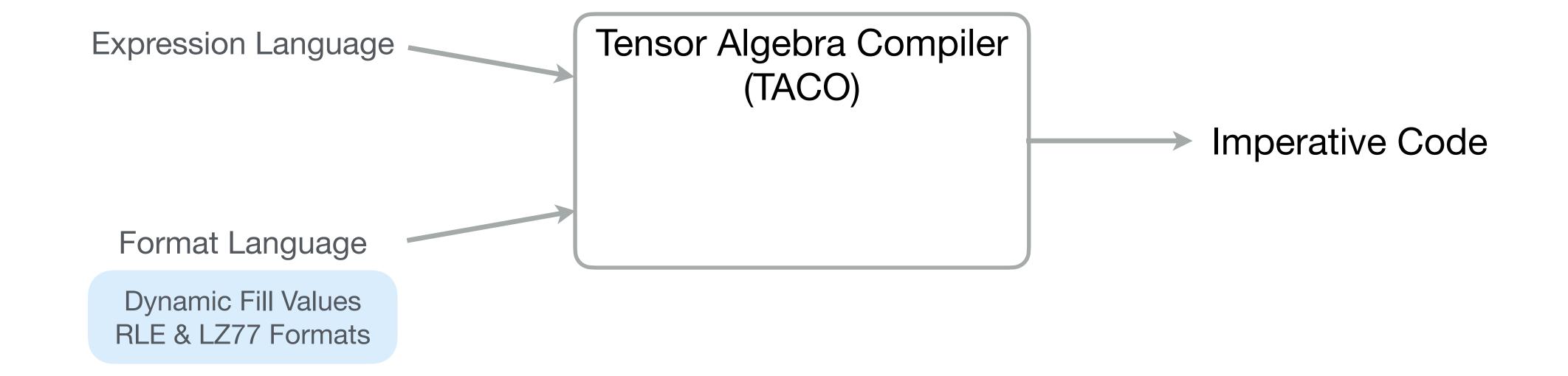
Dense

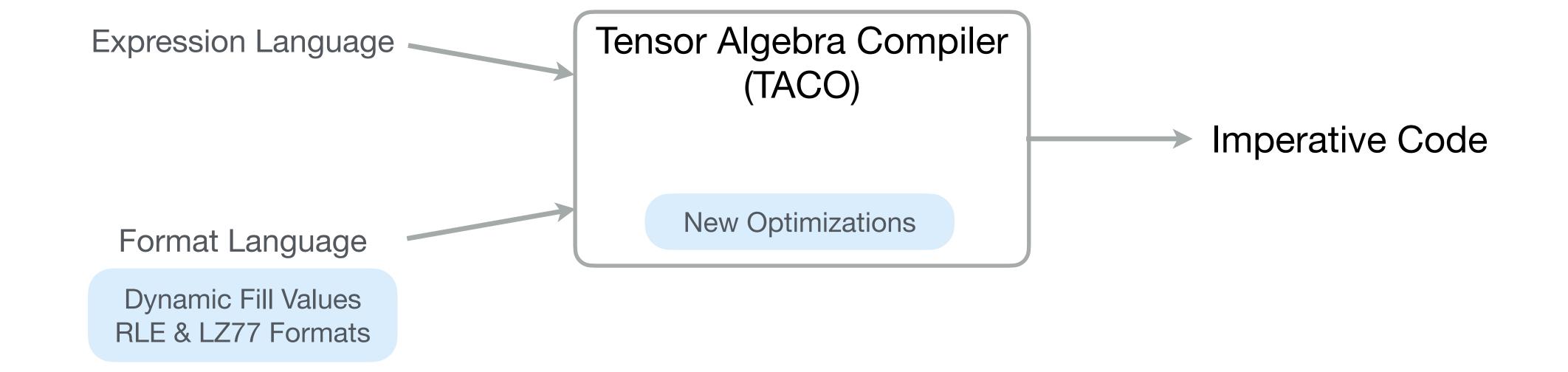
Dense

Singleton

[Kincaid et al. 1989]

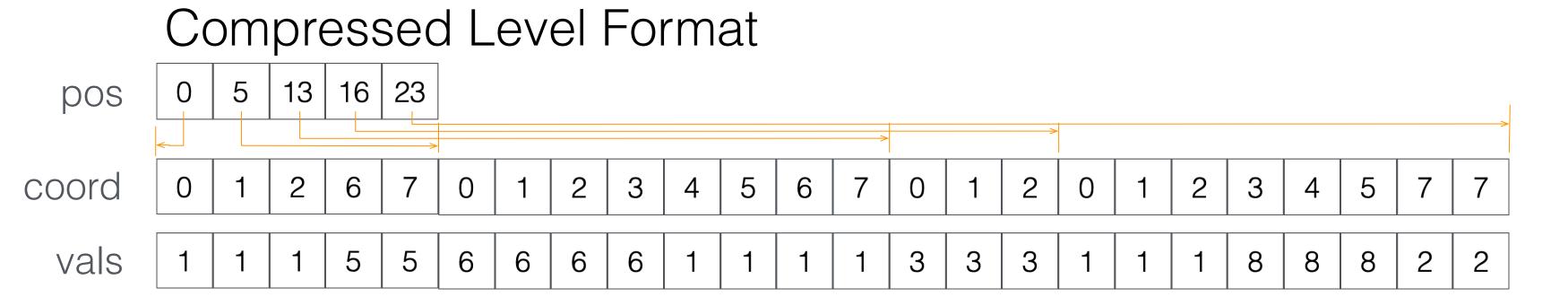




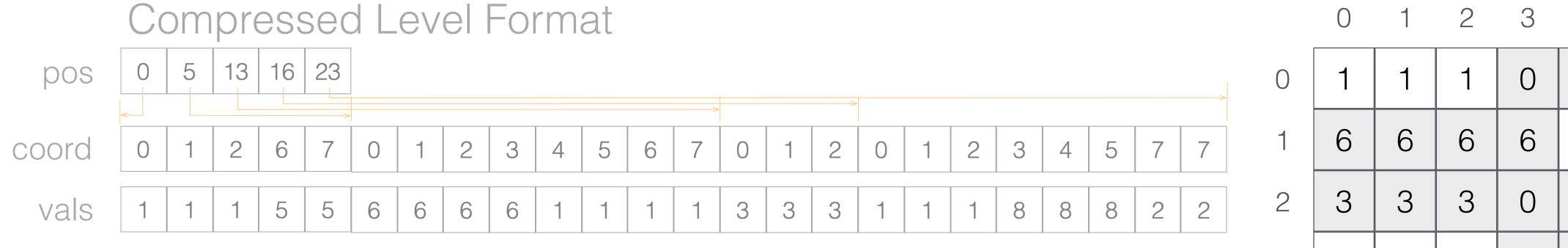


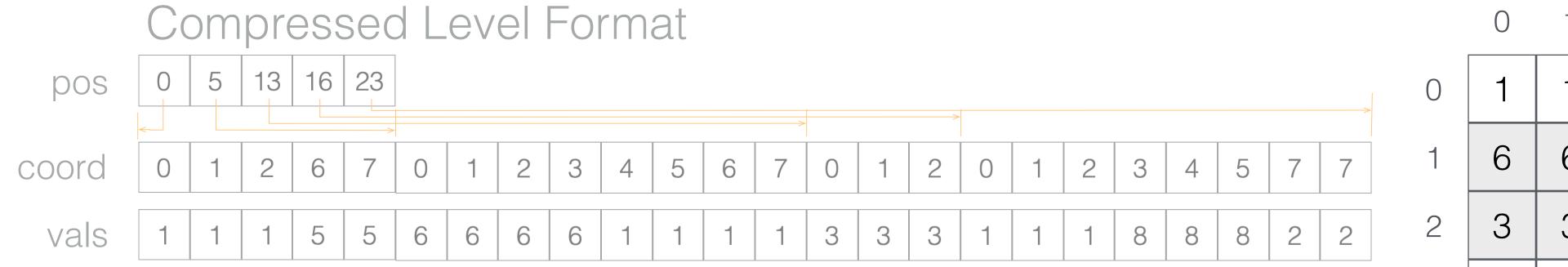
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	О	О	О	0
3	1	1	1	8	8	8	2	2

	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	О	О	О	0
3	1	1	1	8	8	8	2	2

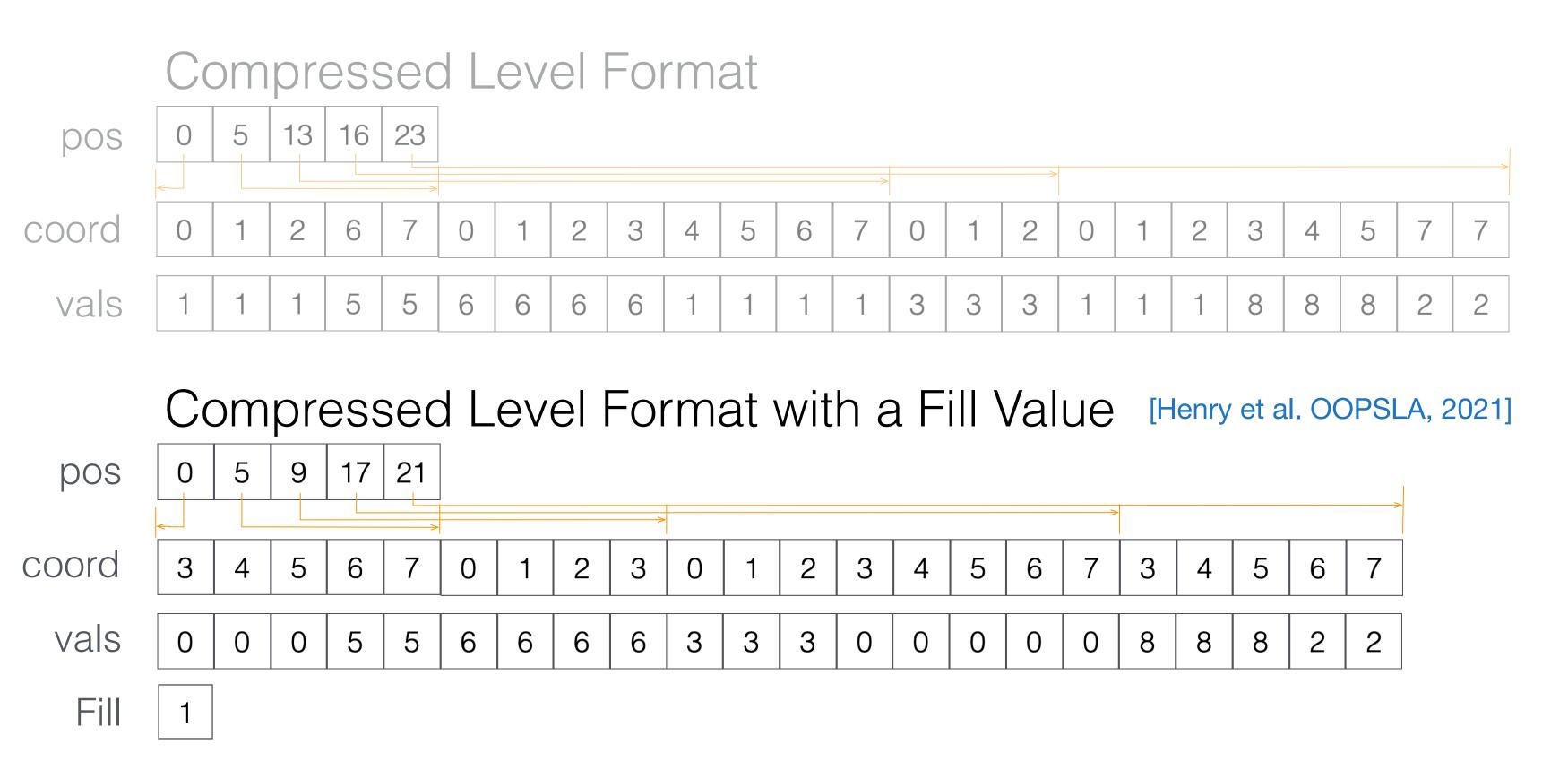


	O	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	О	О	О	0
3	1	1	1	8	8	8	2	2

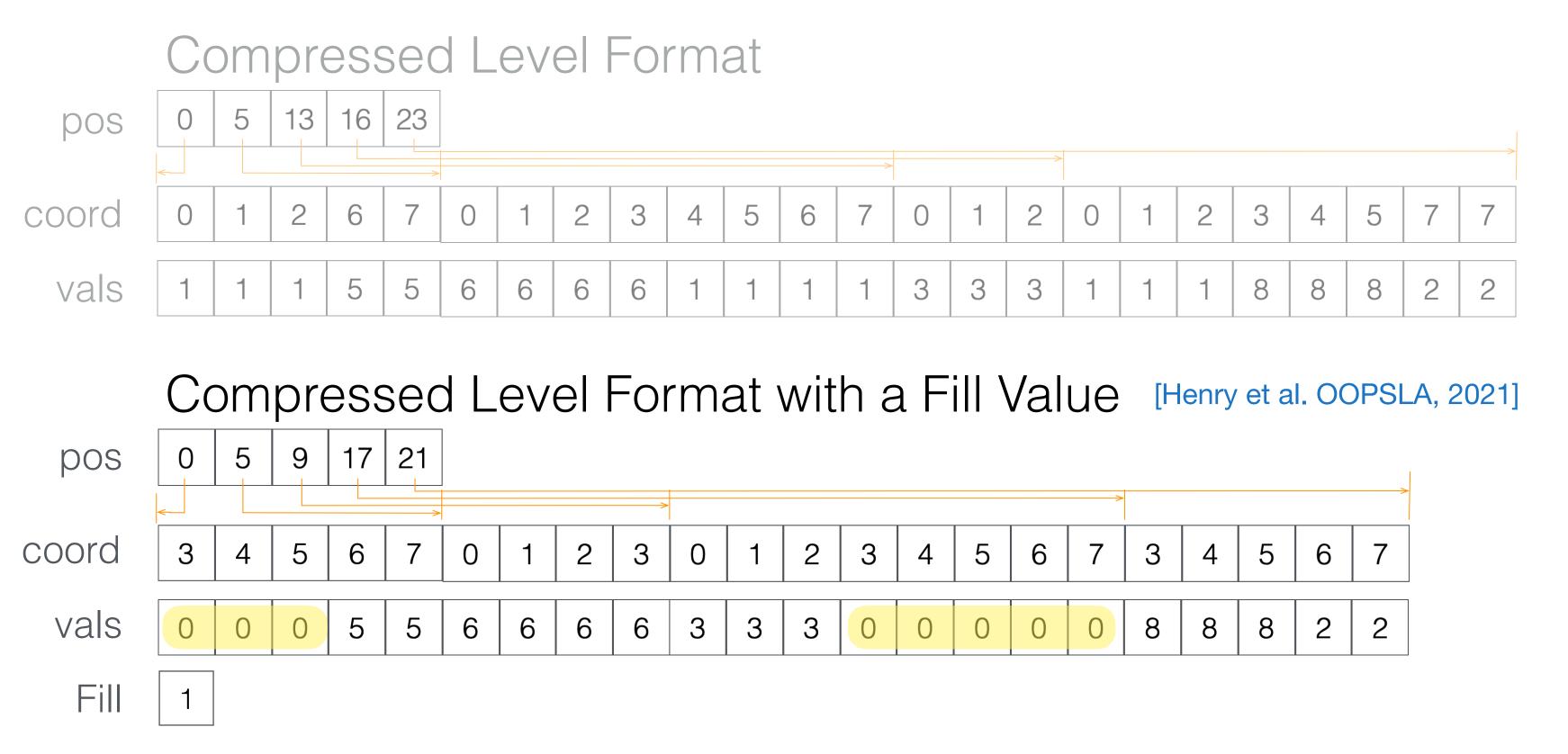




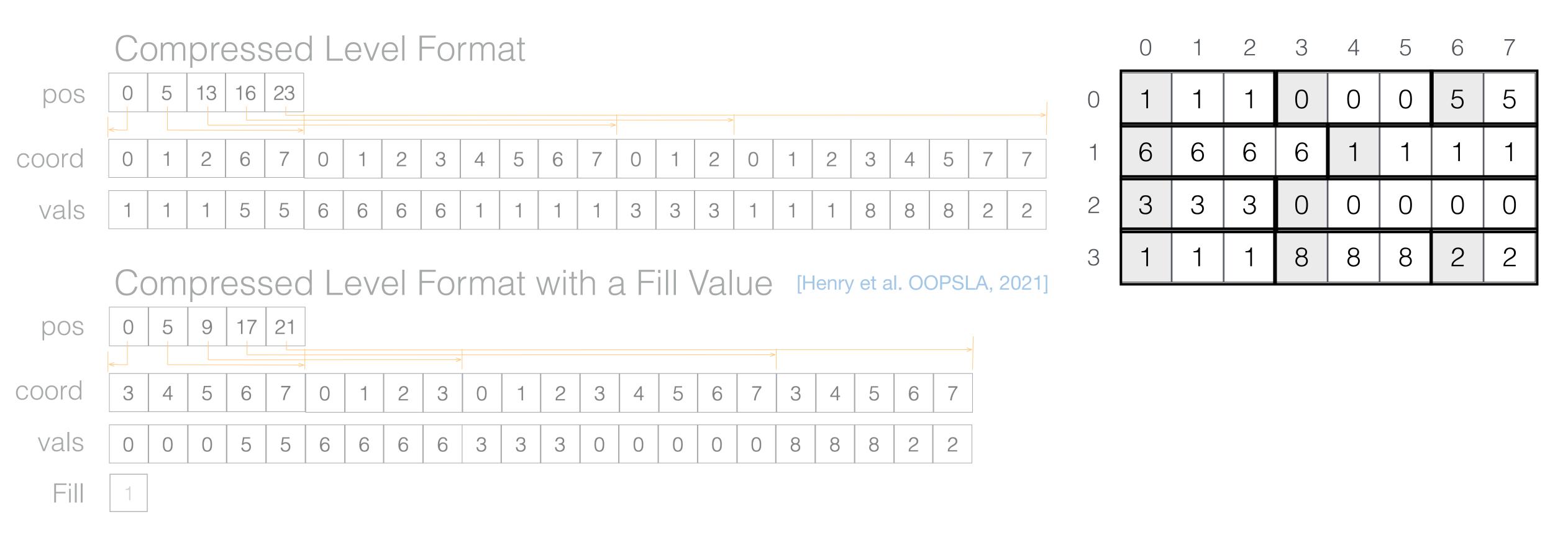
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2



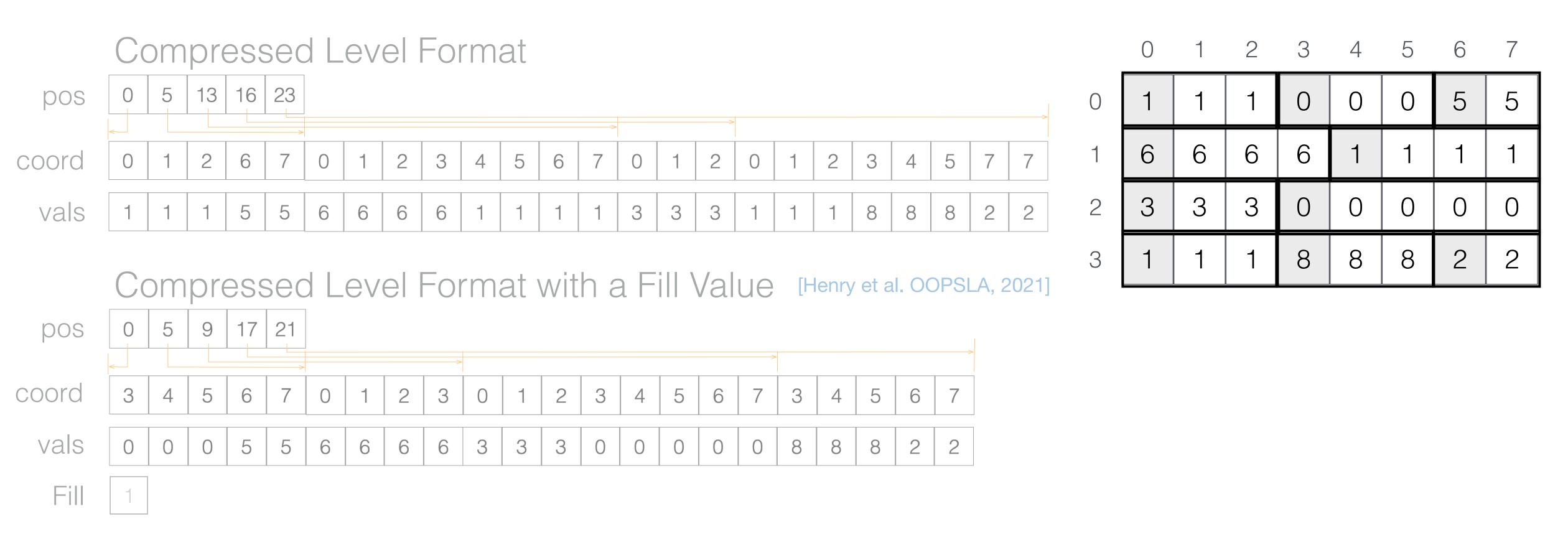
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	О	0	0	0
3	1	1	1	8	8	8	2	2



	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2

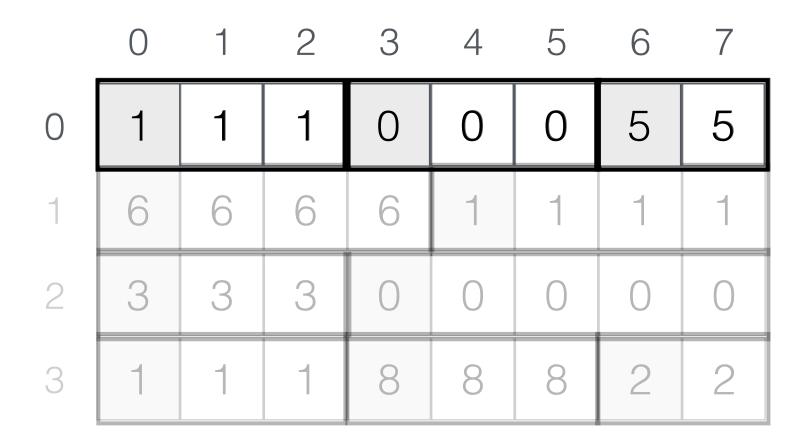


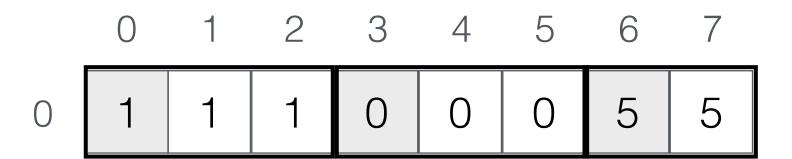
Why are we limited to a single repeated value?

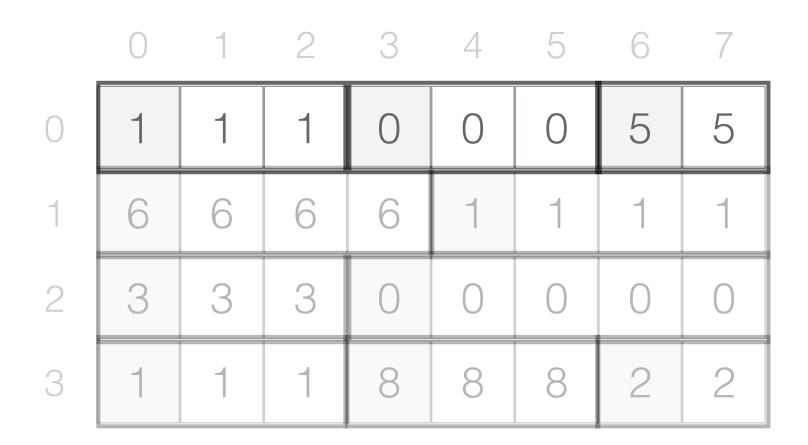


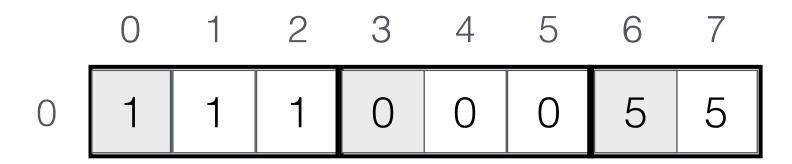
Why are we limited to a single repeated value?

Run Length Encoding (RLE)

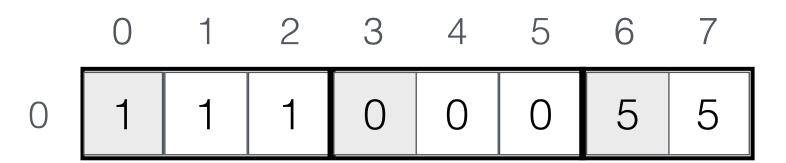






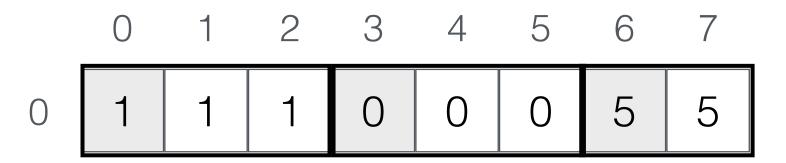


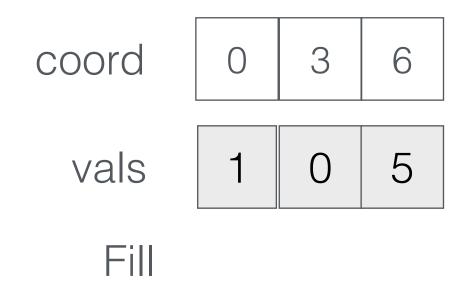
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2



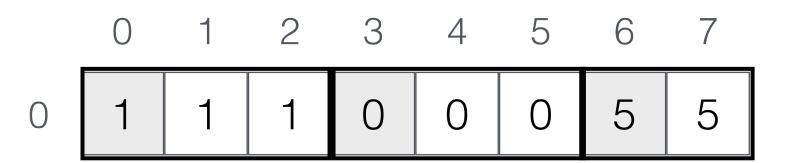


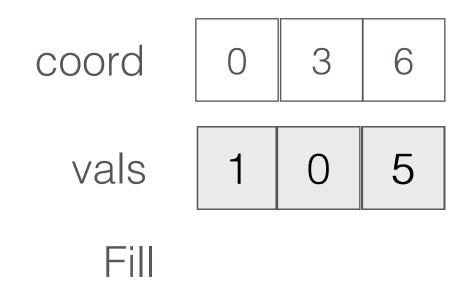
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2





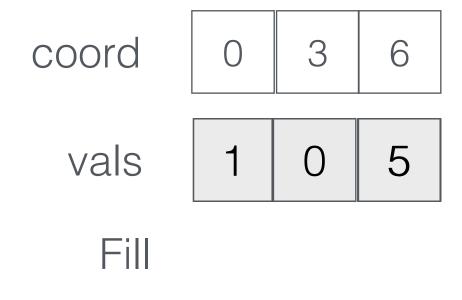
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2





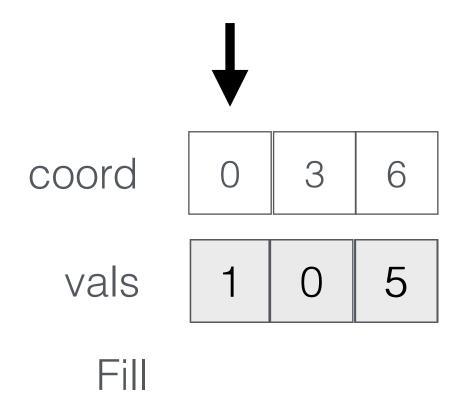
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2





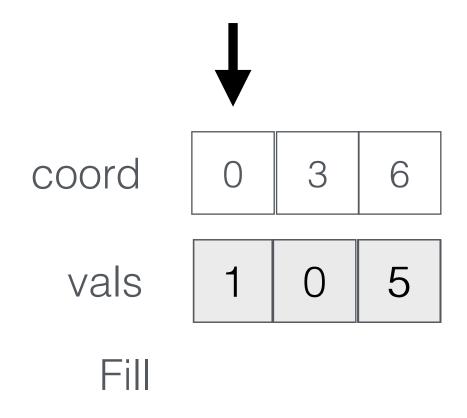
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2





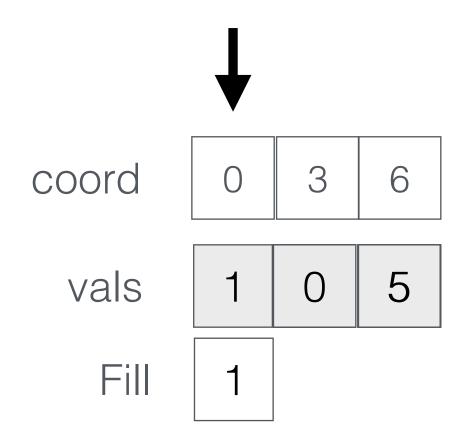
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2





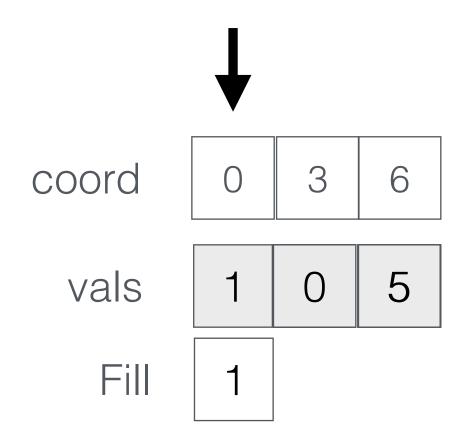
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2





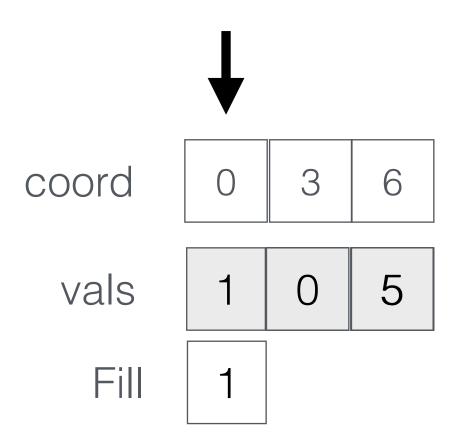
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2



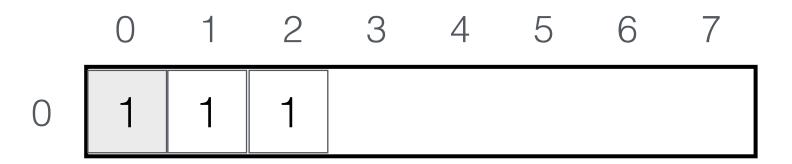


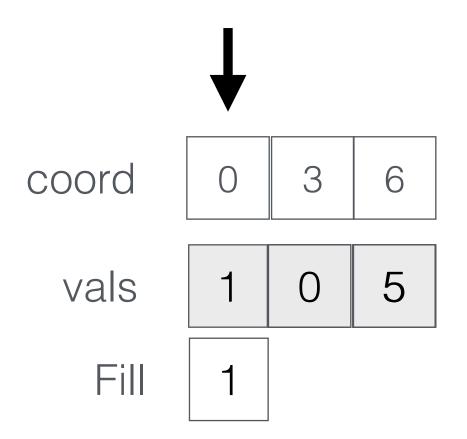
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2



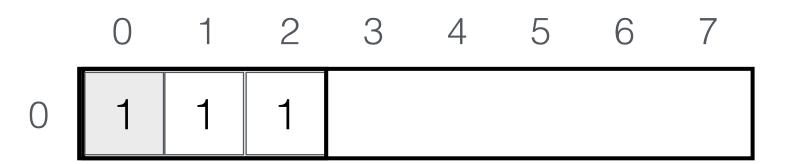


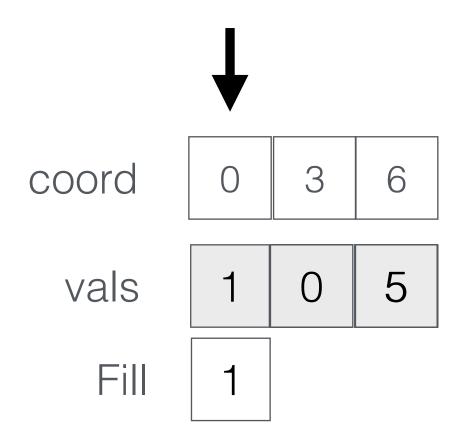
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2



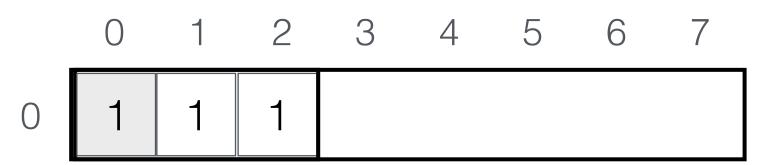


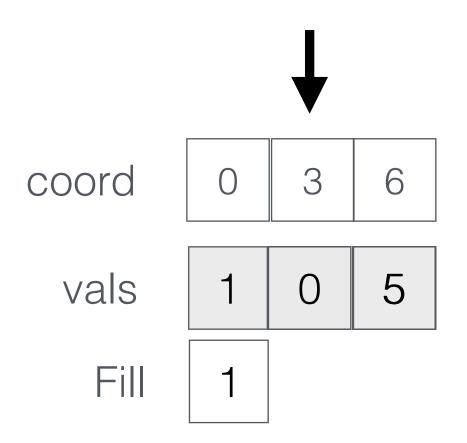
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2



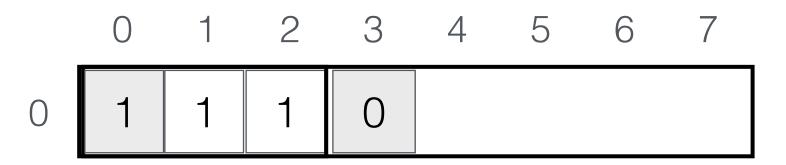


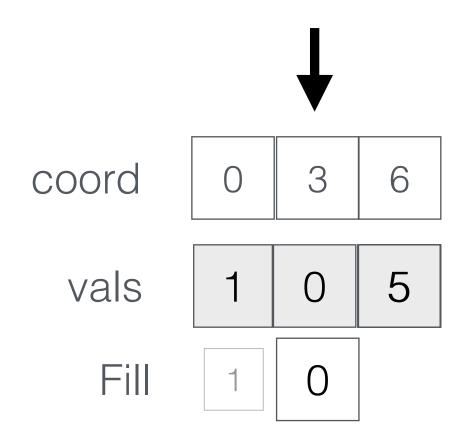
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
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3	1	1	1	8	8	8	2	2



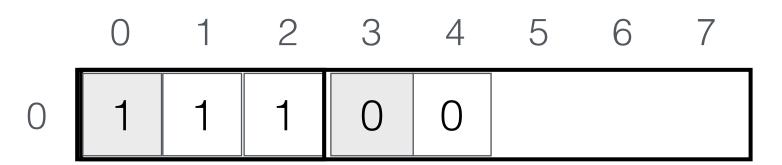


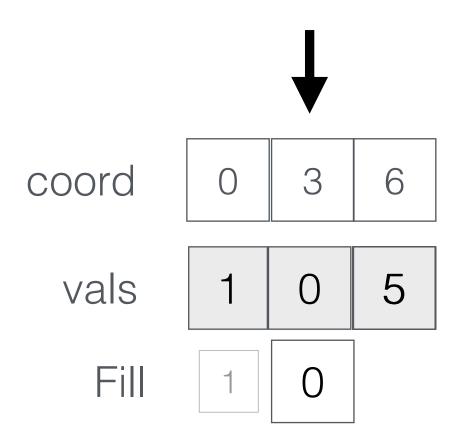
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2



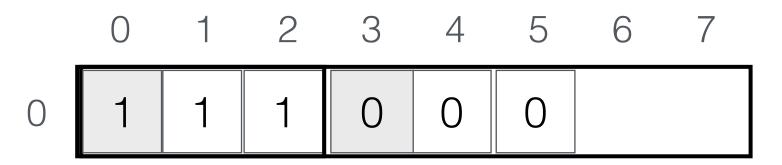


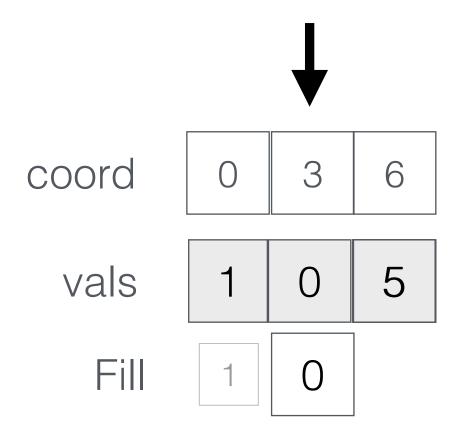
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
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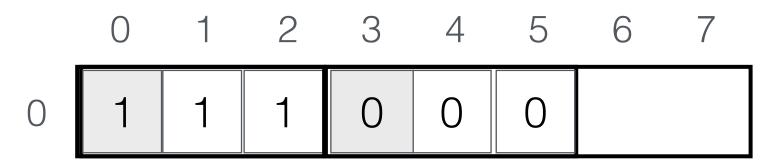


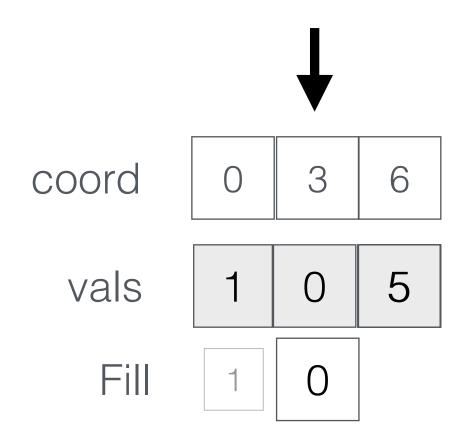
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2



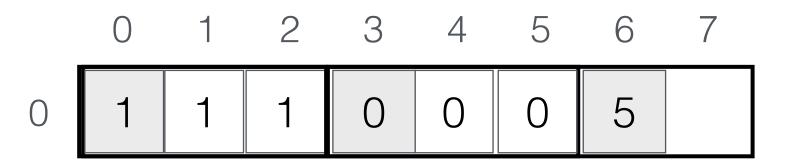


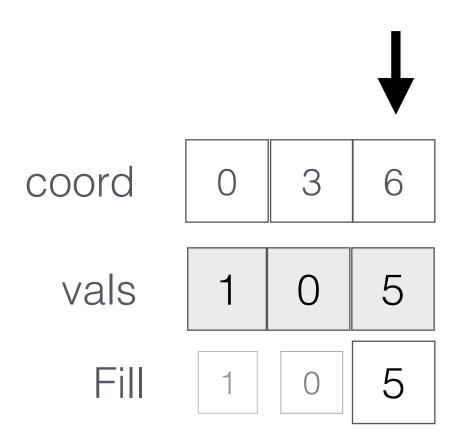
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
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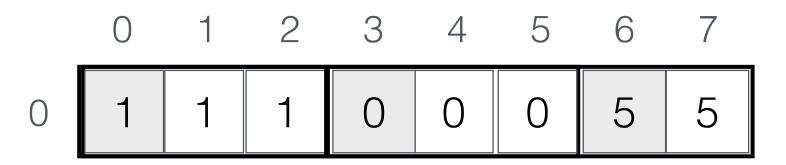


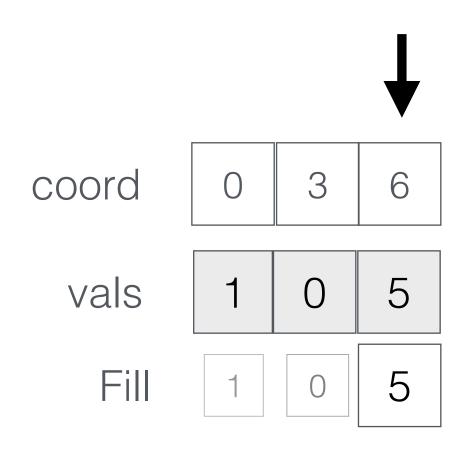
	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2





	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2





	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
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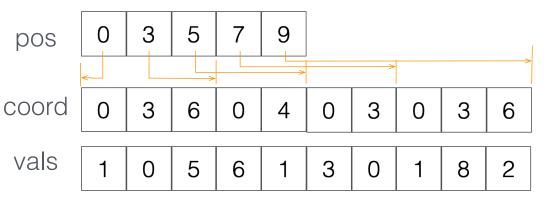
-	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	О	0	0
3	1	1	1	8	8	8	2	2

	0	1	2	3	4	5	6	7
0	1	1	1	0	0	0	5	5
1	6	6	6	6	1	1	1	1
2	3	3	3	0	0	0	0	0
3	1	1	1	8	8	8	2	2

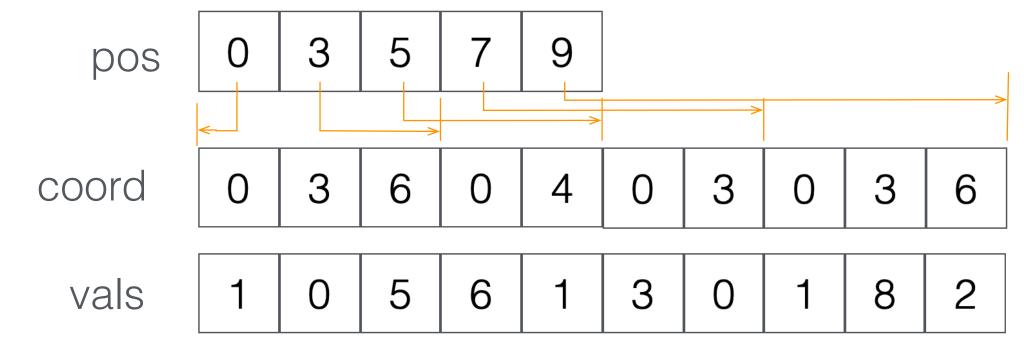
Compressed Level Format



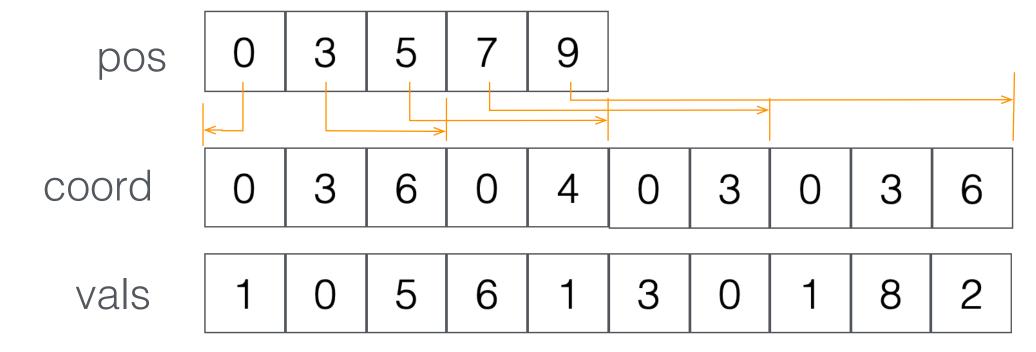
Run Length Encoding (RLE) Level Format



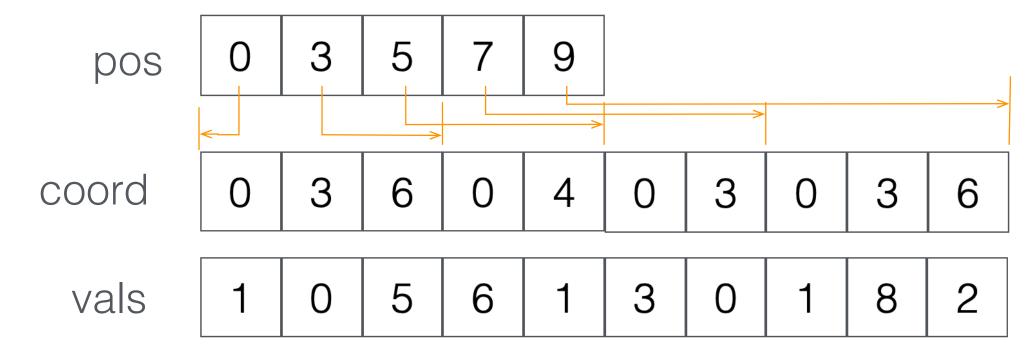
Run Length Encoding (RLE) Level Format





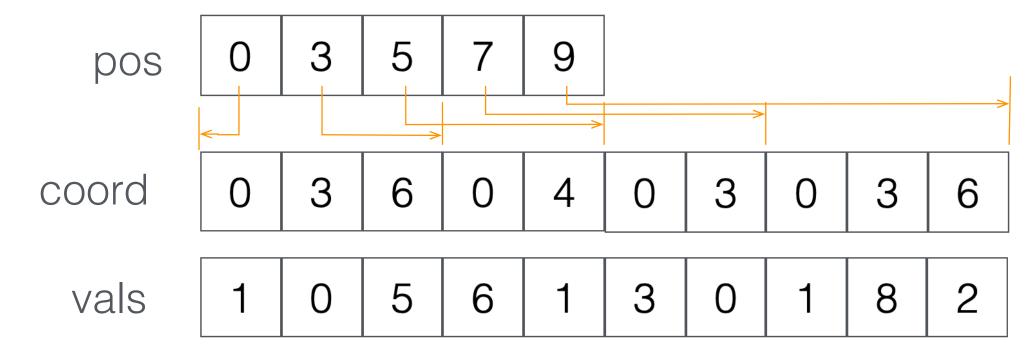


Run Length Encoding (RLE) Level Format



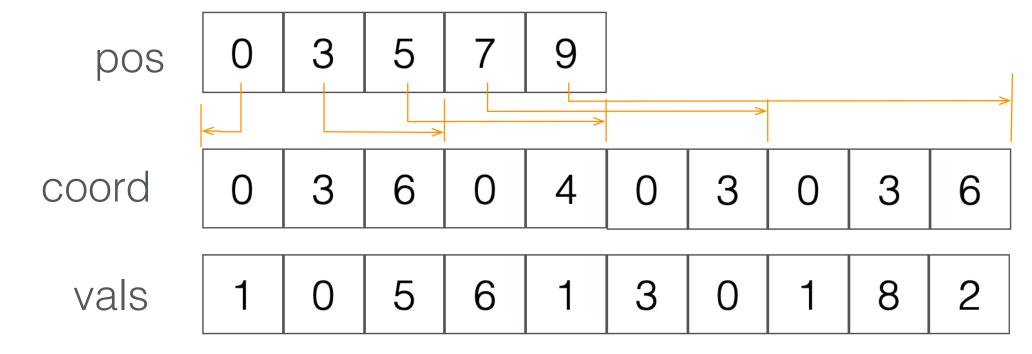


Run Length Encoding (RLE) Level Format





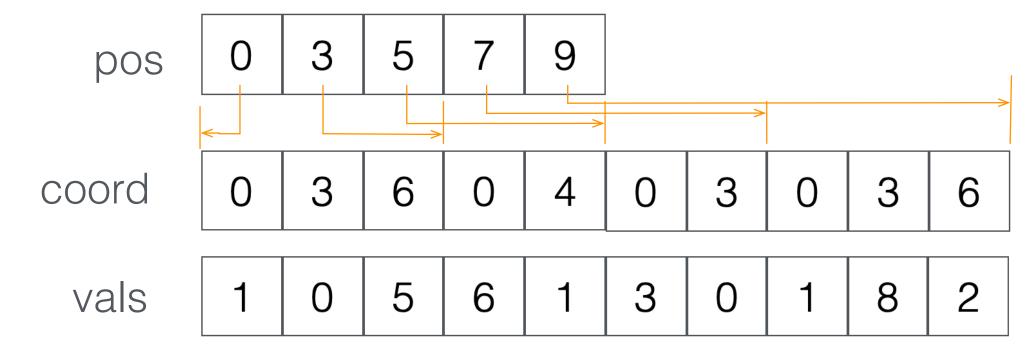
Run Length Encoding (RLE) Level Format



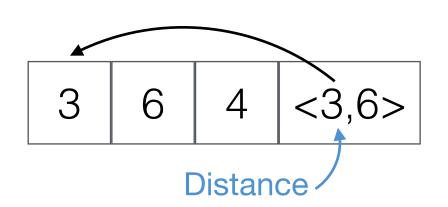




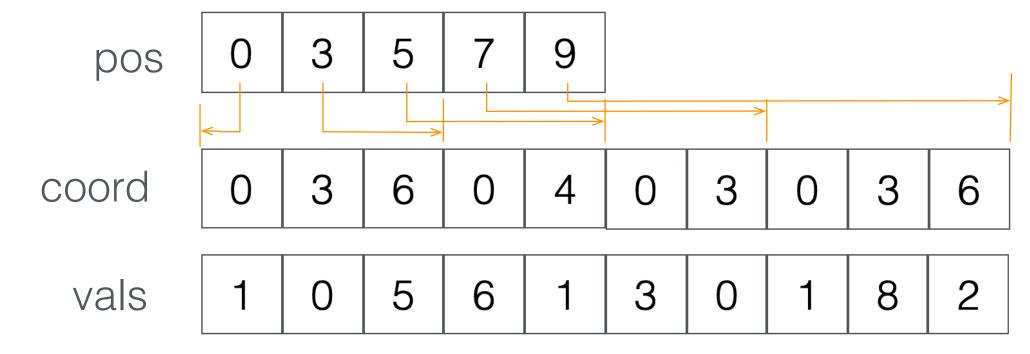
Run Length Encoding (RLE) Level Format

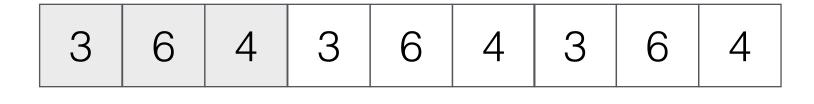


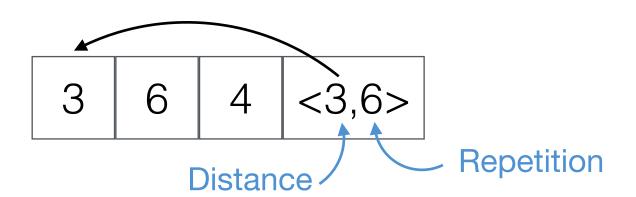




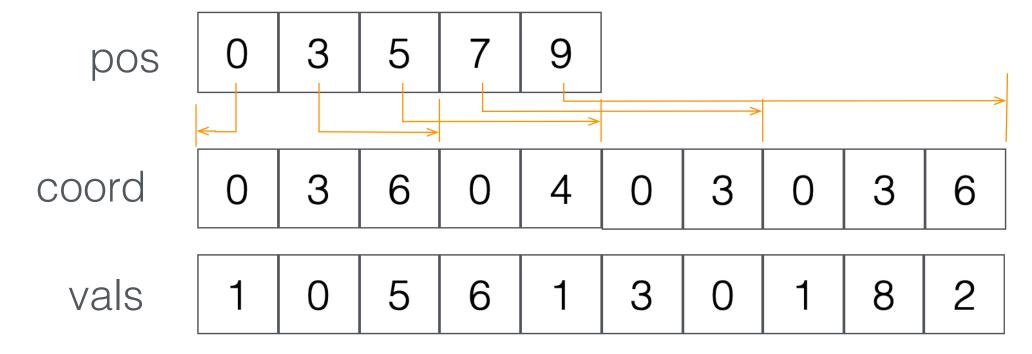
Run Length Encoding (RLE) Level Format

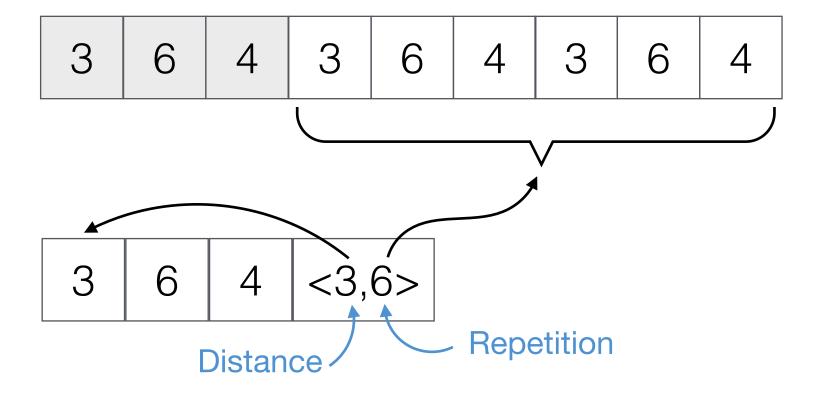




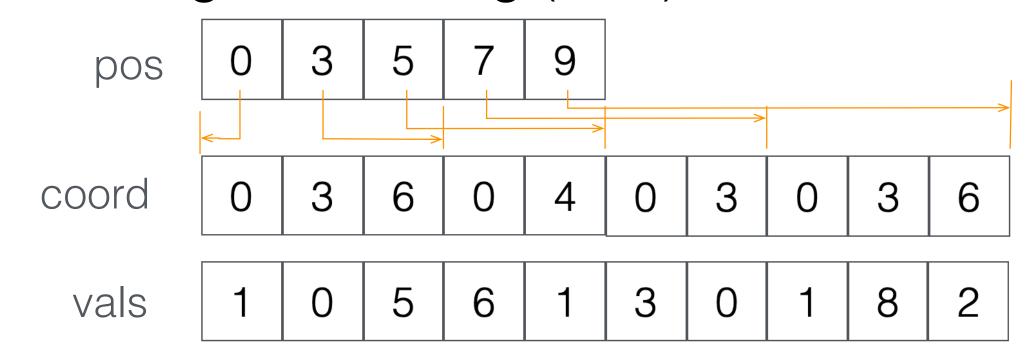


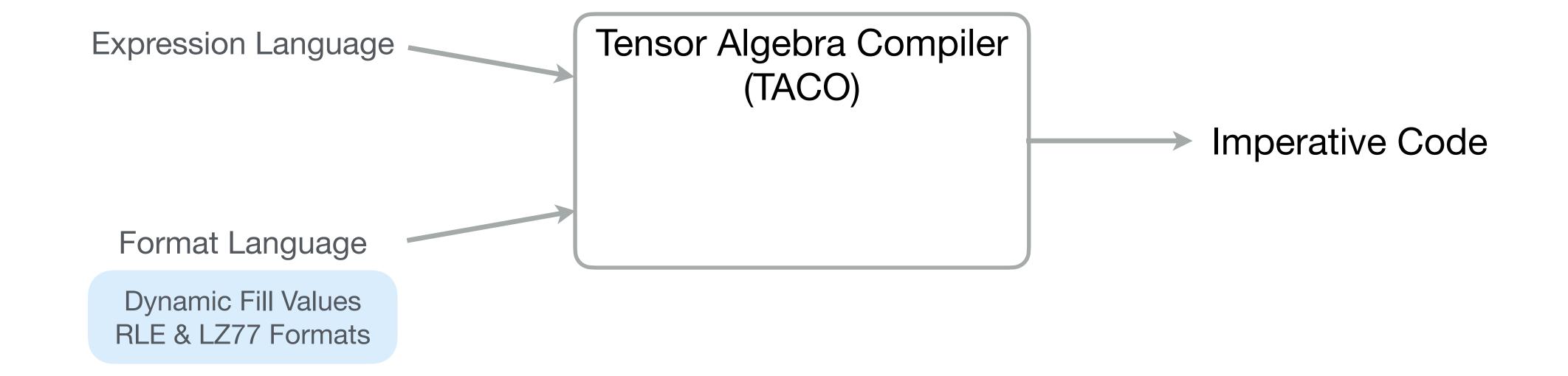
Run Length Encoding (RLE) Level Format

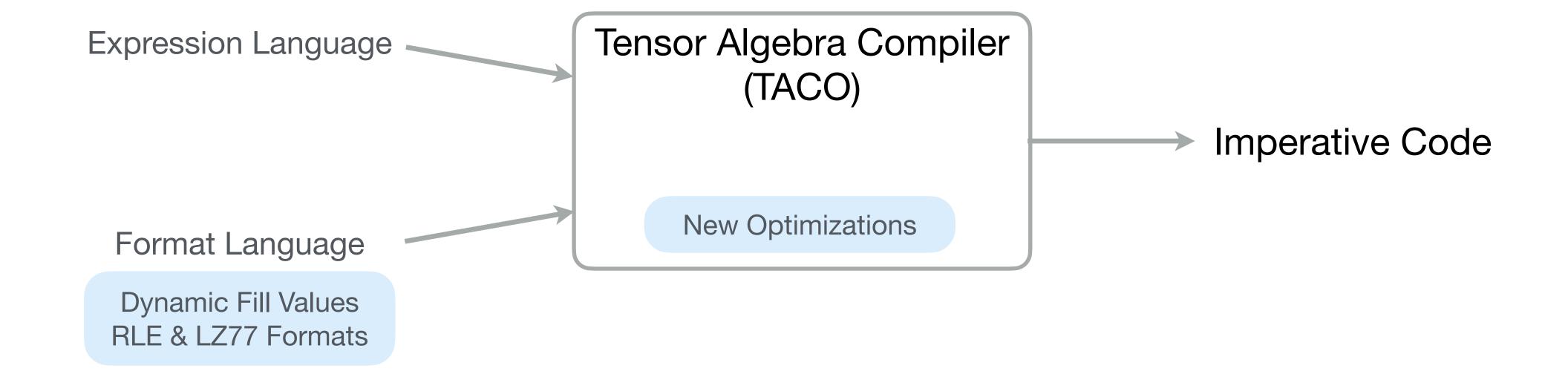




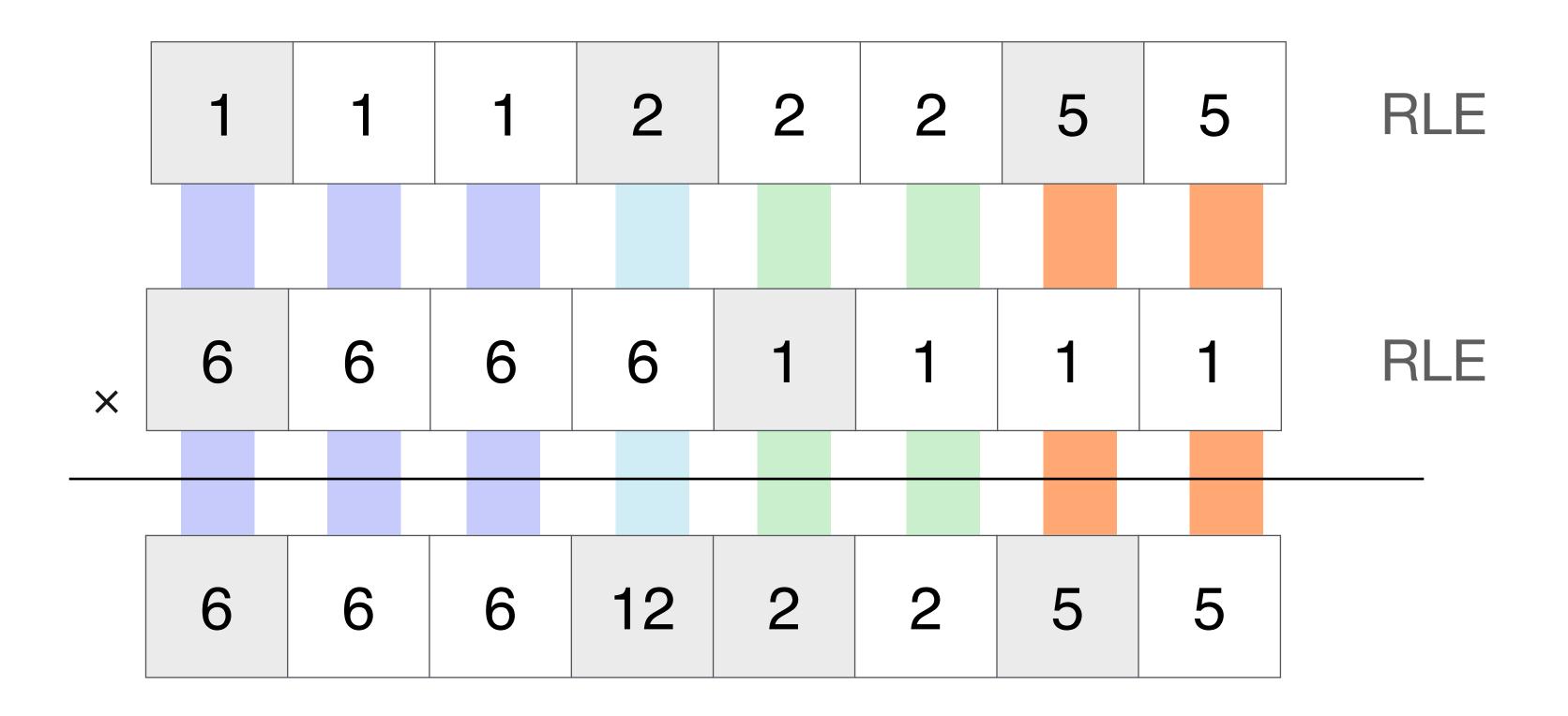
Run Length Encoding (RLE) Level Format

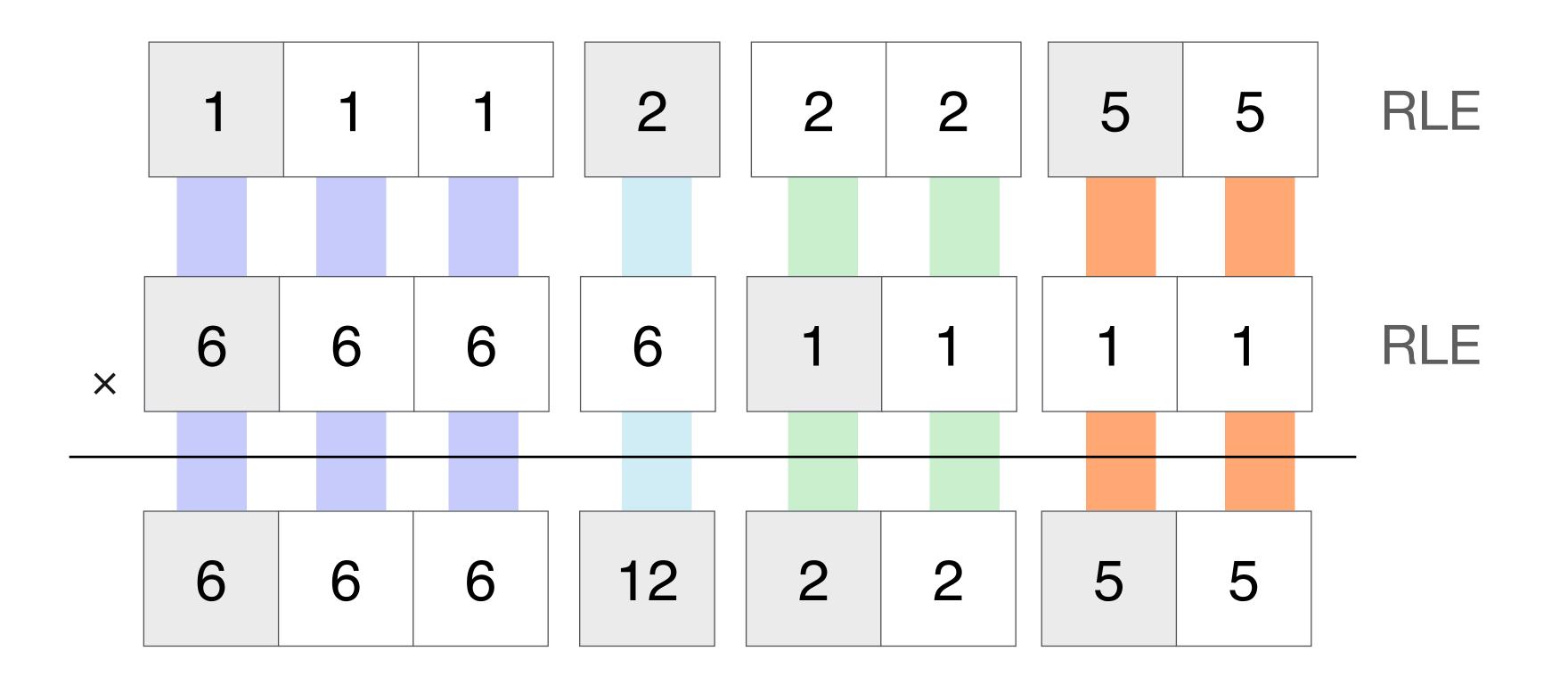


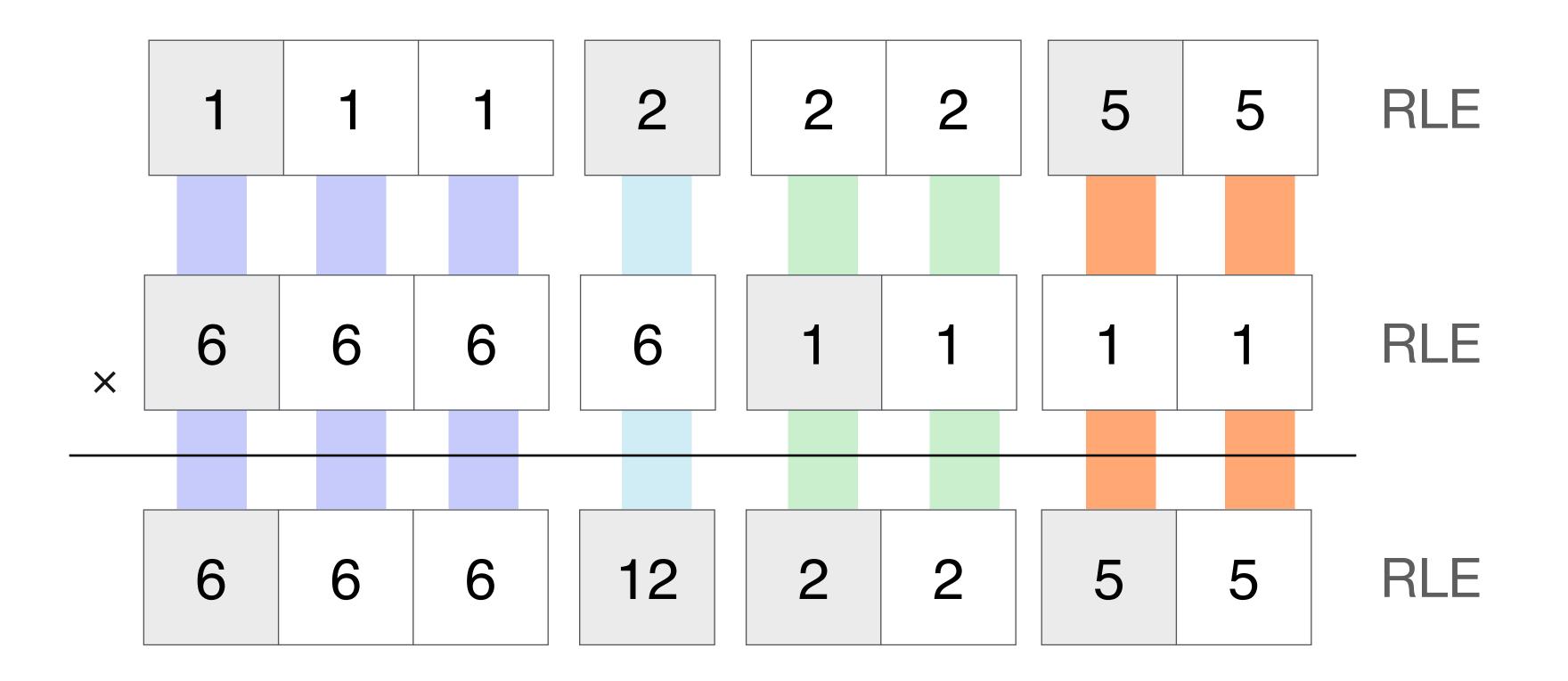


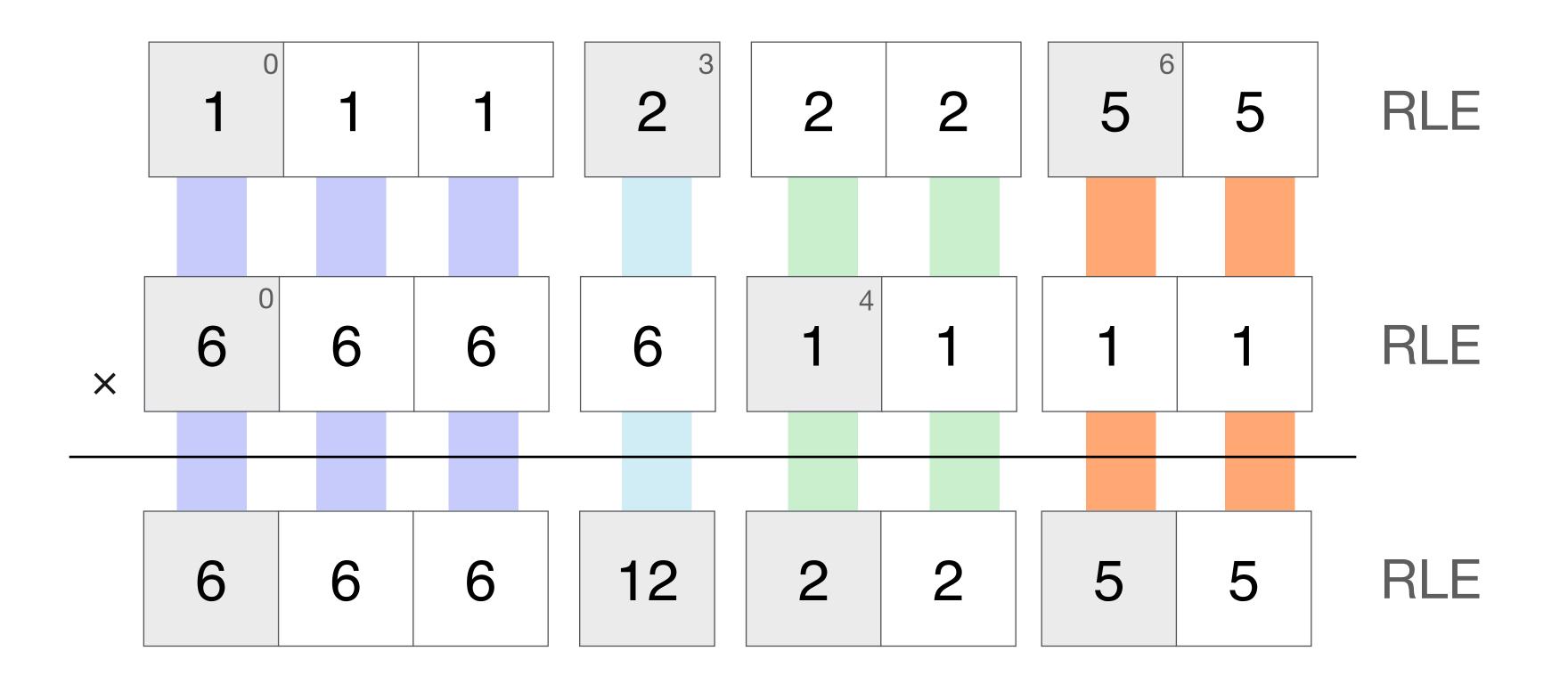


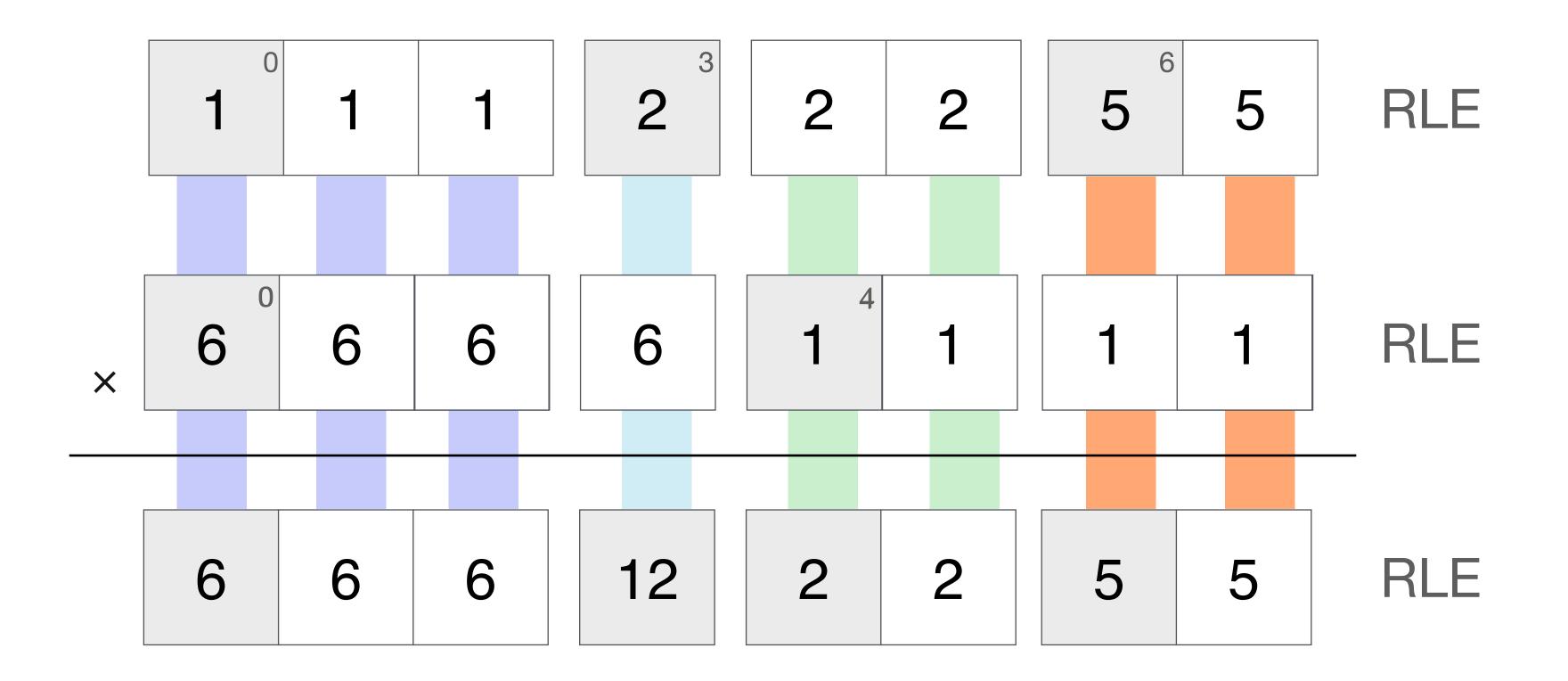
	1	1	1	2	2	2	5	5	RLE
×	6	6	6	6	1	1	1	1	RLE

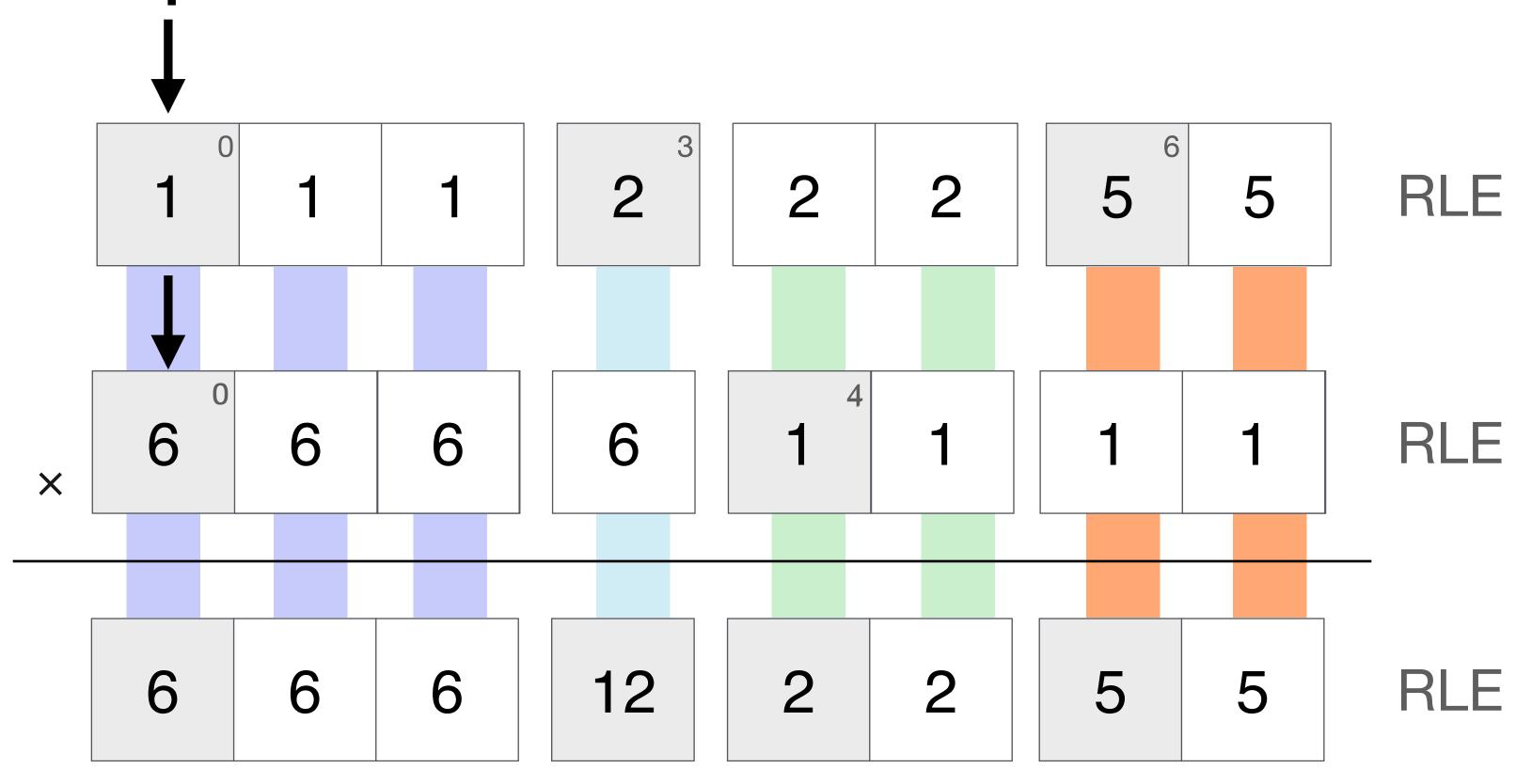


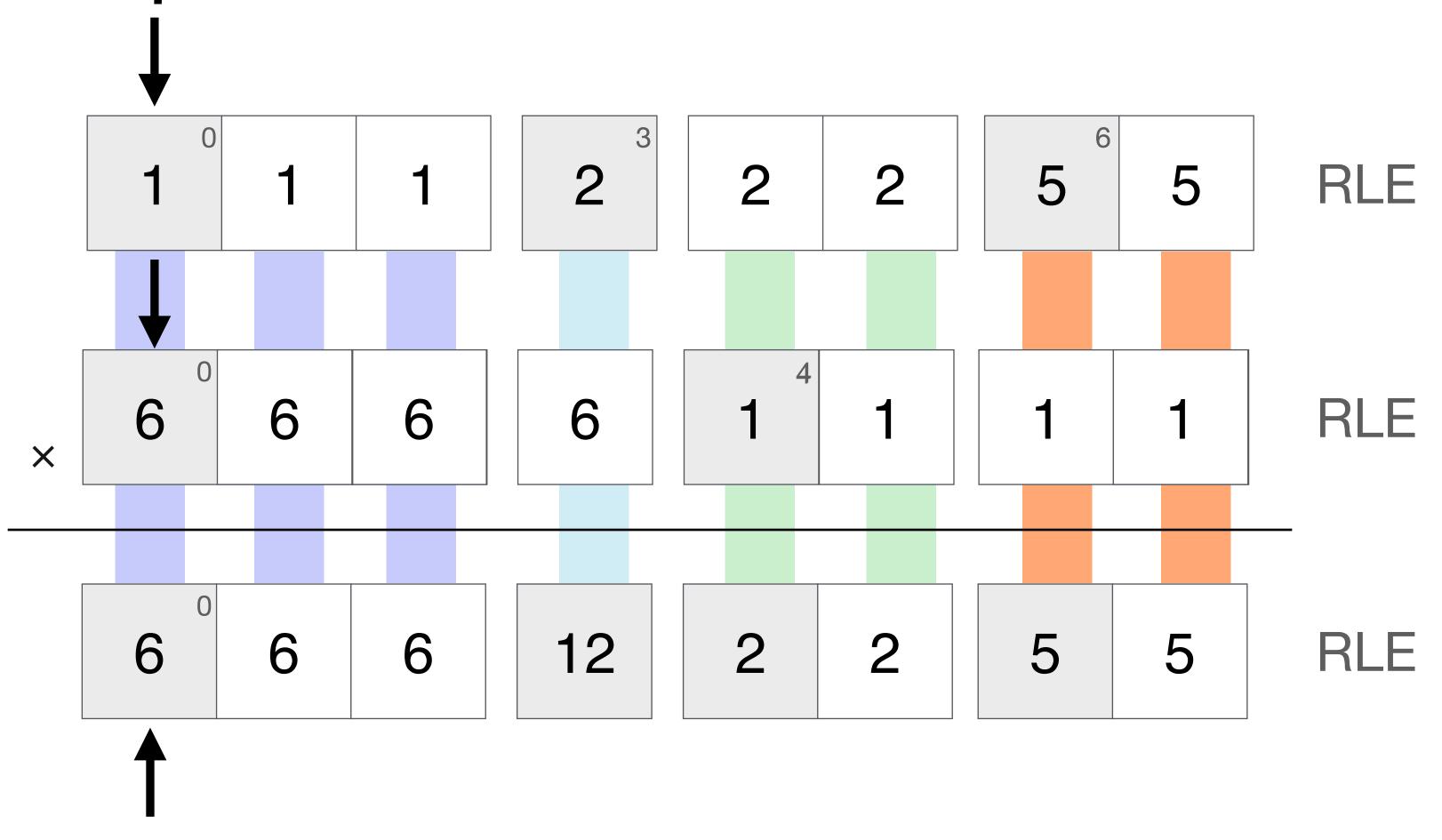


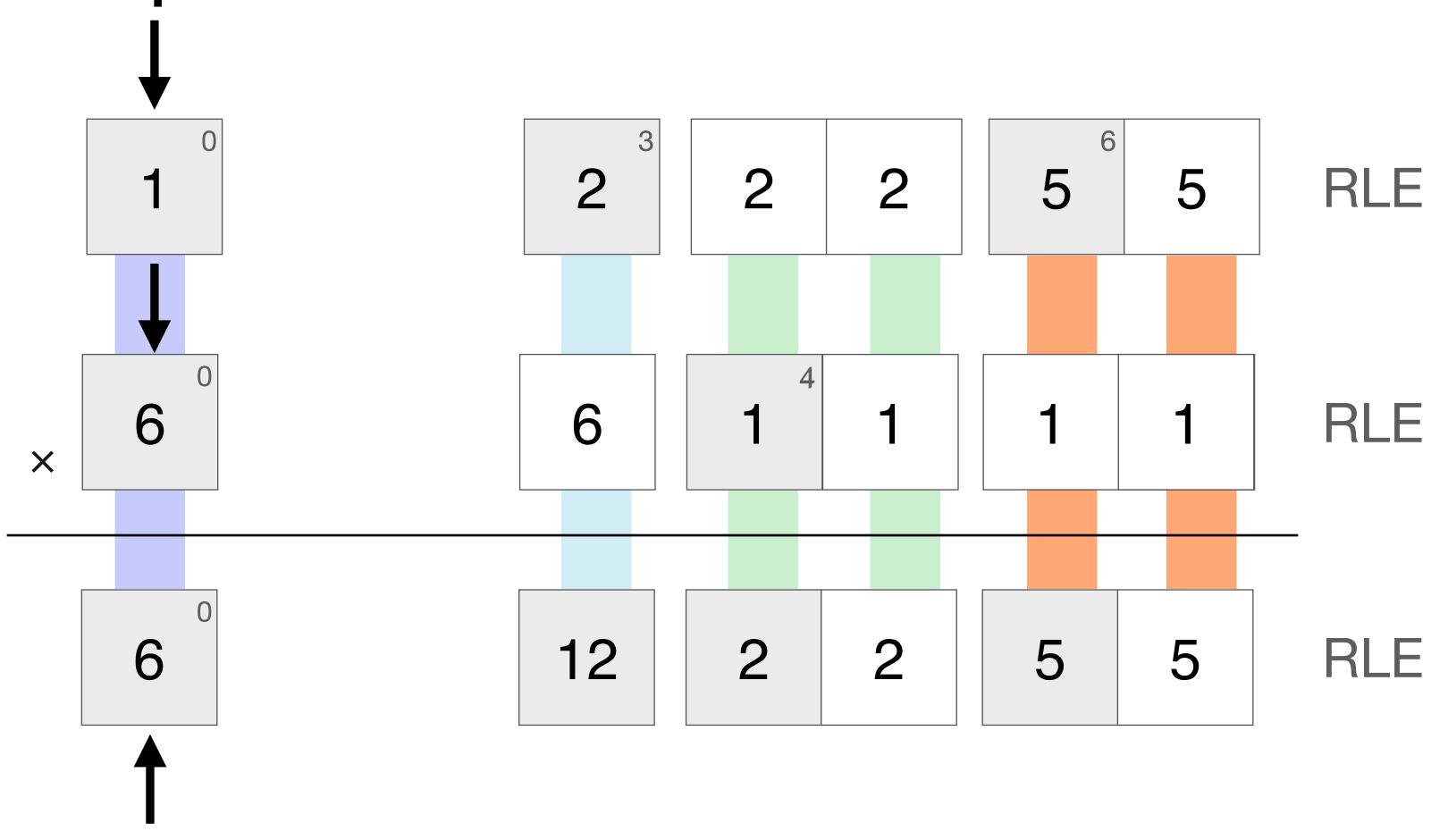


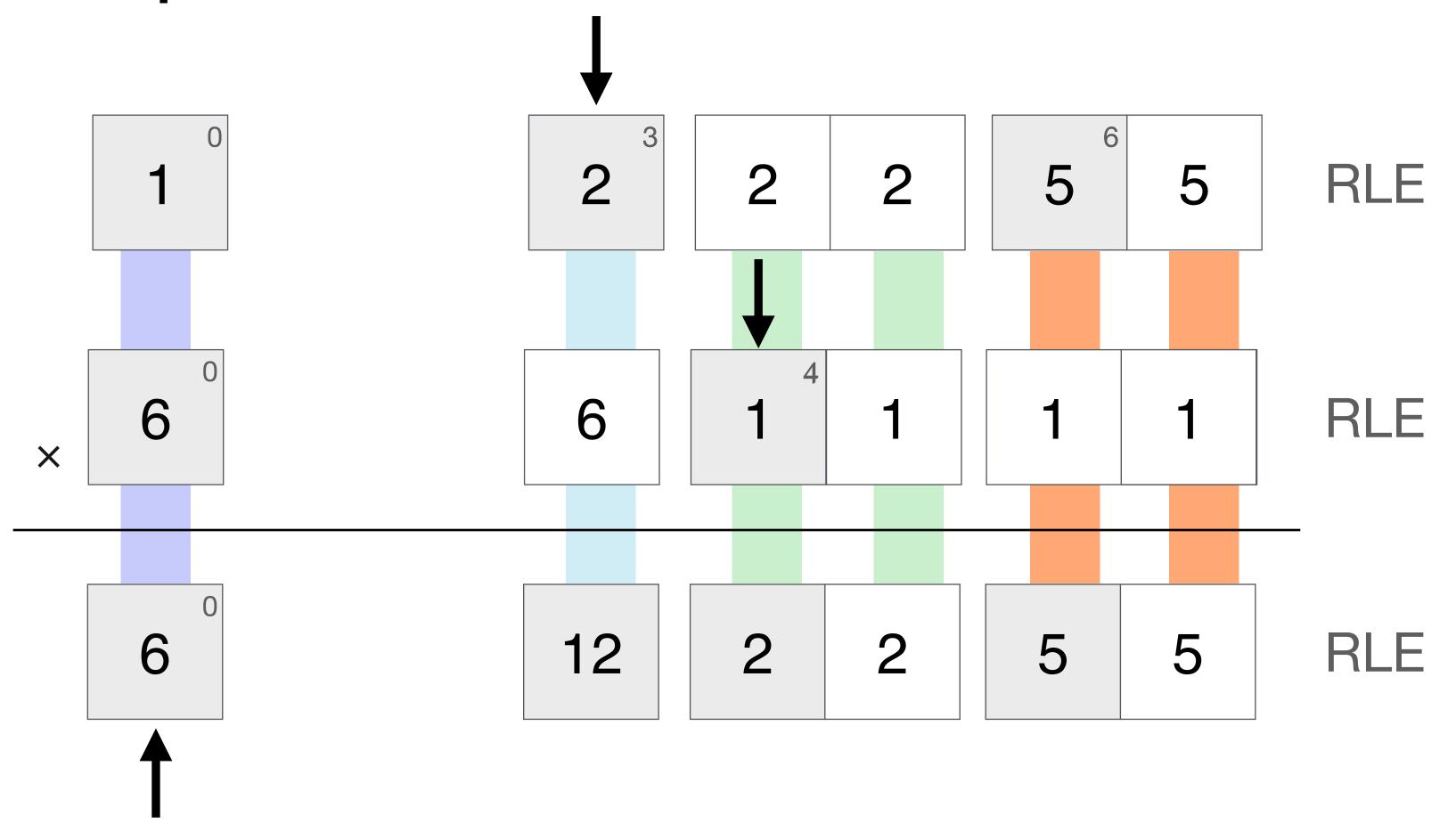


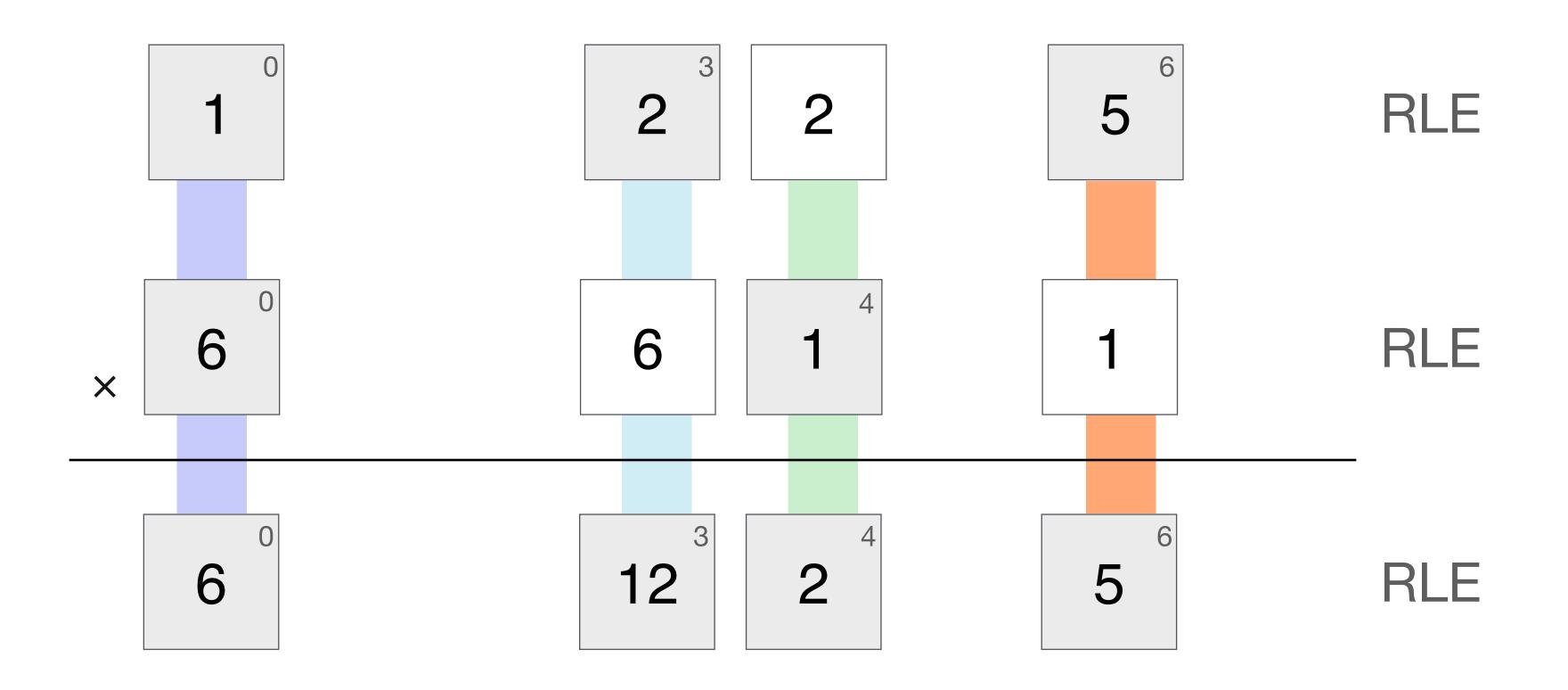


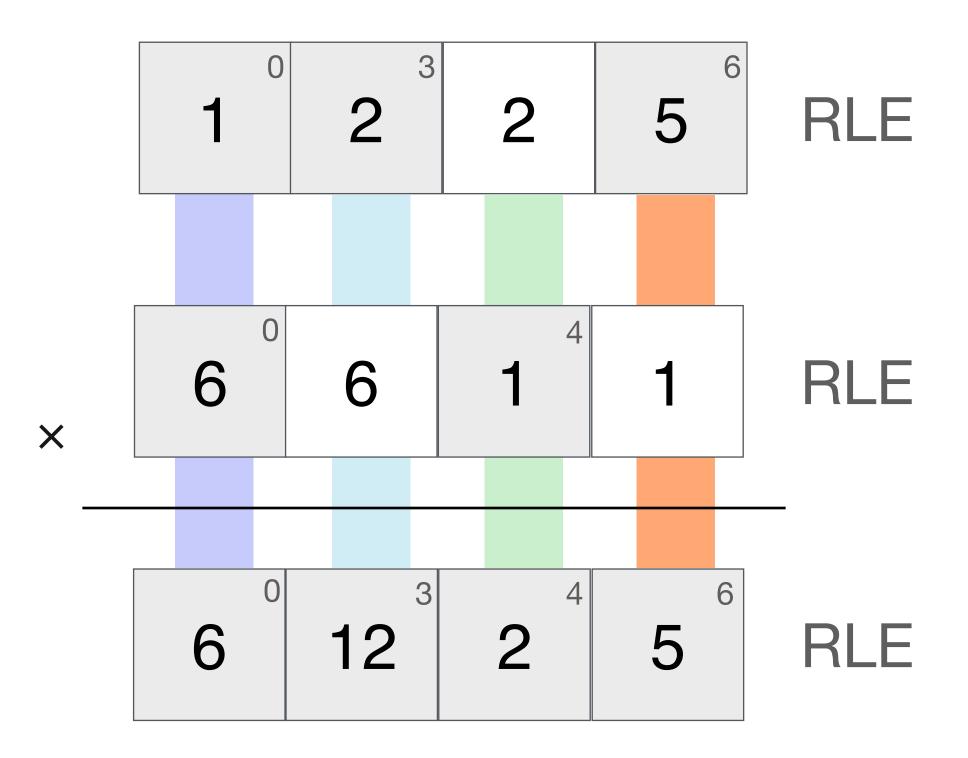


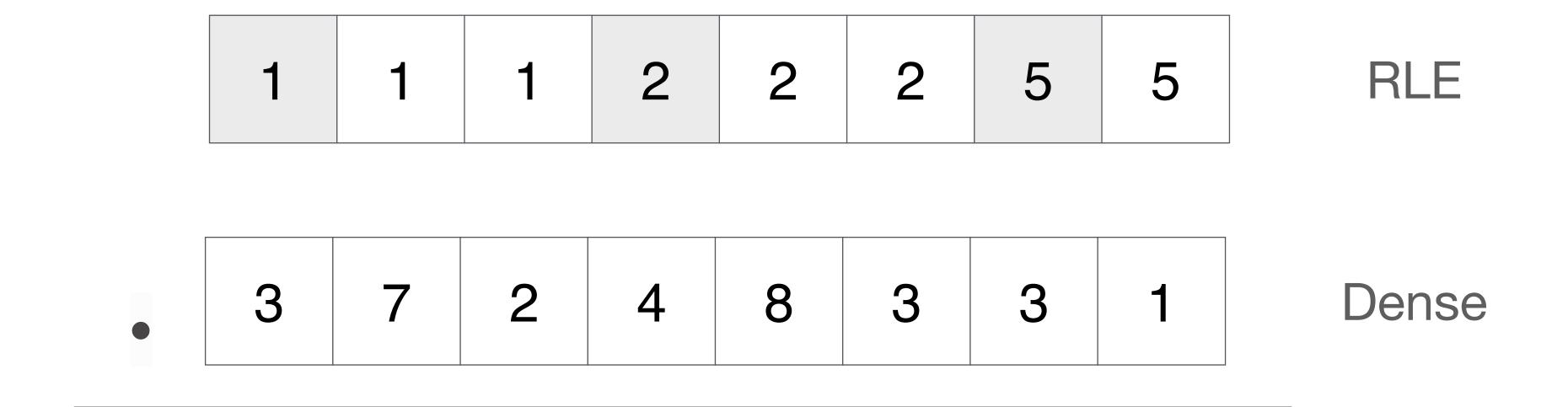




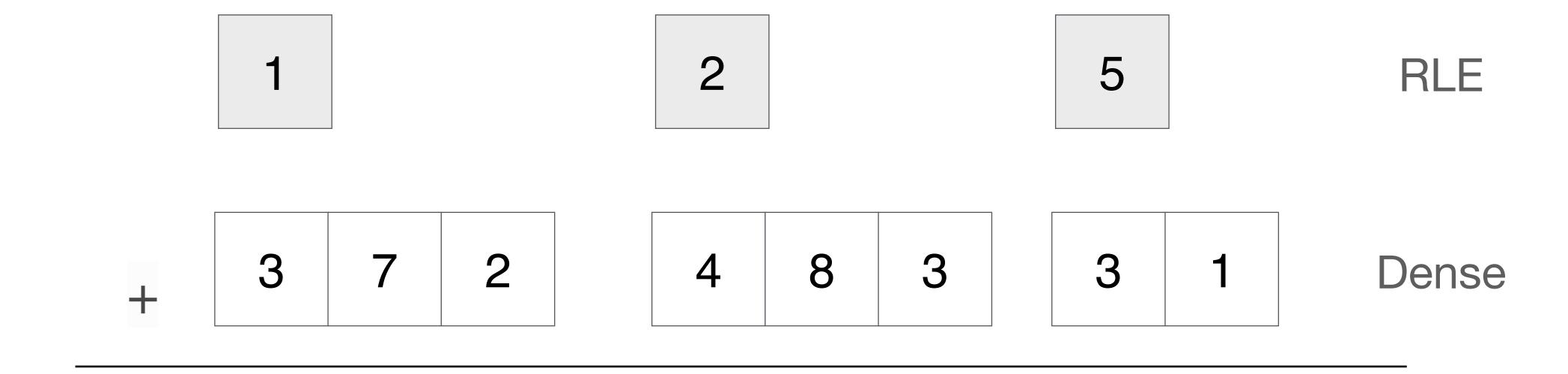




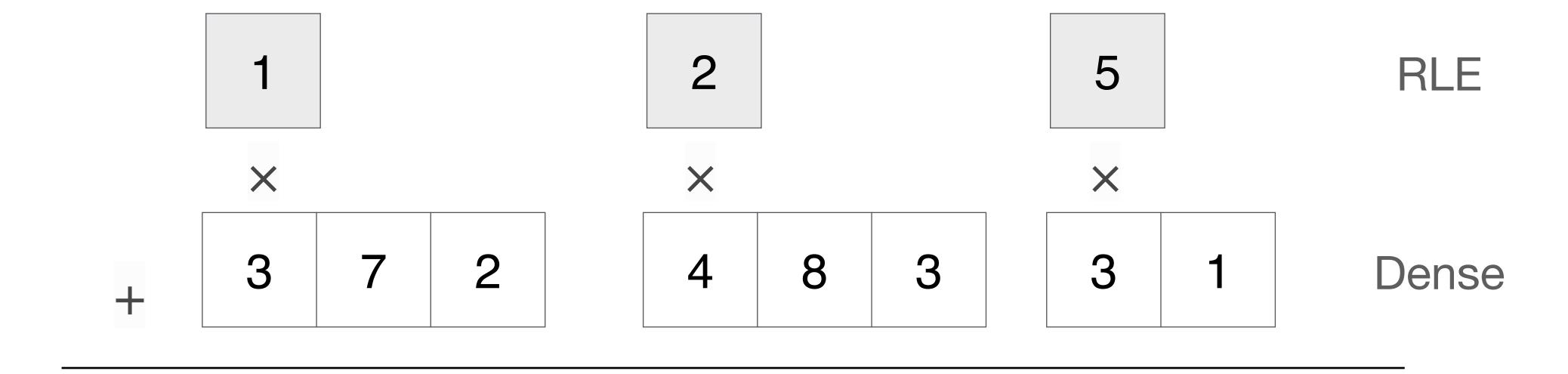


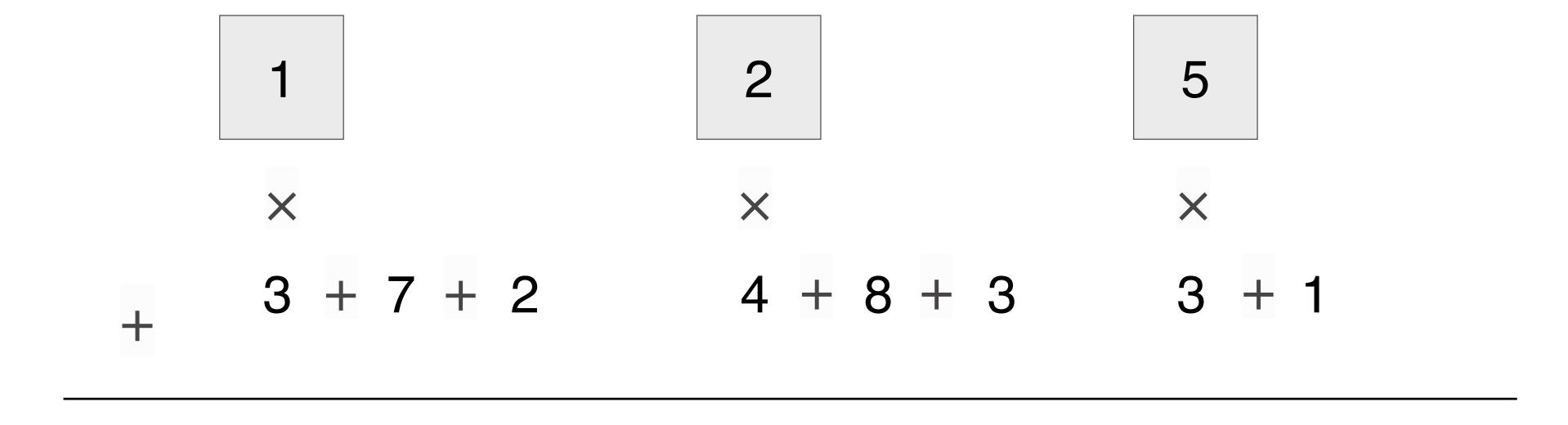


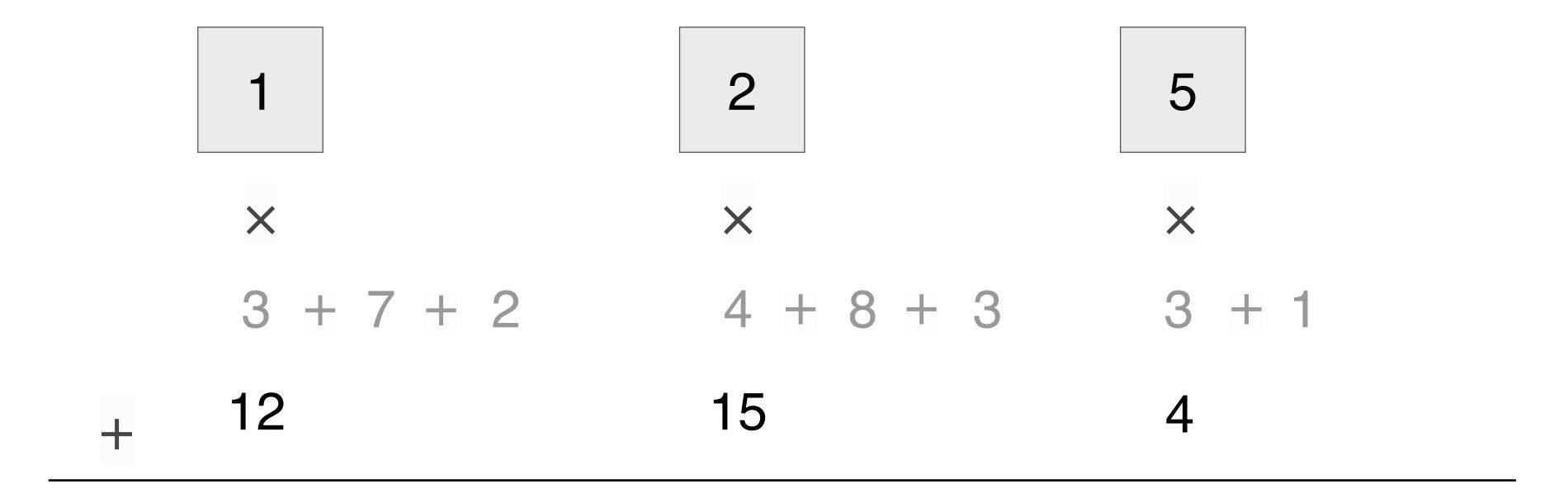
Reduction

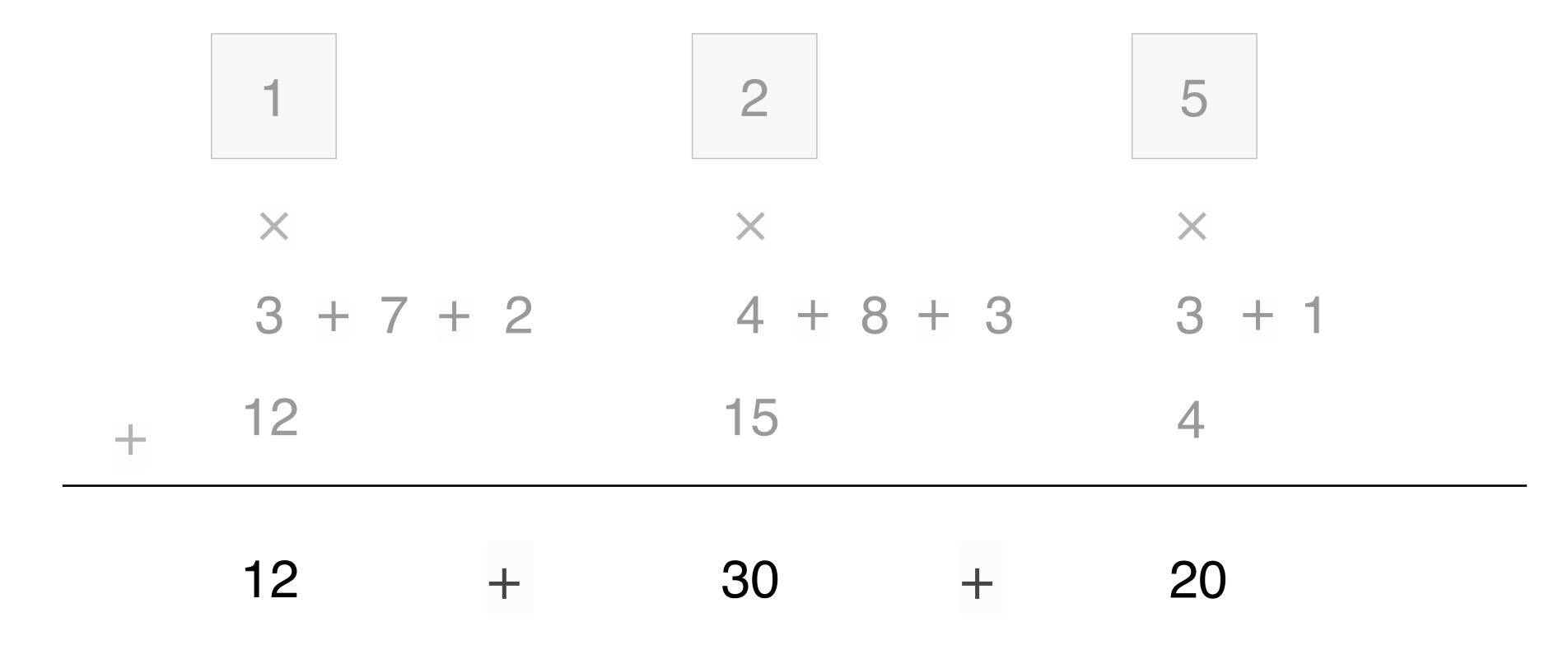


Reduction

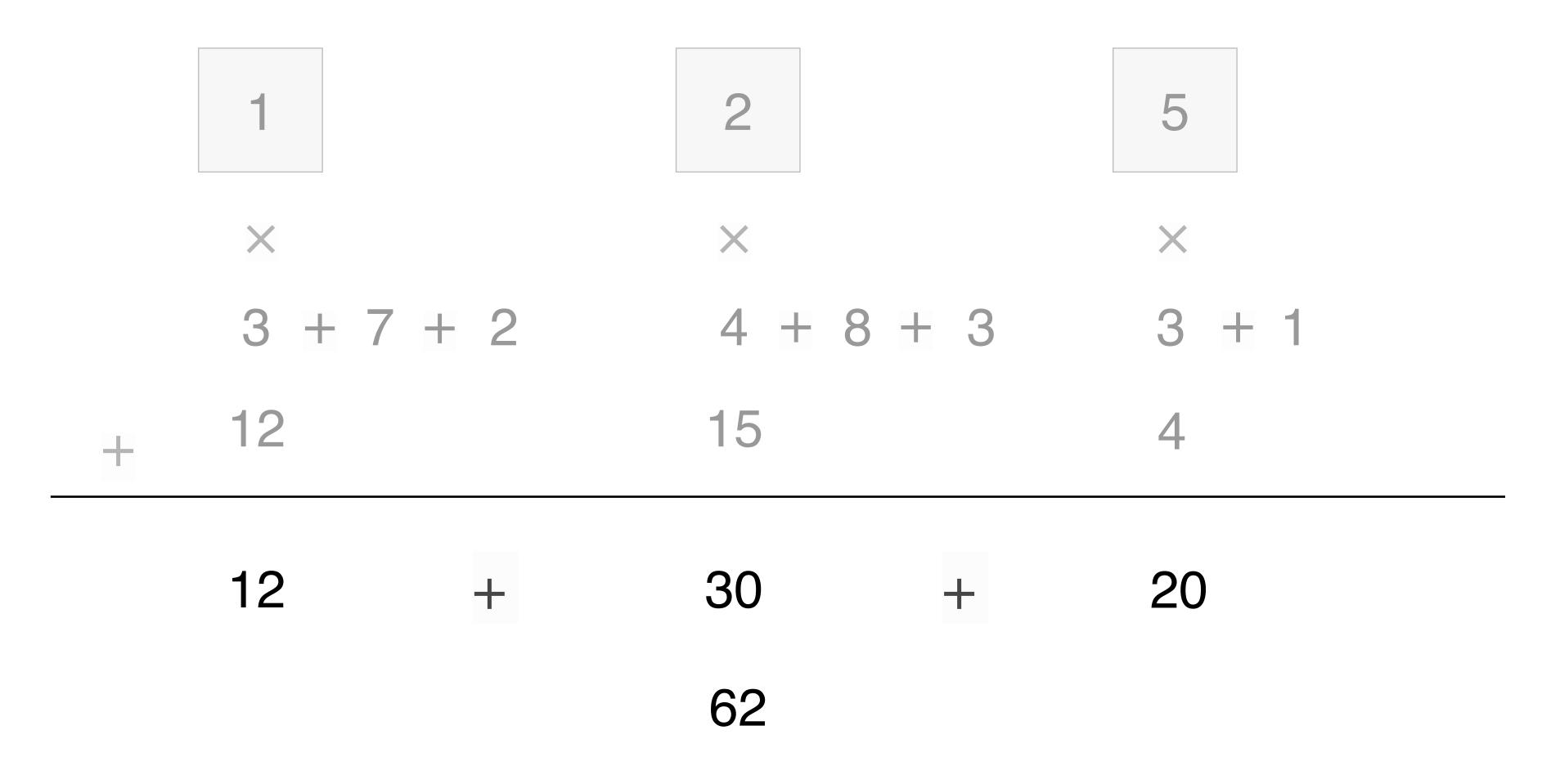








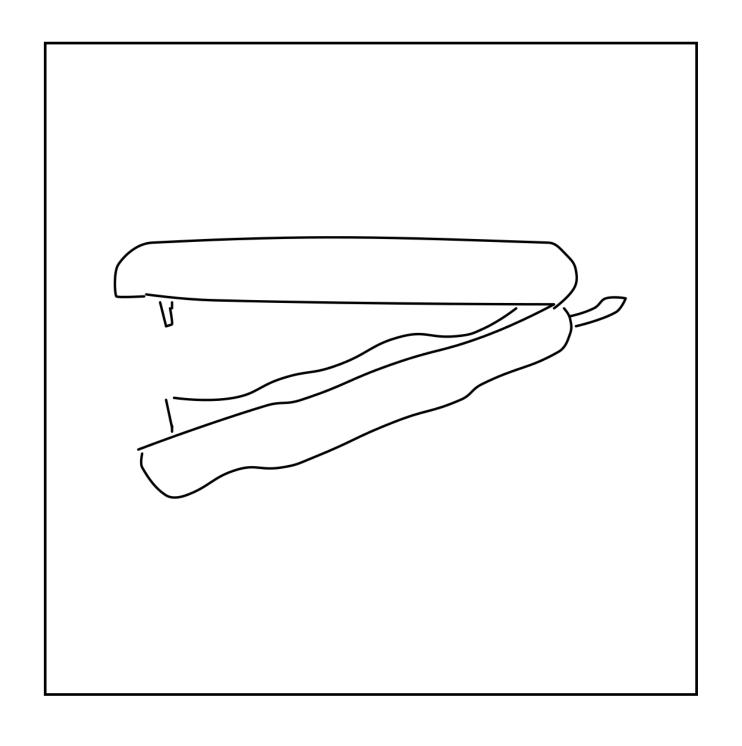
	1	2	5
	3 + 7 + 2	4 + 8 + 3	3 + 1
+	12	15	4
	12 +	30 +	20

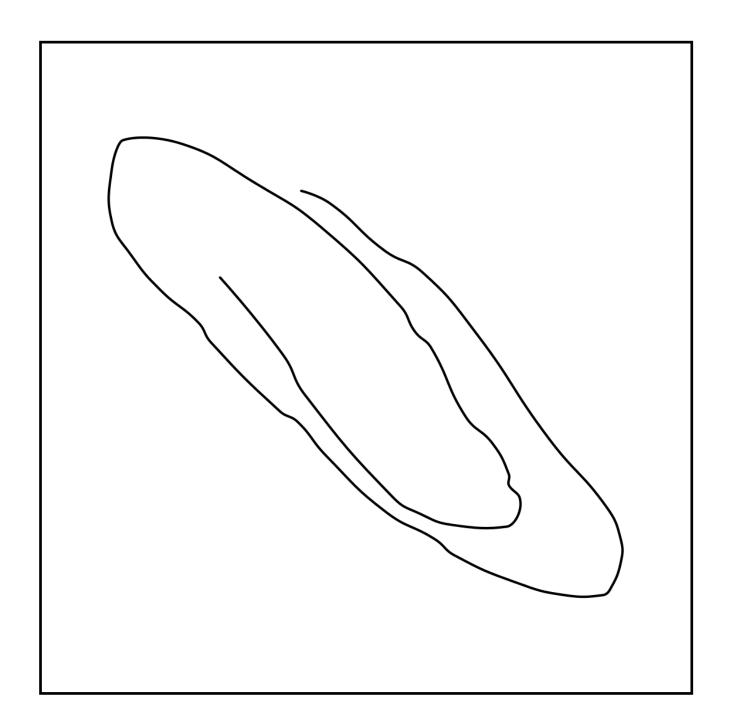


Code Generation

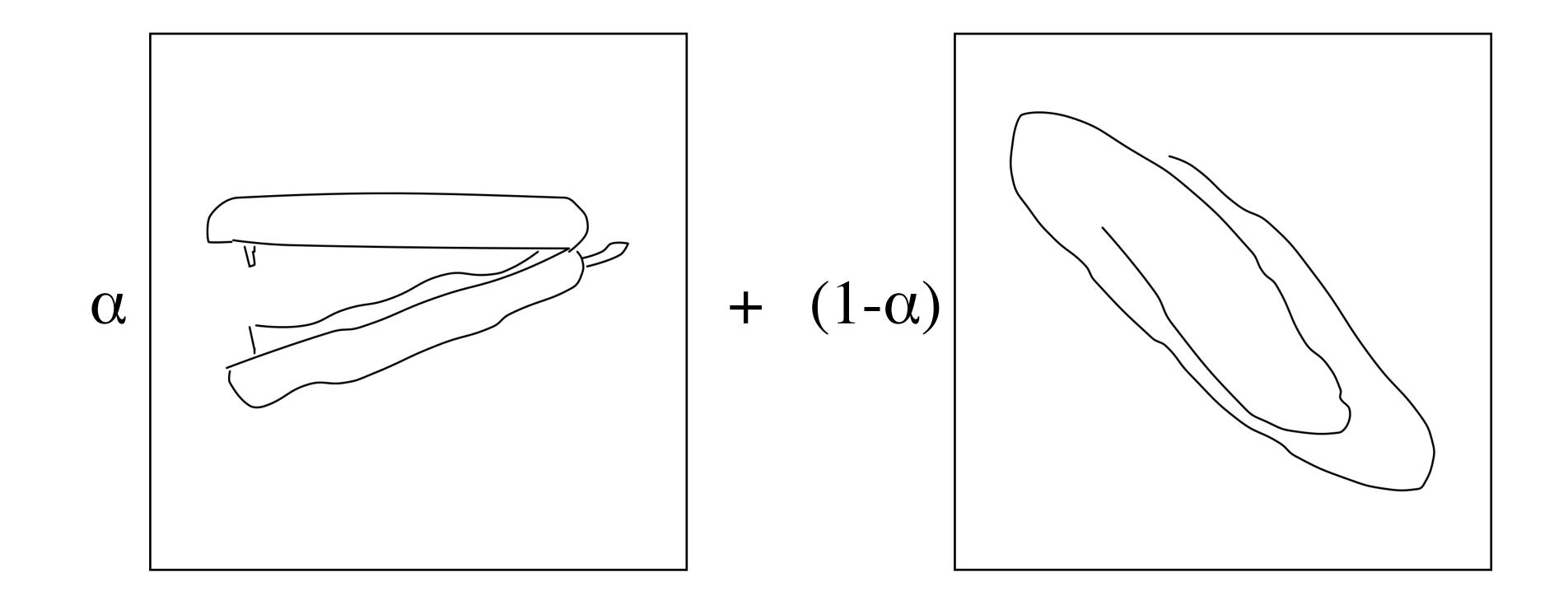
- Implemented as an extension to TACO
- Integrated support for our new abstractions into the format language
- Added compiler support for generating efficient code using our abstractions
- Added our new level formats

$$A_{ij} = \alpha B_{ij} + (1 - \alpha)C_{ij}$$

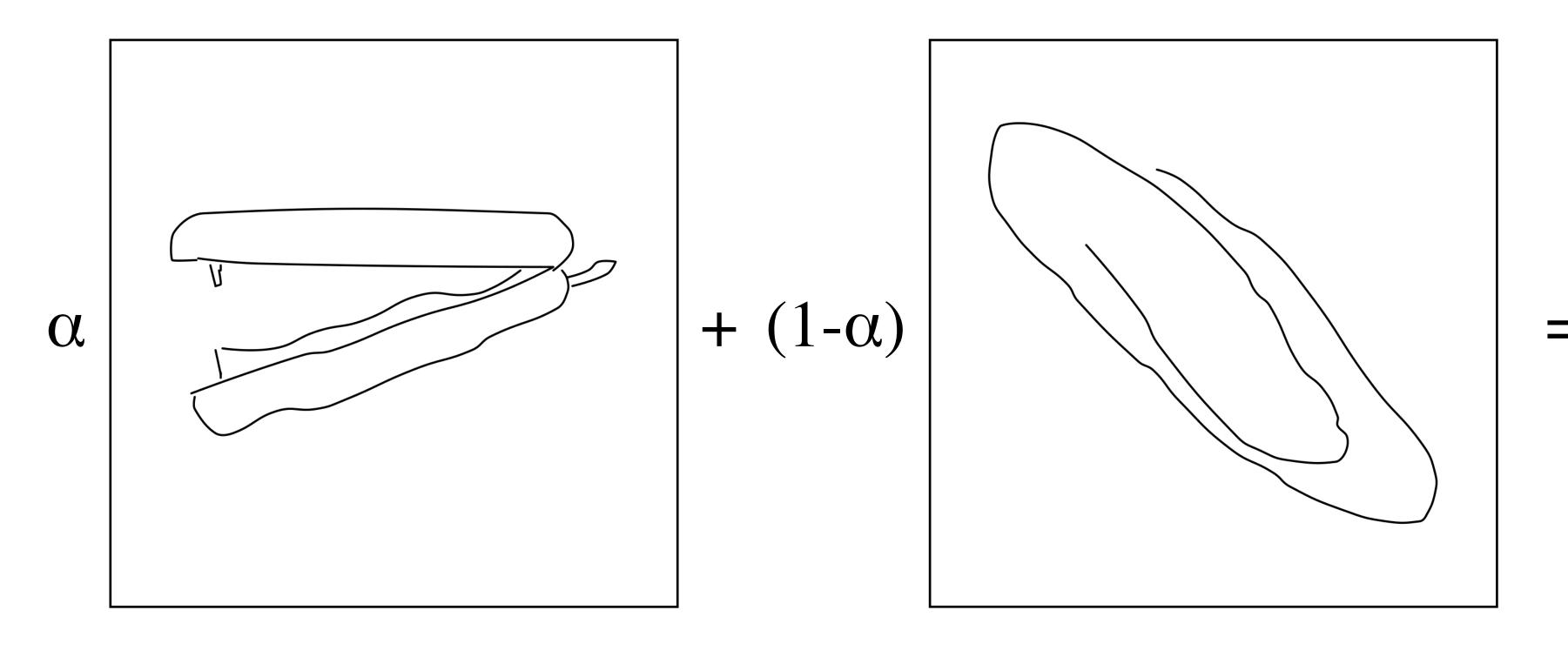


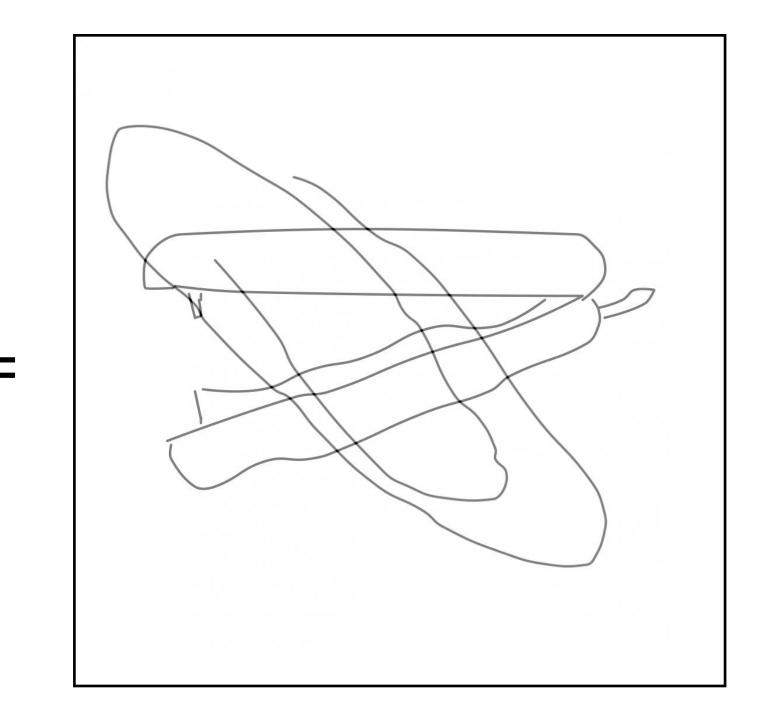


$$A_{ij} = \alpha B_{ij} + (1 - \alpha)C_{ij}$$

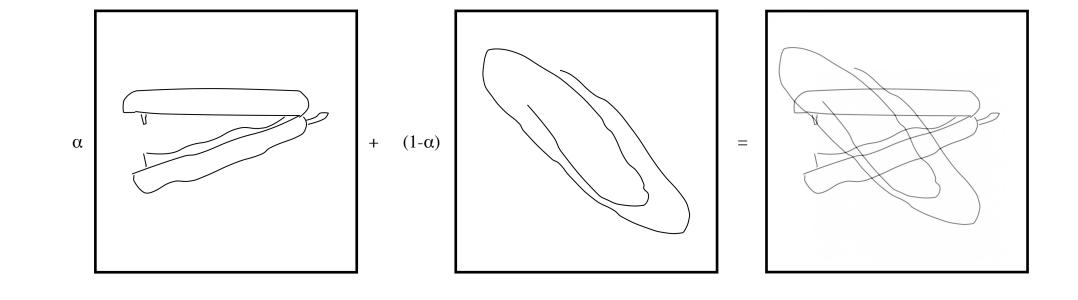


$$A_{ij} = \alpha B_{ij} + (1 - \alpha) C_{ij}$$

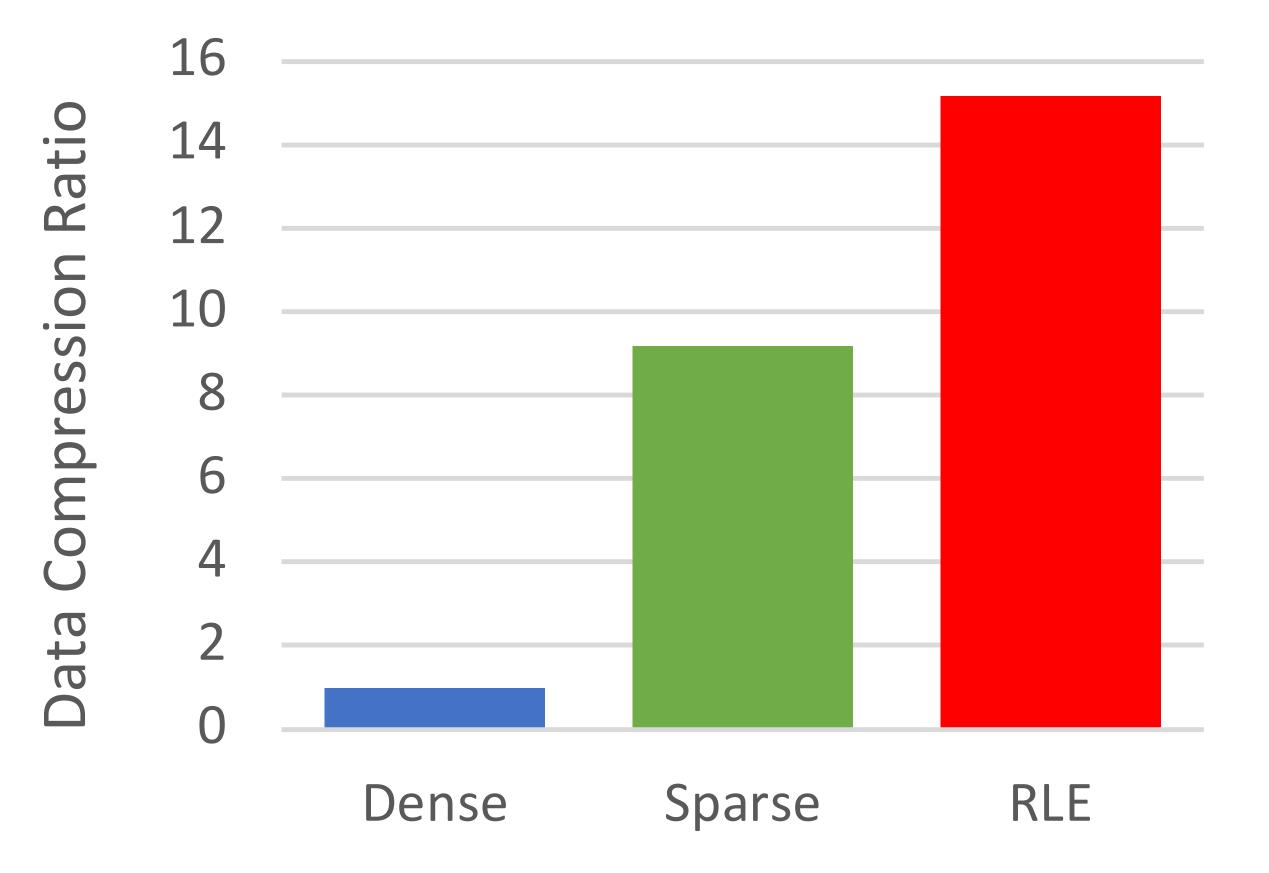


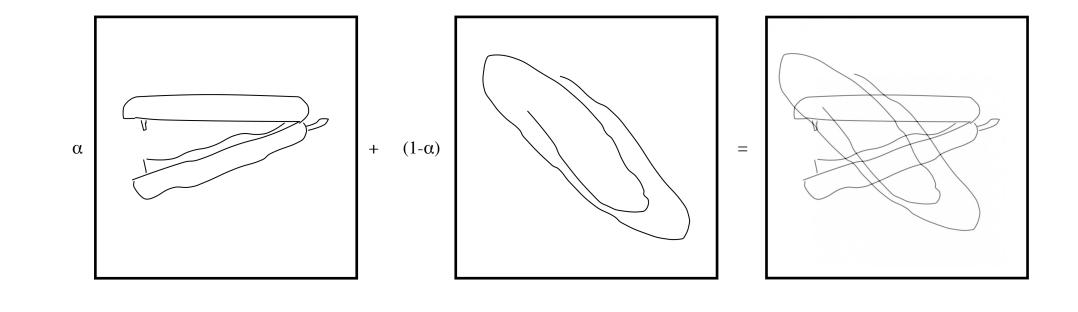


$$A_{ij} = \alpha B_{ij} + (1 - \alpha) C_{ij}$$

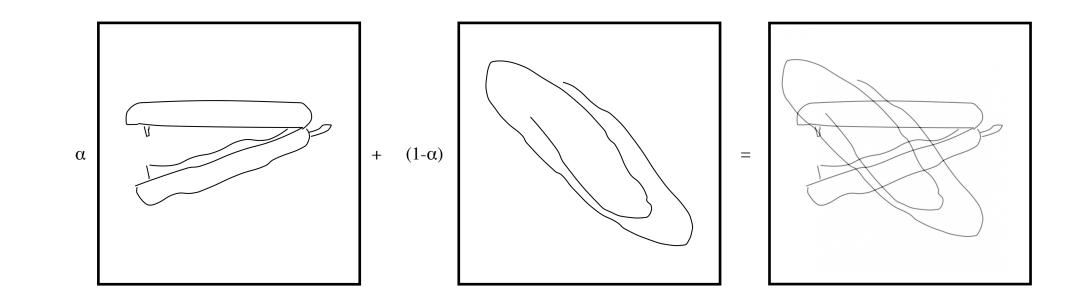


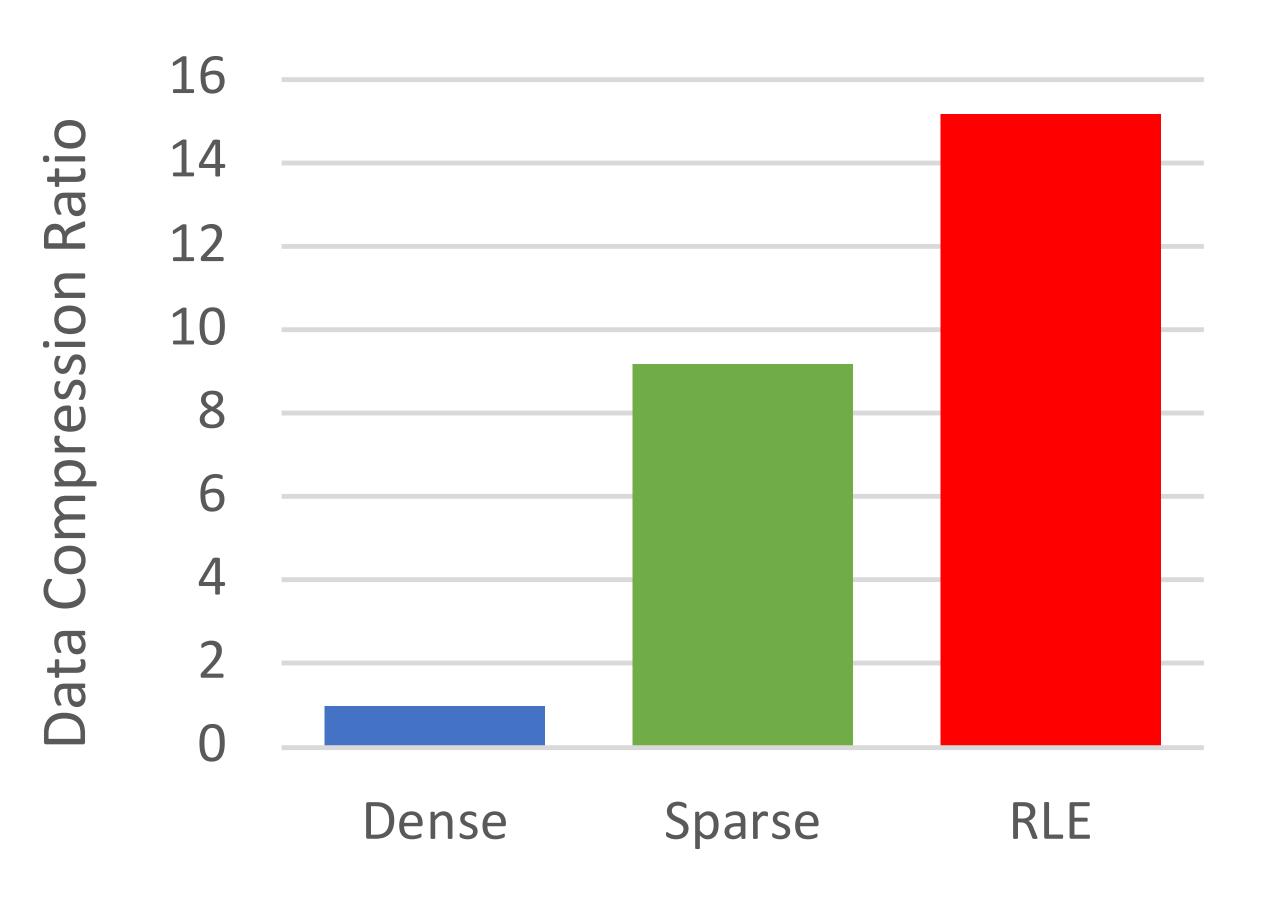
$$A_{ij} = \alpha B_{ij} + (1 - \alpha) C_{ij}$$

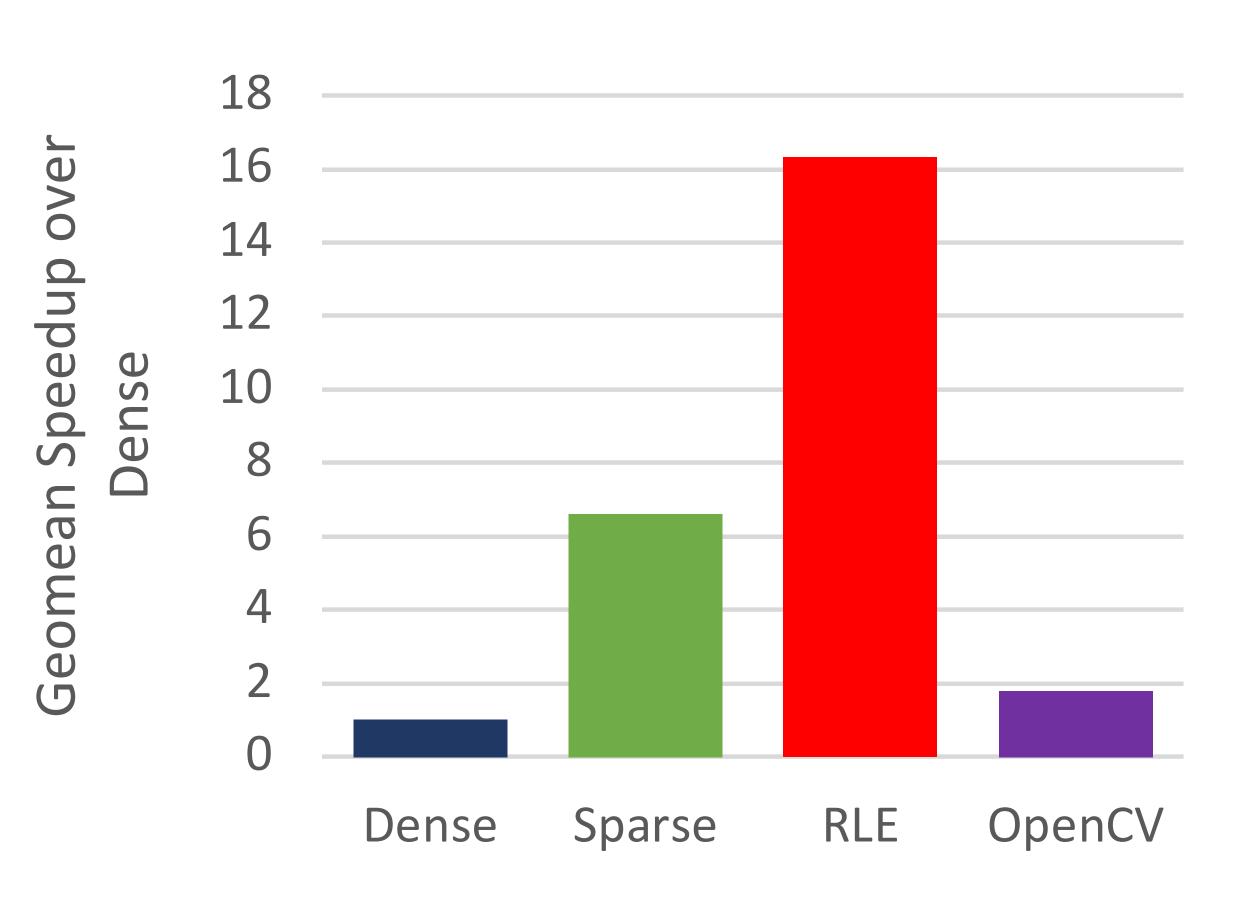




$$A_{ij} = \alpha B_{ij} + (1 - \alpha)C_{ij}$$

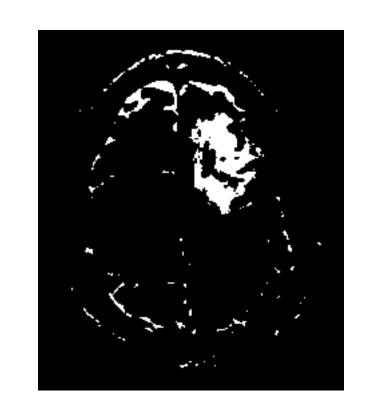




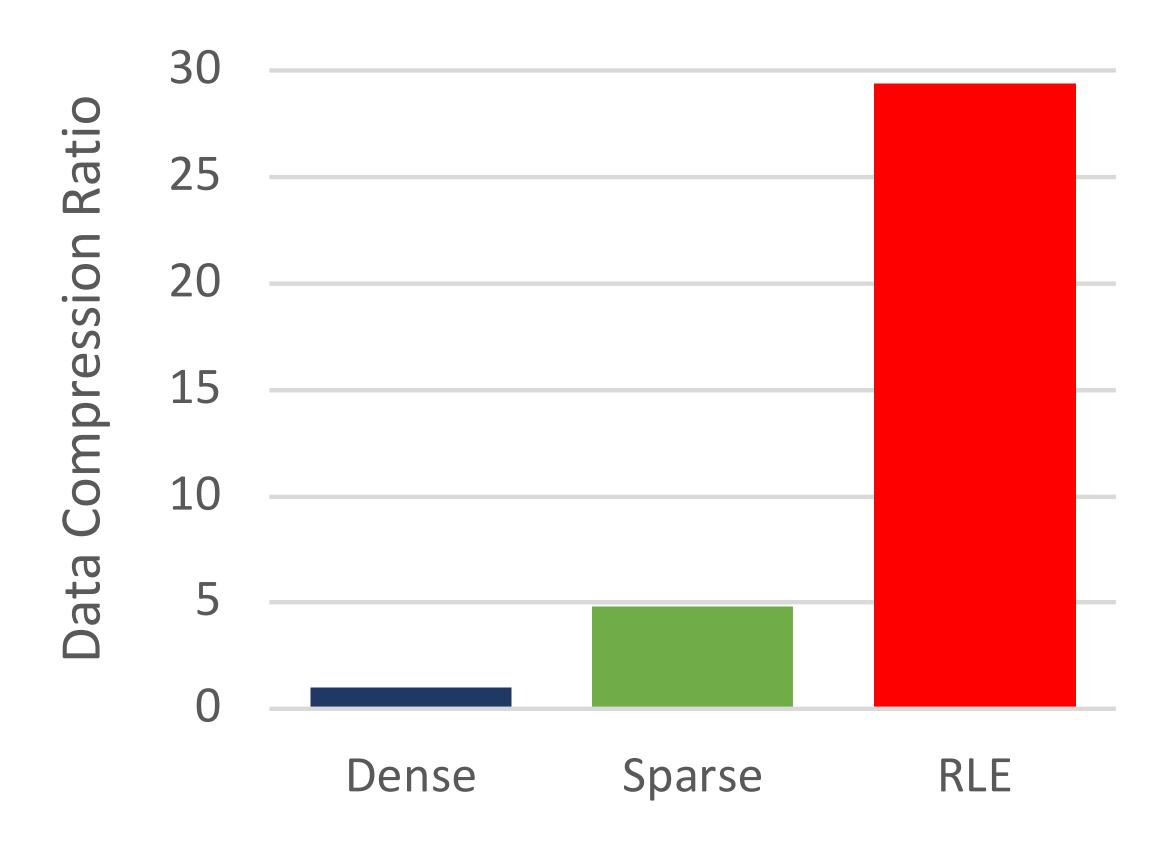


 $Out_{ij} = (A_{ij} \wedge ROI_{ij}) \oplus (B_{ij} \wedge ROI_{ij})$

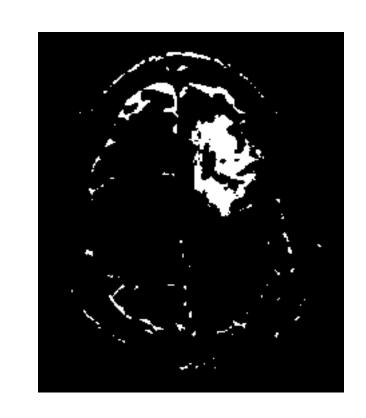




 $Out_{ij} = (A_{ij} \wedge ROI_{ij}) \oplus (B_{ij} \wedge ROI_{ij})$

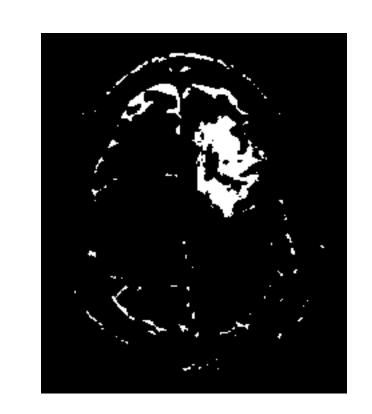


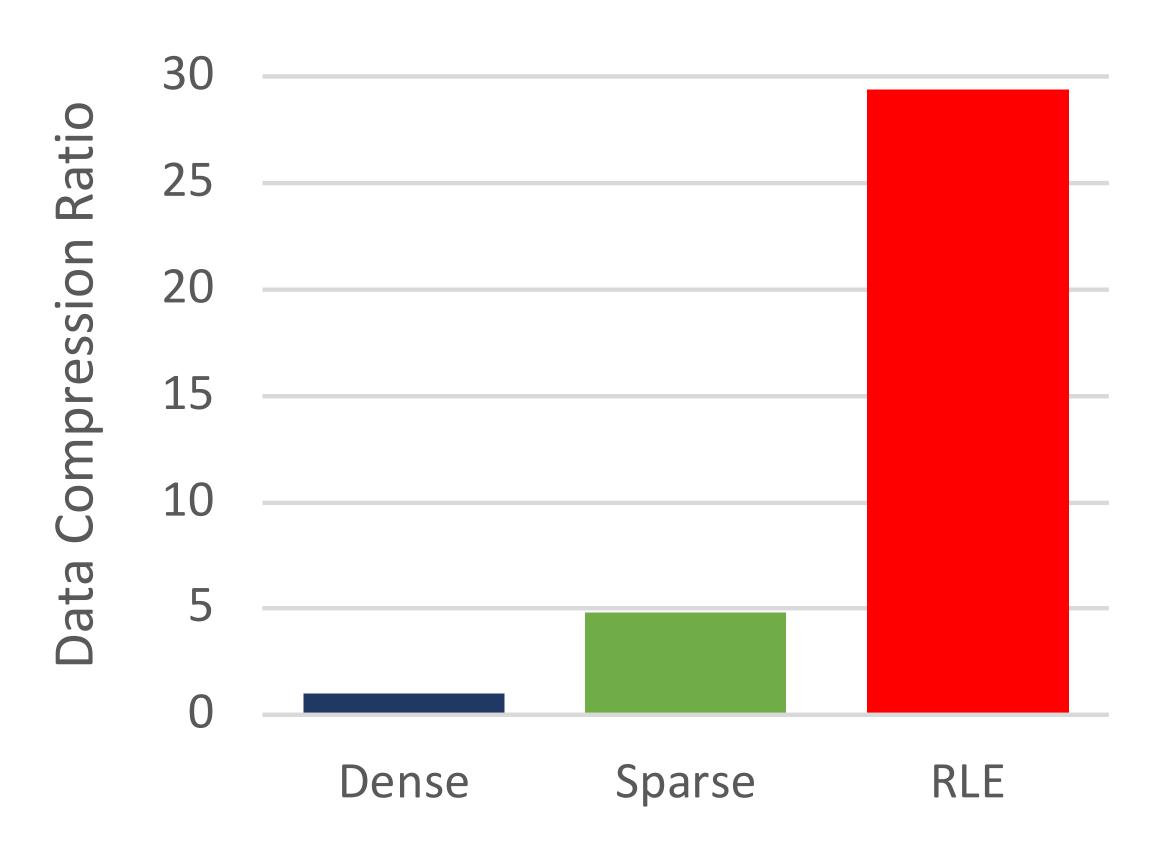


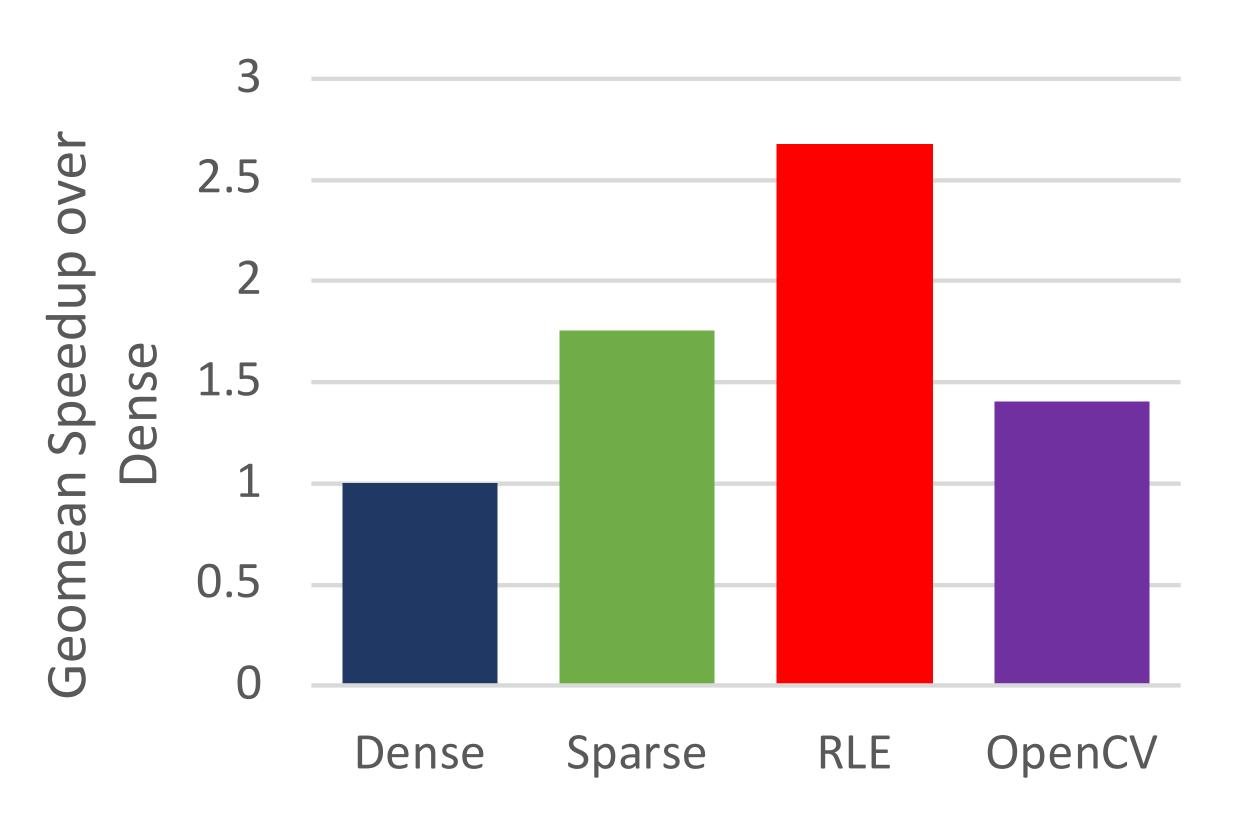


 $Out_{ij} = (A_{ij} \wedge ROI_{ij}) \oplus (B_{ij} \wedge ROI_{ij})$

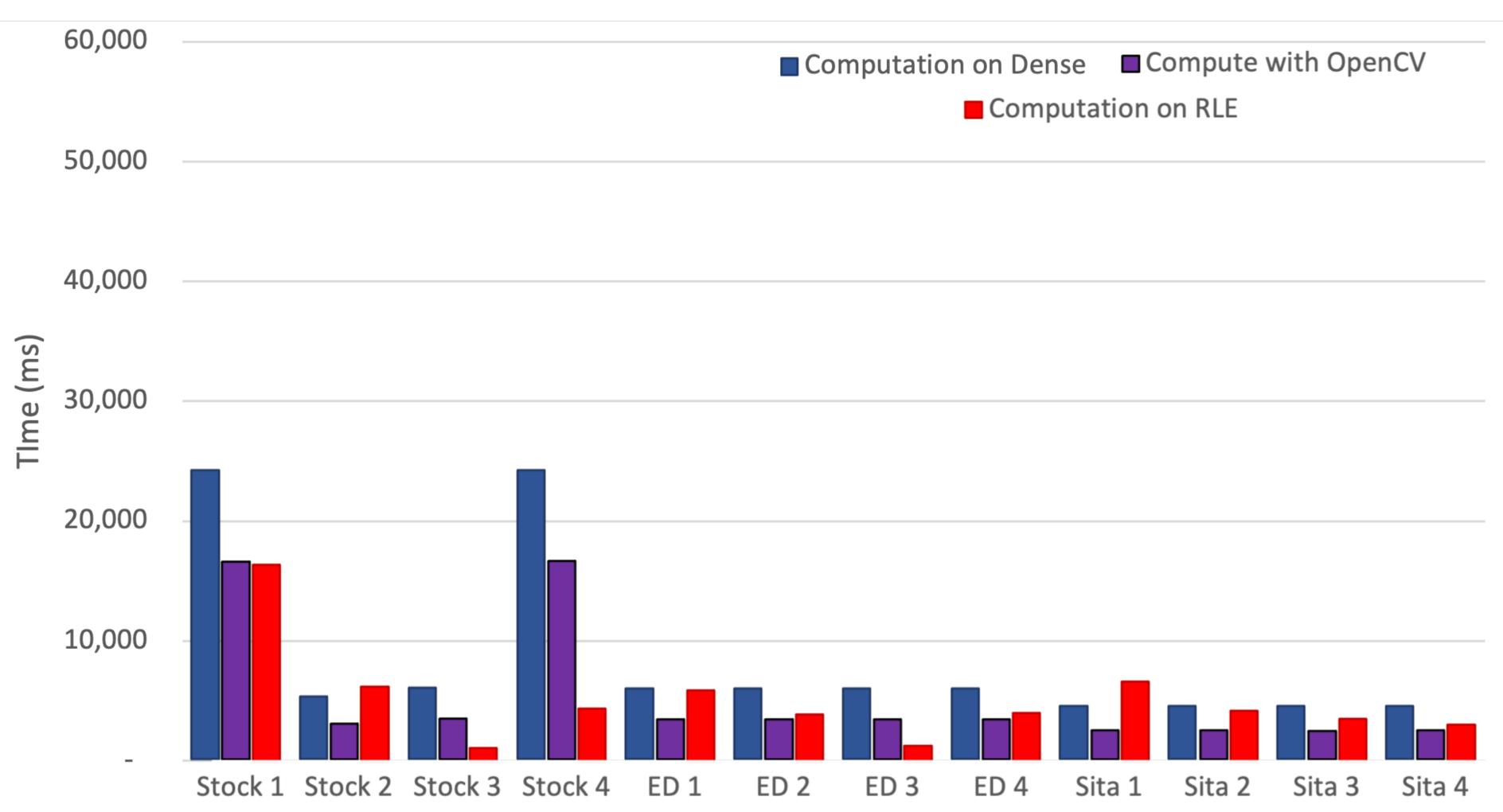




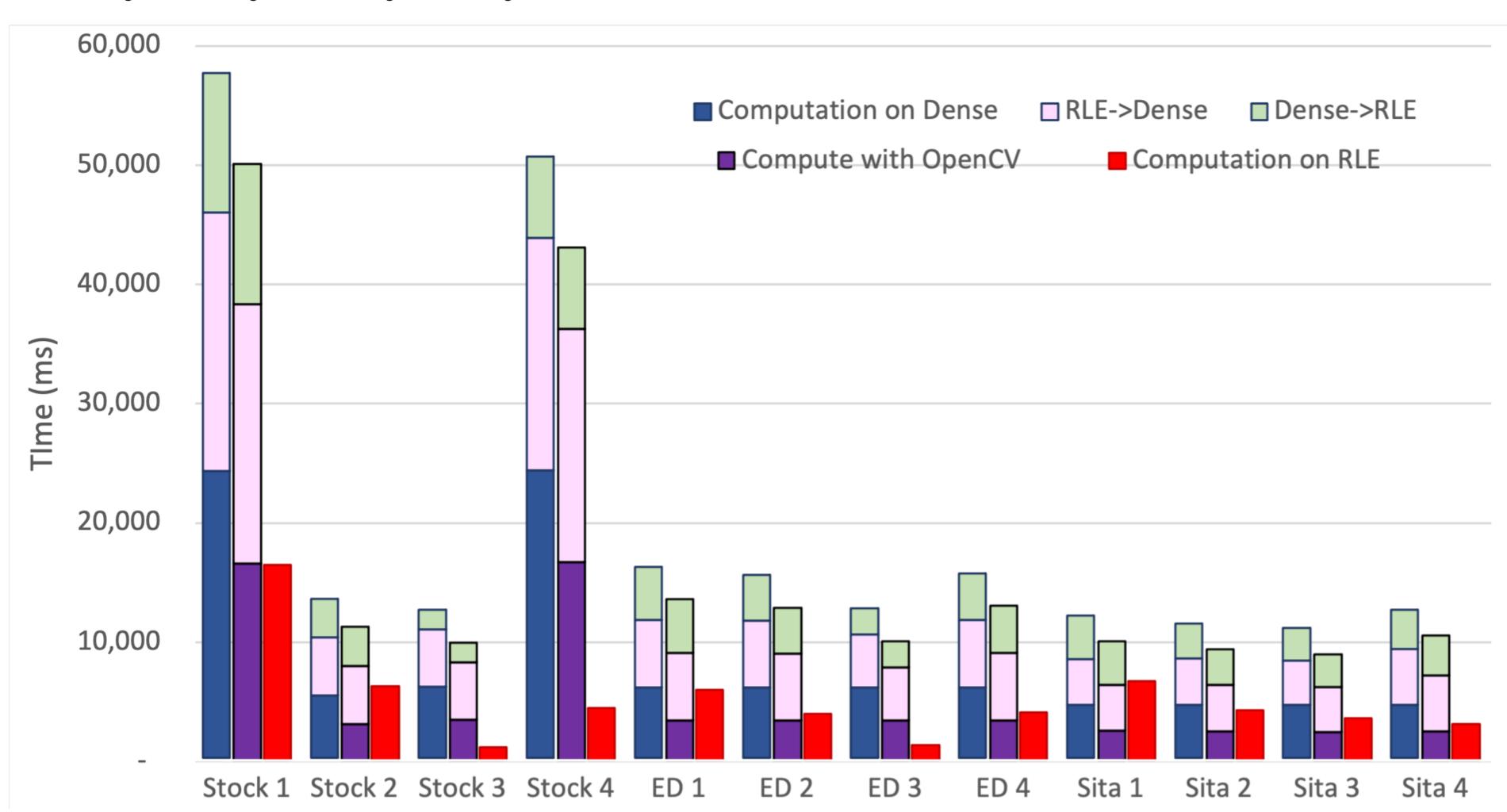




$$Out_{ijc} = (F_{ijc} * M_{ij}) + (S_{ij} * !M_{ij})$$



$$Out_{ijc} = (F_{ijc} * M_{ij}) + (S_{ij} * !M_{ij})$$



In conclusion

 We can automatically generate kernels that compute with both lossless compressed and sparse formats



http://tensor-compiler.org/















In conclusion

- We can automatically generate kernels that compute with both lossless compressed and sparse formats
- Our new abstractions enable implementation of many lossless compression formats



http://tensor-compiler.org/















In conclusion

- We can automatically generate kernels that compute with both lossless compressed and sparse formats
- Our new abstractions enable implementation of many lossless compression formats
- It is possible to get both the best compression and performance



http://tensor-compiler.org/











